

# Multidimensional Tensor Library

primary use in perturbation methods for Stochastic Dynamic  
General Equilibrium (SDGE) models

### 1. Library overview.

The design of the library was driven by the needs of perturbation methods for solving Stochastic Dynamic General Equilibrium models. The aim of the library is not to provide an exhaustive interface to multidimensional linear algebra. The tensor library's main purposes include:

- Define types for tensors, for a multidimensional index of a tensor, and types for folded and unfolded tensors. The tensors defined here have only one multidimensional index and one reserved one-dimensional index. The tensors should allow modelling of higher order derivatives with respect to a few vectors with different sizes (for example  $[g_{y^2u^3}]$ ). The tensors should allow folded and unfolded storage modes and conversion between them. A folded tensor stores symmetric elements only once, while an unfolded stores data as a whole multidimensional cube.
- Define both sparse and dense tensors. We need only one particular type of sparse tensor. This in contrast to dense tensors, where we need much wider family of types.
- Implement the Faa Di Bruno multidimensional formula. So, the main purpose of the library is to implement the following step of Faa Di Bruno:

$$[B_{s^k}]_{\alpha_1 \dots \alpha_k} = [h_{y^l}]_{\gamma_1 \dots \gamma_l} \left( \sum_{c \in M_{l,k}} \prod_{m=1}^l [g_{c_m}]_{c_m(\alpha)}^{\gamma_m} \right)$$

where  $s$  can be a compound vector of variables,  $M_{l,k}$  is a set of all equivalences of  $k$  element set having  $l$  classes,  $c_m$  is  $m$ -th class of equivalence  $c$ , and  $c_m(\alpha)$  is a tuple of picked indices from  $\alpha$  by class  $c_m$ .

Note that the sparse tensors play a role of  $h$  in the Faa Di Bruno, not of  $B$  nor  $g$ .

The following table is a road-map to various abstractions in the library.

Class defined in	Purpose
<code>&lt;Tensor&gt;</code> class declaration <a href="#">214</a> <code>tensor.hweb</code>	Virtual base class for all dense tensors, defines <i>index</i> as the multidimensional iterator
<code>&lt;FTensor&gt;</code> class declaration <a href="#">216</a> <code>tensor.hweb</code>	Virtual base class for all folded tensors
<code>&lt;UTensor&gt;</code> class declaration <a href="#">215</a> <code>tensor.hweb</code>	Virtual base class for all unfolded tensors
<code>&lt;FFSTensor&gt;</code> class declaration <a href="#">231</a> <code>fs_tensor.hweb</code>	Class representing folded full symmetry dense tensor, for instance $[g_{y^3}]$
<code>&lt;FGSTensor&gt;</code> class declaration <a href="#">257</a> <code>gs_tensor.hweb</code>	Class representing folded general symmetry dense tensor, for instance $[g_{y^2u^3}]$
<code>&lt;UFSTensor&gt;</code> class declaration <a href="#">233</a> <code>fs_tensor.hweb</code>	Class representing unfolded full symmetry dense tensor, for instance $[g_{y^3}]$
<code>&lt;UGSTensor&gt;</code> class declaration <a href="#">259</a> <code>gs_tensor.hweb</code>	Class representing unfolded general symmetry dense tensor, for instance $[g_{y^2u^3}]$
<code>&lt;URTensor&gt;</code> class declaration <a href="#">290</a> <code>&lt;FRTensor&gt;</code> class declaration <a href="#">292</a> <code>rfs_tensor.hweb</code>	Class representing unfolded/folded full symmetry, row-oriented, dense tensor. Row-oriented tensors are used in the Faa Di Bruno above as some part (few or one column) of a product of $g$ 's. Their fold/unfold conversions are special in such a way, that they must yield equivalent results if multiplied with folded/unfolded column-oriented counterparts.
<code>&lt;URSingleTensor&gt;</code> class declaration <a href="#">294</a> <code>&lt;FRSingleTensor&gt;</code> class declaration <a href="#">295</a> <code>rfs_tensor.hweb</code>	Class representing unfolded/folded full symmetry, row-oriented, single column, dense tensor. Besides use in the Faa Di Bruno, the single column row oriented tensor models also higher moments of normal distribution.

⟨ **UPSTensor** class declaration 314 ⟩  
`ps_tensor.hweb`

Class representing unfolded, column-oriented tensor whose symmetry is not that of the  $[B_{y^2u^3}]$  but rather of something as  $[B_{yuyyu}]$ . This tensor evolves during the product operation for unfolded tensors and its basic operation is to add itself to a tensor with nicer symmetry, here  $[B_{y^2u^3}]$ .

⟨ **FPSTensor** class declaration 317 ⟩  
`ps_tensor.hweb`

Class representing partially folded, column-oriented tensor whose symmetry is not that of the  $[B_{y^3u^4}]$  but rather something as  $[B_{yu|y^3u|u^4}]$ , where the portions of symmetries represent folded dimensions which are combined in unfolded manner. This tensor evolves during the Faa Di Bruno for folded tensors and its basic operation is to add itself to a tensor with nicer symmetry, here folded  $[B_{y^3u^4}]$ .

⟨ **USubTensor** class declaration 471 ⟩  
`pyramid_prod.hweb`

Class representing unfolded full symmetry, row-oriented tensor which contains a few columns of huge product  $\prod_{m=1}^l [g_{c_m}]_{c_m(\alpha)}^{\gamma_m}$ . This is needed during the Faa Di Bruno for folded matrices.

⟨ **IrregTensor** class declaration 479 ⟩  
`pyramid2_prod.hweb`

Class representing a product of columns of derivatives  $[z_{y^k u^l}]$ , where  $z = [y^T, v^T, w^T]^T$ . Since the first part of  $z$  is  $y$ , the derivatives contain many zeros, which are not stored, hence the tensor's irregularity. The tensor is used when calculating one step of Faa Di Bruno formula, i.e.  $[f_{z^l}] \sum \prod_{m=1}^l [z_{c_m}]_{c_m(\alpha)}^{\gamma_m}$ .

⟨ **FSSparseTensor** class declaration 344 ⟩  
`sparse_tensor.hweb`

Class representing full symmetry, column-oriented, sparse tensor. It is able to store elements keyed by the multidimensional index, and multiply itself with one column of row-oriented tensor.

⟨ **FGSContainer** class declaration 384 ⟩  
`t_container.hweb`

Container of **FGSTensors**. It implements the Faa Di Bruno with unfolded or folded tensor  $h$  yielding folded  $B$ . The methods are **FGSContainer** :: *multAndAdd*.

⟨ **UGSContainer** class declaration 383 ⟩  
`t_container.hweb`

Container of **FGSTensors**. It implements the Faa Di Bruno with unfolded tensor  $h$  yielding unfolded  $B$ . The method is **UGSContainer** :: *multAndAdd*.

⟨ **StackContainerInterface** class declaration 395 ⟩  
`stack_container.hweb`

Virtual pure interface describing all logic of stacked containers for which we will do the Faa Di Bruno operation.

⟨ **UnfoldedStackContainer** class declaration 404 ⟩  
`stack_container.hweb`

Implements the Faa Di Bruno operation for stack of containers of unfolded tensors.

⟨ **FoldedStackContainer** class declaration 403 ⟩  
`stack_container.hweb`

Implements the Faa Di Bruno for stack of containers of folded tensors.

⟨ **ZContainer** class declaration 405 ⟩  
`stack_container.hweb`

The class implements the interface **StackContainerInterface** according to  $z$  appearing in context of SDGE models. By a simple inheritance, we obtain ⟨ **UnfoldedZContainer** class declaration 408 ⟩ and also ⟨ **FoldedZContainer** class declaration 407 ⟩.

⟨ **GContainer** class declaration 409 ⟩  
`stack_container.hweb`

The class implements the interface **StackContainerInterface** according to  $G$  appearing in context of SDGE models. By a simple inheritance, we obtain ⟨ **UnfoldedGContainer** class declaration 412 ⟩ and also ⟨ **FoldedGContainer** class declaration 411 ⟩.

⟨ **Equivalence** class declaration 154 ⟩  
`equivalence.hweb`

The class represents an equivalence on  $n$ -element set. Useful in the Faa Di Bruno.

⟨ **EquivalenceSet** class declaration 155 ⟩  
`equivalence.hweb`

The class representing all equivalences on  $n$ -element set. Useful in the Faa Di Bruno.

⟨ **Symmetry** class declaration 135 ⟩  
`symmetry.hweb`

The class defines a symmetry of general symmetry tensor. This is it defines a basic shape of the tensor. For  $[B_{y^2u^3}]$ , the symmetry is  $y^2u^3$ .

⟨ **Permutation** class declaration 193 ⟩  
`permutation.hweb`

The class represents a permutation of  $n$  indices. Useful in the Faa Di Bruno.

⟨ **IntSequence** class declaration 50 ⟩  
`int_sequence.hweb`

The class represents a sequence of integers. Useful everywhere.

⟨ **TwoDMatrix** class declaration 82 ⟩  
 ⟨ **ConstTwoDMatrix** class  
 declaration 81 ⟩  
`twod_matrix.hweb`

The class provides an interface to a code handling two-dimensional matrices. The code resides in Sylvester module, in directory `sylv/cc`. The object files from that directory need to be linked: `GeneralMatrix.o`, `Vector.o` and `SylvException.o`. There is no similar interface to **Vector** and **ConstVector** classes from the Sylvester module and they are used directly.

⟨ **KronProdAll** class declaration 99 ⟩  
`kron_prod.hweb`

The class represents a Kronecker product of a sequence of arbitrary matrices and is able to multiply a matrix from the right without storing the Kronecker product in memory.

⟨ **KronProdAllOptim** class  
 declaration 100 ⟩  
`kron_prod.hweb`

The same as **KronProdAll** but it optimizes the order of matrices in the product to minimize the used memory during the Faa Di Bruno operation. Note that it is close to optimal flops.

⟨ **FTensorPolynomial** class  
 declaration 504 ⟩  
 ⟨ **UTensorPolynomial** class  
 declaration 503 ⟩  
`t_polynomial.hweb`

Abstractions representing a polynomial whose coefficients are folded/unfolded tensors and variable is a column vector. The classes provide methods for traditional and horner-like polynomial evaluation. This is useful in simulation code.

⟨ **FNormalMoments** class  
 declaration 520 ⟩  
 ⟨ **UNormalMoments** class  
 declaration 519 ⟩  
`normal_moments.hweb`

These are containers for folded/unfolded single column tensors for higher moments of normal distribution. The code contains an algorithm for generating the moments for arbitrary covariance matrix.

⟨ **TLStatic** class declaration 531 ⟩  
`tl_static.hweb`

The class encapsulates all static information needed for the library. It includes a Pascal triangle (for quick computation of binomial coefficients), and precalculated equivalence sets.

⟨ **TLException** class definition 47 ⟩  
`tl_exception.hweb`

Simple class thrown as an exception.

**2.** The tensor library is multi-threaded. This means, if appropriate compilation options were set, some codes are launched concurrently. This boosts the performance on SMP machines or single processors with hyper-threading support. The basic property of the thread implementation in the library is that we do not allow running more concurrent threads than the preset limit. This prevents threads from competing for memory in such a way that the OS constantly switches among threads with frequent I/O for swaps. This may occur since one thread might need much own memory. The threading support allows for detached threads, the synchronization points during the Faa Di Bruno operation are relatively short, so the resulting load is close to the preset maximum number parallel threads.

**3.** A few words to the library's test suite. The suite resides in directory `tl/testing`. There is a file `tests.cpp` which contains all tests and `main()` function. Also there are files `factory.h` and `factory.cpp` implementing random generation of various objects. The important property of these random objects is that they are the same for all object's invocations. This is very important in testing and debugging. Further, one can find files `monoms.h` and `monoms.cpp`. See below for their explanation.

There are a few types of tests:

- 1) We test for tensor indices. We go through various tensors with various symmetries, convert indices from folded to unfolded and vice-versa. We test whether their coordinates are as expected.
- 2) We test the Faa Di Bruno by comparison of the results of `FGSContainer::multAndAdd` against the results of `UGSContainer::multAndAdd`. The two implementations are pretty different, so this is a good test.
- 3) We use a code in `monoms.h` and `monoms.cpp` to generate a random vector function  $f(x(y, u))$  along with derivatives of  $[f_x]$ ,  $[x_{y^k u^l}]$ , and  $[f_{y^k u^l}]$ . Then we calculate the resulting derivatives  $[f_{y^k u^l}]$  using `multAndAdd` method of `UGSContainer` or `FGSContainer` and compare the derivatives provided by `monoms`. The functions generated in `monoms` are monomials with integer exponents, so the implementation of `monoms` is quite easy.
- 4) We do a similar thing for sparse tensors. In this case the `monoms` generate a function  $f(y, v(y, u), w(y, u))$ , provide all the derivatives and the result  $[f_{y^k u^l}]$ . Then we calculate the derivatives with `multAndAdd` of `ZContainer` and compare.
- 5) We test the polynomial evaluation by evaluating a folded and unfolded polynomial in traditional and horner-like fashion. This gives four methods in total. The four results are compared.

**4. Utilities.**

### 5. Simple threads. Start of `sthreads.h` file.

This file defines types making a simple interface to multi-threading. It follows the classical C++ idioms for traits. We have three sorts of traits. The first is a **thread\_traits**, which make interface to thread functions (run, exit, create and join), the second is **mutex\_traits**, which make interface to mutexes (create, lock, unlock), and third is **cond\_traits**, which make interface to conditions (create, wait, broadcast, and destroy). At present, there are two implementations. The first are POSIX threads, mutexes, and conditions, the second is serial (no parallelization).

The file provides the following interfaces templated by the types implementing the threading (like types **pthread\_t**, and **pthread\_mutex\_t** for POSIX thread and mutex):

- **thread** is a pure virtual class, which must be inherited and a method **operator()()** be implemented as the running code of the thread. This code is run as a new thread by calling *run* method.
- **thread\_group** allows insertion of **threads** and running all of them simultaneously joining them. The number of maximum parallel threads can be controlled. See below.
- **synchro** object locks a piece of code to be executed only serially for a given data and specified entry-point. It locks the code until it is destructed. So, the typical use is to create the **synchro** object on the stack of a function which is to be synchronized. The synchronization can be subjected to specific data (then a pointer can be passed to **synchro**'s constructor), and can be subjected to specific entry-point (then **const char \*** is passed to the constructor).
- **detach\_thread** inherits from **thread** and models a detached thread in contrast to **thread** which models the joinable thread.
- **detach\_thread\_group** groups the detached threads and runs them. They are not joined, they are synchronized by means of a counter counting running threads. A change of the counter is checked by waiting on an associated condition.

What implementation is selected is governed (at present) by **HAVE\_PTHREAD**. If it is defined, then POSIX threads are linked. If it is not defined, then serial implementation is taken. In accordance with this, the header file defines macros **THREAD**, **THREAD\_GROUP**, and **SYNCHRO** as the picked specialization of **thread** (or **detach\_thread**), **thread\_group** (or **detach\_thread\_group**), and **synchro**.

The type of implementation is controlled by **thread\_impl** integer template parameter, this can be *posix* or *empty*.

The number of maximum parallel threads is controlled via a static member of **thread\_group** and **detach\_thread\_group** classes.

```
#ifndef STHREAD_H
#define STHREAD_H
#ifdef HAVE_PTHREAD
#include <pthread.h>
#else
/* Give valid types for POSIX thread types, otherwise the templates fail in empty mode. Don't use
   typedefs because on some systems pthread_t and friends are typedefs even without the include. */
#define pthread_t void *
#define pthread_mutex_t void *
#define pthread_cond_t void *
#endif
#include <cstdio>
#include <list>
#include <map>
namespace sthread {
    using namespace std;
    class Empty {};
    < classical IF template 6 >;
    enum { posix, empty };
}
```

```

template<int> class thread_traits;
template<int> class detach_thread;
< thread template class declaration 7 >;
< thread_group template class declaration 8 >;
< thread_traits template class declaration 12 >;
< mutex_traits template class declaration 13 >;
< mutex_map template class declaration 14 >;
< synchro template class declaration 17 >;
< cond_traits template class declaration 20 >;
< condition_counter template class declaration 21 >;
< detach_thread template class declaration 27 >;
< detach_thread_group template class declaration 28 >;
#ifdef HAVE_PTHREAD
    < POSIX thread specializations 32 >;
#else
    < No threading specializations 33 >;
#endif
    };
#endif

```

6. Here is the classical IF template.

```

< classical IF template 6 > ≡
    template<bool condition, class Then, class Else> struct IF {
        typedef Then RET;
    };
    template<class Then, class Else> struct IF<false, Then, Else> {
        typedef Else RET;
    };

```

This code is used in section 5.



7. The class of **thread** is clear. The user implements **operator()()**, the method *run* runs the user's code as joinable thread, *exit* kills the execution.

```
<thread template class declaration 7> ≡
template<int thread_impl> class thread {
    typedef thread_traits<thread_impl> _Ttraits;
    typedef typename _Ttraits::_Tthread _Tthread;
    _Tthread th;
public:
    virtual ~thread()
    {}
    _Tthread &getThreadId()
    { return th; }
    const _Tthread &getThreadId() const
    { return th; }
    virtual void operator()() = 0;
    void run()
    { _Ttraits::run(this); }
    void detach_run()
    { _Ttraits::detach_run(this); }
    void exit()
    { _Ttraits::exit(); }
};
```

This code is used in section 5.

8. The **thread\_group** is also clear. We allow a user to insert the **threads**, and then launch *run*, which will run all the threads not allowing more than *max\_parallel\_threads* joining them at the end. This static member can be set from outside.

```
<thread_group template class declaration 8> ≡
template<int thread_impl> class thread_group {
    typedef thread_traits<thread_impl> _Ttraits;
    typedef thread<thread_impl> _Ctype;
    list<_Ctype*> tlist;
    typedef typename list<_Ctype*>::iterator iterator;
public:
    static int max_parallel_threads;
    void insert(_Ctype *c)
    { tlist.push_back(c); }
    <thread_group destructor code 9>;
    <thread_group::run code 11>;
private:
    <thread_group::run_portion code 10>;
};
```

This code is used in section 5.

9. The thread group class maintains list of pointers to threads. It takes responsibility of deallocating the threads. So we implement the destructor.

```
<thread_group destructor code 9> ≡
~thread_group()
{
    while (!tlist.empty()) {
        delete tlist.front();
        tlist.pop_front();
    }
}
```

This code is used in section 8.

10. This runs a given number of threads in parallel starting from the given iterator. It returns the first iterator not run.

```
<thread_group::run_portion code 10> ≡
iterator run_portion(iterator start, int n)
{
    int c = 0;
    for (iterator i = start; c < n; ++i, c++) {
        (*i)→run();
    }
    iterator ret;
    c = 0;
    for (ret = start; c < n; ++ret, c++) {
        _Ttraits::join(*ret);
    }
    return ret;
}
```

This code is used in section 8.

11. Here we run the threads ensuring that not more than *max\_parallel\_threads* are run in parallel. More over, we do not want to run a too low number of threads, since it is wasting with resource (if there are). Therefore, we run in parallel *max\_parallel\_threads* batches as long as the remaining threads are greater than the double number. And then the remaining batch (less than  $2 * \text{max\_parallel\_threads}$ ) is run half by half.

```
<thread_group::run code 11> ≡
void run()
{
    int rem = tlist.size();
    iterator pfirst = tlist.begin();
    while (rem > 2 * max_parallel_threads) {
        pfirst = run_portion(pfirst, max_parallel_threads);
        rem -= max_parallel_threads;
    }
    if (rem > max_parallel_threads) {
        pfirst = run_portion(pfirst, rem/2);
        rem -= rem/2;
    }
    run_portion(pfirst, rem);
}
```

This code is used in section 8.

12. Clear. We have only *run*, *detach\_run*, *exit* and *join*, since this is only a simple interface.

```
<thread_traits template class declaration 12> ≡
template<int thread_impl> struct thread_traits {
    typedef typename IF<thread_impl ≡ posix, pthread_t, Empty>::RET _Tthread;
    typedef thread<thread_impl> _Ctype;
    typedef detach_thread<thread_impl> _Dtype;
    static void run(_Ctype *c);
    static void detach_run(_Dtype *c);
    static void exit();
    static void join(_Ctype *c);
};
```

This code is used in section 5.

13. Clear. We have only *init*, *lock*, and *unlock*.

```
<mutex_traits template class declaration 13> ≡
struct ltmmkey;
typedef pair<const void *, const char *> mmkey;
template<int thread_impl> struct mutex_traits {
    typedef typename IF<thread_impl ≡ posix, pthread_mutex_t, Empty>::RET _Tmutex;
    typedef map<mmkey, pair<_Tmutex, int>, ltmmkey> mutex_int_map;
    static void init(_Tmutex &m);
    static void lock(_Tmutex &m);
    static void unlock(_Tmutex &m);
};
```

This code is used in section 5.

**14.** Here we define a map of mutexes keyed by a pair of address, and a string. A purpose of the map of mutexes is that, if synchronizing, we need to publish mutexes locking some piece of codes (characterized by the string) accessing the data (characterized by the pointer). So, if any thread needs to pass a **synchro** object, it creates its own with the same address and string, and must look to some public storage to unlock the mutex. If the **synchro** object is created for the first time, the mutex is created and inserted to the map. We count the references to the mutex (number of waiting threads) to know, when it is save to remove the mutex from the map. This is the only purpose of counting the references. Recall, that the mutex is keyed by an address of the data, and without removing, the number of mutexes would only grow.

The map itself needs its own mutex to avoid concurrent insertions and deletions.

(**mutex\_map** template class declaration 14)  $\equiv$

```
struct ltmmkey {
    bool operator()(const mmkey &k1, const mmkey &k2) const
    {
        return k1.first < k2.first  $\vee$  (k1.first  $\equiv$  k2.first  $\wedge$  strcmp(k1.second, k2.second) < 0);
    }
};

template<int thread_impl> class mutex_map : public
    mutex_traits<thread_impl>::mutex_int_map {
    typedef typename mutex_traits<thread_impl>::_Tmutex _Tmutex;
    typedef mutex_traits<thread_impl> _Mtraits;
    typedef pair<_Tmutex, int> mmval;
    typedef map<mmkey, mmval, ltmmkey> _Tparent;
    typedef typename _Tparent::iterator iterator;
    typedef typename _Tparent::value_type _mvtype;
    _Tmutex m;

public:
    mutex_map()
    { _Mtraits::init(m); }

    void insert(const void *c, const char *id, const _Tmutex &m)
    { _Tparent::insert(_mvtype(mmkey(c, id), mmval(m, 0))); }

    bool check(const void *c, const char *id) const
    { return _Tparent::find(mmkey(c, id))  $\neq$  _Tparent::end(); }

    <mutex_map::get code 15>;
    <mutex_map::remove code 16>;

    void lock_map()
    { _Mtraits::lock(m); }

    void unlock_map()
    { _Mtraits::unlock(m); }
};
```

This code is used in section 5.

15. This returns a pointer to the pair of mutex and count reference number.

```

<mutex_map::get code 15> ≡
    mmval *get(const void *c, const char *id)
    {
        iterator it = _Tparent::find(mmkey(c, id));
        if (it == _Tparent::end()) return Λ;
        return &(*it).second;
    }

```

This code is used in section 14.

16. This removes unconditionally the mutex from the map regardless its number of references. The only user of this class should be **synchro** class, its implementation must not remove referenced mutex.

```

<mutex_map::remove code 16> ≡
    void remove(const void *c, const char *id)
    {
        iterator it = _Tparent::find(mmkey(c, id));
        if (it != _Tparent::end()) this->erase(it);
    }

```

This code is used in section 14.

17. This is the **synchro** class. The constructor of this class tries to lock a mutex for a particular address (identification of data) and string (identification of entry-point). If the mutex is already locked, it waits until it is unlocked and then returns. The destructor releases the lock. The typical use is to construct the object on the stack of the code being synchronized.

```

<synchro template class declaration 17> ≡
    template<int thread_impl> class synchro {
        typedef typename mutex_traits<thread_impl>::_Tmutex _Tmutex;
        typedef mutex_traits<thread_impl> _Mtraits;
    public:
        typedef mutex_map<thread_impl> mutex_map_t;
    private:
        const void *caller;
        const char *iden;
        mutex_map_t &mutmap;
    public:
        synchro(const void *c, const char *id, mutex_map_t &mmap)
        : caller(c), iden(id), mutmap(mmap) { lock(); }
        ~synchro()
        { unlock(); }
    private:
        <synchro::lock code 18>;
        <synchro::unlock code 19>;
    };

```

This code is used in section 5.

**18.** The *lock* function acquires the mutex in the map. First it tries to get an exclusive access to the map. Then it increases a number of references of the mutex (if it does not exists, it inserts it). Then unlocks the map, and finally tries to lock the mutex of the map.

```

<synchro::lock code 18> ≡
void lock()
{
    mutmap.lock_map();
    if (¬mutmap.check(caller, iden)) {
        _Tmutex mut;
        _Mtraits::init(mut);
        mutmap.insert(caller, iden, mut);
    }
    mutmap.get(caller, iden)→second++;
    mutmap.unlock_map();
    _Mtraits::lock(mutmap.get(caller, iden)→first);
}

```

This code is used in section 17.

**19.** The *unlock* function first locks the map. Then releases the lock, and decreases a number of references. If it is zero, it removes the mutex.

```

<synchro::unlock code 19> ≡
void unlock()
{
    mutmap.lock_map();
    if (mutmap.check(caller, iden)) {
        _Mtraits::unlock(mutmap.get(caller, iden)→first);
        mutmap.get(caller, iden)→second--;
        if (mutmap.get(caller, iden)→second ≡ 0) mutmap.remove(caller, iden);
    }
    mutmap.unlock_map();
}

```

This code is used in section 17.

**20.** These are traits for conditions. We need *init*, *broadcast*, *wait* and *destroy*.

```

<cond_traits template class declaration 20> ≡
template<int thread_impl> struct cond_traits {
    typedef typename IF<thread_impl ≡ posix, pthread_cond_t, Empty>::RET _Tcond;
    typedef typename mutex_traits<thread_impl>::_Tmutex _Tmutex;
    static void init(_Tcond &cond);
    static void broadcast(_Tcond &cond);
    static void wait(_Tcond &cond, _Tmutex &mutex);
    static void destroy(_Tcond &cond);
};

```

This code is used in section 5.

**21.** Here is the condition counter. It is a counter which starts at 0, and can be increased and decreased. A thread can wait until the counter is changed, this is implemented by condition. After the wait is done, another (or the same) thread, by calling *waitForChange* waits for another change. This can be dangerous, since it is possible to wait for a change which will not happen, because all the threads which can cause the change (by increase or decrease) might have finished.

```
<condition_counter template class declaration 21> ≡
template<int thread_impl> class condition_counter {
    typedef typename mutex_traits<thread_impl>::_Tmutex _Tmutex;
    typedef typename cond_traits<thread_impl>::_Tcond _Tcond;
    int counter;
    _Tmutex mut;
    _Tcond cond;
    bool changed;

public:
    <condition_counter constructor code 22>;
    <condition_counter destructor code 23>;
    <condition_counter::increase code 24>;
    <condition_counter::decrease code 25>;
    <condition_counter::waitForChange code 26>;
};
```

This code is used in section 5.

**22.** We initialize the counter to 0, and *changed* flag to *true*, since the counter was changed from undefined value to 0.

```
<condition_counter constructor code 22> ≡
condition_counter()
: counter(0), changed(true) {
    mutex_traits<thread_impl>::init(mut);
    cond_traits<thread_impl>::init(cond);
}
```

This code is used in section 21.

**23.** In destructor, we only release the resources associated with the condition.

```
<condition_counter destructor code 23> ≡
~condition_counter()
{
    cond_traits<thread_impl>::destroy(cond);
}
```

This code is used in section 21.

**24.** When increasing, we lock the mutex, advance the counter, remember it is changed, broadcast, and release the mutex.

```
⟨condition_counter::increase code 24⟩ ≡
  void increase()
  {
    mutex_traits<thread_impl>::lock(mut);
    counter++;
    changed = true;
    cond_traits<thread_impl>::broadcast(cond);
    mutex_traits<thread_impl>::unlock(mut);
  }
```

This code is used in section 21.

**25.** Same as increase.

```
⟨condition_counter::decrease code 25⟩ ≡
  void decrease()
  {
    mutex_traits<thread_impl>::lock(mut);
    counter--;
    changed = true;
    cond_traits<thread_impl>::broadcast(cond);
    mutex_traits<thread_impl>::unlock(mut);
  }
```

This code is used in section 21.

**26.** We lock the mutex, and if there was a change since the last call of *waitForChange*, we return immediately, otherwise we wait for the change. The mutex is released.

```
⟨condition_counter::waitForChange code 26⟩ ≡
  int waitForChange()
  {
    mutex_traits<thread_impl>::lock(mut);
    if (¬changed) {
      cond_traits<thread_impl>::wait(cond, mut);
    }
    changed = false;
    int res = counter;
    mutex_traits<thread_impl>::unlock(mut);
    return res;
  }
```

This code is used in section 21.



**27.** The detached thread is the same as joinable **thread**. We only re-implement *run* method to call **thread\_traits::detach\_run**, and add a method which installs a counter. The counter is increased and decreased on the body of the new thread.

```
<detach_thread template class declaration 27> ≡
template<int thread_impl> class detach_thread : public thread<thread_impl> {
public:
    condition_counter<thread_impl> *counter;
    detach_thread()
    : counter(Λ) {}
    void installCounter(condition_counter<thread_impl> *c)
    { counter = c; }
    void run()
    { thread_traits<thread_impl>::detach_run(this); }
};
```

This code is used in section 5.

**28.** The detach thread group is (by interface) the same as **thread\_group**. The extra thing we have here is the *counter*. The implementation of *insert* and *run* is different.

```
<detach_thread_group template class declaration 28> ≡
template<int thread_impl> class detach_thread_group {
    typedef thread_traits<thread_impl> _Ttraits;
    typedef cond_traits<thread_impl> _Ctrait;
    typedef detach_thread<thread_impl> _Ctype;
    list<_Ctype*> tlist;
    typedef typename list<_Ctype*>::iterator iterator;
    condition_counter<thread_impl> counter;
public:
    static int max_parallel_threads;
    <detach_thread_group::insert code 29>;
    <detach_thread_group destructor code 30>;
    <detach_thread_group::run code 31>;
};
```

This code is used in section 5.

**29.** When inserting, the counter is installed to the thread.

```
<detach_thread_group::insert code 29> ≡
void insert(_Ctype *c)
{
    tlist.push_back(c);
    c->installCounter(&counter);
}
```

This code is used in section 28.

30. The destructor is clear.

```

<detach_thread_group destructor code 30> ≡
~detach_thread_group()
{
    while (¬tlist.empty()) {
        delete tlist.front();
        tlist.pop_front();
    }
}

```

This code is used in section 28.

31. We cycle through all threads in the group, and in each cycle we wait for the change in the *counter*. If the counter indicates less than maximum parallel threads running, then a new thread is run, and the iterator in the list is moved.

At the end we have to wait for all thread to finish.

```

<detach_thread_group::run code 31> ≡
void run()
{
    int mpt = max_parallel_threads;
    iterator it = tlist.begin();
    while (it ≠ tlist.end()) {
        if (counter.waitForChange() < mpt) {
            counter.increase();
            (*it)→run();
            ++it;
        }
    }
    while (counter.waitForChange() > 0) {}
}

```

This code is used in section 28.

32. Here we only define the specializations for POSIX threads. Then we define the macros. Note that the *PosixSynchro* class construct itself from the static map defined in `sthreads.cpp`.

```

<POSIX thread specializations 32> ≡
typedef detach_thread<posix> PosixThread;
typedef detach_thread_group<posix> PosixThreadGroup;
typedef synchro<posix> posix_synchro;
class PosixSynchro : public posix_synchro {
public:
    PosixSynchro(const void *c, const char *id);
};

#define THREAD sthread::PosixThread
#define THREAD_GROUP sthread::PosixThreadGroup
#define SYNCHRO sthread::PosixSynchro

```

This code is used in section 5.

**33.** Here we define an empty class and use it as thread and mutex. *NoSynchro* class is also empty, but an empty constructor is declared. The empty destructor is declared only to avoid “unused variable warning”.

```
<No threading specializations 33> ≡
    typedef thread<empty> NoThread;
    typedef thread_group<empty> NoThreadGroup;
    typedef synchro<empty> no_synchro;
    class NoSynchro {
    public:
        NoSynchro(const void *c, const char *id)
        {}
        ~NoSynchro()
        {}
    };
#define THREAD sthread::NoThread
#define THREAD_GROUP sthread::NoThreadGroup
#define SYNCHRO sthread::NoSynchro
```

This code is used in section 5.

**34.** End of `sthreads.h` file.

**35.** Start of `sthreads.h` file. We set the default values for *max\_parallel\_threads* for both *posix* and *empty* implementation and both joinable and detach group. For *posix* this defaults to uniprocessor machine with hyper-threading, this is 2.

```
#include <cstring>
#include "sthread.h"
#ifdef HAVE_PTHREAD
    namespace sthread {
        template<> int thread_group<posix>::max_parallel_threads = 2;
        template<> int detach_thread_group<posix>::max_parallel_threads = 2;
        <POSIX specializations methods 36>;
    }
#else
    namespace sthread {
        template<> int thread_group<empty>::max_parallel_threads = 1;
        template<> int detach_thread_group<empty>::max_parallel_threads = 1;
        <non-threading specialization methods 43>;
    }
#endif
```

**36.**

```
<POSIX specializations methods 36> ≡
    <thread_traits method codes 37>;
    <mutex_traits method codes 38>;
    <cond_traits method codes 39>;
    <PosixSynchro constructor 40>;
    <posix_thread_function code 41>;
    <posix_detach_thread_function code 42>;
```

This code is used in section 35.

**37.**

```

<thread_traits method codes 37> ≡
  void *posix_thread_function(void *c);
  template<> void thread_traits<posix>::run(_Ctype *c)
  {
    pthread_create(&(c->getThreadId()), Λ, posix_thread_function, (void *) c);
  }
  void *posix_detach_thread_function(void *c);
  template<> void thread_traits<posix>::detach_run(_Dtype *c)
  {
    pthread_attr_t attr;
    pthread_attr_init(&attr);
    pthread_attr_setdetachstate(&attr, PTHREAD_CREATE_DETACHED);
    pthread_create(&(c->getThreadId()), &attr, posix_detach_thread_function, (void *) c);
    pthread_attr_destroy(&attr);
  }
  template<> void thread_traits<posix>::exit()
  {
    pthread_exit(Λ);
  }
  template<> void thread_traits<posix>::join(_Ctype *c)
  {
    pthread_join(c->getThreadId(), Λ);
  }

```

This code is used in section 36.

**38.**

```

<mutex_traits method codes 38> ≡
  template<> void mutex_traits<posix>::init(pthread_mutex_t &m)
  {
    pthread_mutex_init(&m, Λ);
  }
  template<> void mutex_traits<posix>::lock(pthread_mutex_t &m)
  {
    pthread_mutex_lock(&m);
  }
  template<> void mutex_traits<posix>::unlock(pthread_mutex_t &m)
  {
    pthread_mutex_unlock(&m);
  }

```

This code is used in section 36.

**39.**

```

<cond_traits method codes 39> ≡
  template<> void cond_traits<posix>::init(_Tcond &cond)
  {
    pthread_cond_init(&cond, Λ);
  }
  template<> void cond_traits<posix>::broadcast(_Tcond &cond)
  {
    pthread_cond_broadcast(&cond);
  }
  template<> void cond_traits<posix>::wait(_Tcond &cond, _Tmutex &mutex)
  {
    pthread_cond_wait(&cond, &mutex);
  }
  template<> void cond_traits<posix>::destroy(_Tcond &cond)
  {
    pthread_cond_destroy(&cond);
  }

```

This code is used in section 36.

**40.** Here we instantiate the static map, and construct **PosixSynchro** using that map.

```

<PosixSynchro constructor 40> ≡
  static posix_synchro::mutex_map_t posix_mm;
  PosixSynchro::PosixSynchro(const void *c, const char *id)
  : posix_synchro(c, id, posix_mm) {}

```

This code is used in section 36.

**41.** This function is of the type **void \*function(void \*)** as required by POSIX, but it typecasts its argument and runs **operator()()**.

```

<posix_thread_function code 41> ≡
  void *posix_thread_function(void *c)
  {
    thread_traits<posix>::_Ctype *ct = (thread_traits<posix>::_Ctype *) c;
    try {
      ct->operator()();
    }
    catch(...)
    {
      ct->exit();
    }
    return Λ;
  }

```

This code is used in section 36.

42.

```

<posix_detach_thread_function code 42> ≡
void *posix_detach_thread_function(void *c)
{
    thread_traits<posix>::_Dtype *ct = (thread_traits<posix>::_Dtype *) c;
    condition_counter<posix> *counter = ct->counter;
    try {
        ct->operator()();
    }
    catch(...)
    {
        ct->exit();
    }
    if (counter) counter->decrease();
    return Λ;
}

```

This code is used in section 36.

43. The only trait methods we need to work are **thread\_traits::run** and **thread\_traits::detach\_run**, which directly call **operator()()**. Anything other is empty.

```

<non-threading specialization methods 43> ≡
template<> void thread_traits<empty>::run(_Ctype *c)
{
    c->operator()();
}
template<> void thread_traits<empty>::detach_run(_Dtype *c)
{
    c->operator()();
}
template<> void thread_traits<empty>::exit()
{}
template<> void thread_traits<empty>::join(_Ctype *c)
{}
template<> void mutex_traits<empty>::init(Empty &m)
{}
template<> void mutex_traits<empty>::lock(Empty &m)
{}
template<> void mutex_traits<empty>::unlock(Empty &m)
{}
template<> void cond_traits<empty>::init(_Tcond &cond)
{}
template<> void cond_traits<empty>::broadcast(_Tcond &cond)
{}
template<> void cond_traits<empty>::wait(_Tcond &cond, _Tmutex &mutex)
{}
template<> void cond_traits<empty>::destroy(_Tcond &cond)
{}

```

This code is used in section 35.

44. End of `sthreads.h` file.

45. **Exception.** Start of `tl_exception.h` file.

Within the code we often check some state of variables, typically preconditions or postconditions. If the state is not as required, it is worthless to continue, since this means some fatal error in algorithms. In this case we raise an exception which can be caught at some higher level. This header file defines a simple infrastructure for this.

```
#ifndef TL_EXCEPTION_H
#define TL_EXCEPTION_H
#include <cstring>
#include <stdio>
    (body of tl_exception header 46);
#endif
```

46. The basic idea of raising an exception if some condition fails is that the conditions is checked only if required. We define global `TL_DEBUG` macro which is integer and says, how many debug messages the programm has to emit. We also define `TL_DEBUG_EXCEPTION` which says, for what values of `TL_DEBUG` we will check for conditions of the exceptions. If the `TL_DEBUG` is equal or higher than `TL_DEBUG_EXCEPTION`, the exception conditions are checked.

We define `TL_RAISE`, and `TL_RAISE_IF` macros which throw an instance of **`TLException`** if `TL_DEBUG`  $\geq$  `TL_DEBUG_EXCEPTION`. The first is unconditional throw, the second is conditioned by a given expression. Note that if `TL_DEBUG`  $<$  `TL_DEBUG_EXCEPTION` then the code is compiled but evaluation of the condition is passed. If code is optimized, the optimizer also passes evaluation of `TL_DEBUG` and `TL_DEBUG_EXCEPTION` comparison (I hope).

We provide default values for `TL_DEBUG` and `TL_DEBUG_EXCEPTION`.

```
(body of tl_exception header 46)  $\equiv$ 
#ifndef TL_DEBUG_EXCEPTION
#define TL_DEBUG_EXCEPTION 1
#endif
#ifndef TL_DEBUG
#define TL_DEBUG 0
#endif
#define TL_RAISE(mes)
    if (TL_DEBUG  $\geq$  TL_DEBUG_EXCEPTION) throw TLException(__FILE__, __LINE__, mes);
#define TL_RAISE_IF(expr, mes)
    if (TL_DEBUG  $\geq$  TL_DEBUG_EXCEPTION  $\wedge$  (expr)) throw TLException(__FILE__, __LINE__, mes);
    (TLException class definition 47);
```

This code is used in section 45.

**47.** Primitive exception class containing file name, line number and message.

```

< TLEException class definition 47 > ≡
class TLEException {
    char fname[50];
    int lnum;
    char message[500];
public:
    TLEException(const char *f, int l, const char *mes)
    {
        strncpy(fname, f, 50); fname[49] = '\0';
        strncpy(message, mes, 500); message[499] = '\0';
        lnum = l;
    }
    virtual ~TLEException() {}
    virtual void print() const
    { printf("At %s:%d:%s\n", fname, lnum, message); }
};

```

This code is cited in section 1.

This code is used in section 46.

**48.** End of `tl_exception.h` file.

**49. Integer sequence.** Start of `int_sequence.h` file.

Here we define an auxiliary abstraction for a sequence of integers. The basic functionality is to hold an ordered sequence of integers with constant length. We prefer using this simple class before STL `vector<int>` since it is more efficient for our purposes.

The class is used in index of a tensor, in symmetry definition, in Kronecker product dimensions, or as a class of an equivalence. The latter case is not ordered, but we always order equivalence classes in order to ensure unique representativeness. For almost all cases we need the integer sequence to be ordered (sort), or monotonize (indices of folded tensors), or partially monotonize (indices of folded tensors not fully symmetric), or calculate a product of all members or only of a part (used in Kronecker product dimensions). When we calculate offsets in folded tensors, we need to obtain a number of the same items in the front (*getPrefixLength*), and also to add some integer number to all items.

Also, we need to construct a subsequence of a sequence, so some instances do destroy the underlying data, and some not.

```

#ifndef INT_SEQUENCE_H
#define INT_SEQUENCE_H
#include <cstring>
#include <vector>
    using namespace std;
    < IntSequence class declaration 50 >;
#endif

```



**50.** The implementation of **IntSequence** is straightforward. It has a pointer *data*, a *length* of the data, and a flag *destroy*, whether the instance must destroy the underlying data.

⟨**IntSequence** class declaration 50⟩ ≡

```
class Symmetry;
class IntSequence {
    int *data;
    int length;
    bool destroy;
public:
    ⟨IntSequence constructors 51⟩;
    ⟨IntSequence inlines and operators 52⟩;
    ⟨IntSequence orderings 53⟩;
    void sort();
    void monotone();
    void pmonotone(const Symmetry &s);
    int sum() const;
    int mult(int i1, int i2) const;
    int mult() const
    { return mult(0, length); }
    void add(int i);
    void add(int f, const IntSequence &s);
    int getPrefixLength() const;
    int getNumDistinct() const;
    int getMax() const;
    bool isPositive() const;
    bool isConstant() const;
    bool isSorted() const;
    void print() const;
};
```

This code is cited in section 1.

This code is used in section 49.

**51.** We have a constructor allocating a given length of data, constructor allocating and then initializing all members to a given number, a copy constructor, a conversion from **vector****<int>**, a subsequence constructor, a constructor used for calculating implied symmetry from a more general symmetry and one equivalence class (see **Symmetry** class). Finally we have a constructor which unfolds a sequence with respect to a given symmetry and constructor which inserts a given number to the ordered sequence or given number to a given position.

```

<IntSequence constructors 51> ≡
IntSequence(int l)
: data(new int[l]), length(l), destroy(true) {}
IntSequence(int l, int n)
: data(new int[l]), length(l), destroy(true) { for (int i = 0; i < length; i++) data[i] = n; }
IntSequence(const IntSequence &s)
: data(new int[s.length]), length(s.length), destroy(true) { memcpy(data, s.data, length * sizeof(int));
}
IntSequence(IntSequence &s, int i1, int i2)
: data(s.data + i1), length(i2 - i1), destroy(false) {}
IntSequence(const IntSequence &s, int i1, int i2)
: data(new int[i2 - i1]), length(i2 - i1), destroy(true) {
    memcpy(data, s.data + i1, sizeof(int) * length); }
IntSequence(const Symmetry &sy, const vector<int> &se);
IntSequence(const Symmetry &sy, const IntSequence &se);
IntSequence(int i, const IntSequence &s);
IntSequence(int i, const IntSequence &s, int pos);
IntSequence(int l, const int *d)
: data(new int[l]), length(l), destroy(true) { memcpy(data, d, sizeof(int) * length); }

```

This code is used in section 50.

**52.** These are clear inlines and operators.

```

<IntSequence inlines and operators 52> ≡
const IntSequence &operator=(const IntSequence &s);
virtual ~IntSequence()
{ if (destroy) delete[] data; }
bool operator==(const IntSequence &s) const;
bool operator!=(const IntSequence &s) const
{ return !operator==(s); }
int &operator[](int i)
{ return data[i]; }
int operator[](int i) const
{ return data[i]; }
int size() const
{ return length; }

```

This code is used in section 50.

**53.** We provide two orderings. The first **operator <** is the linear lexicographic ordering, the second *less* is the non-linear Cartesian ordering.

```
<IntSequence orderings 53> ≡
  bool operator < (const IntSequence &s) const;
  bool operator ≤ (const IntSequence &s) const
  { return (operator ≡ (s) ∨ operator < (s)); }
  bool lessEq (const IntSequence &s) const;
  bool less (const IntSequence &s) const;
```

This code is used in section 50.

**54.** End of `int_sequence.h` file.

**55.** Start of `int_sequence.cpp` file.

```
#include "int_sequence.h"
#include "symmetry.h"
#include "tl_exception.h"
#include <cstdio>
#include <climits>
<IntSequence constructor code 1 56>;
<IntSequence constructor code 2 57>;
<IntSequence constructor code 3 58>;
<IntSequence constructor code 4 59>;
<IntSequence::operator= code 60>;
<IntSequence::operator ≡ code 61>;
<IntSequence::operator < code 62>;
<IntSequence::lessEq code 63>;
<IntSequence::less code 64>;
<IntSequence::sort code 65>;
<IntSequence::monotone code 66>;
<IntSequence::pmonotone code 67>;
<IntSequence::sum code 68>;
<IntSequence::mult code 69>;
<IntSequence::getPrefixLength code 70>;
<IntSequence::getNumDistinct code 71>;
<IntSequence::getMax code 72>;
<IntSequence::add code 1 73>;
<IntSequence::add code 2 74>;
<IntSequence::isPositive code 75>;
<IntSequence::isConstant code 76>;
<IntSequence::isSorted code 77>;
<IntSequence::print code 78>;
```

**56.** This unfolds a given integer sequence with respect to the given symmetry. If for example the symmetry is  $(2, 3)$ , and the sequence is  $(a, b)$ , then the result is  $(a, a, b, b, b)$ .

```

<IntSequence constructor code 1 56> ≡
IntSequence::IntSequence(const Symmetry &sy, const IntSequence &se)
: data(new int[sy.dimen()], length(sy.dimen()), destroy(true)) {
    int k = 0;
    for (int i = 0; i < sy.num(); i++)
        for (int j = 0; j < sy[i]; j++, k++) operator[](k) = se[i];
}

```

This code is used in section 55.

**57.** This constructs an implied symmetry (implemented as **IntSequence** from a more general symmetry and equivalence class (implemented as **vector<int>**). For example, let the general symmetry be  $y^3u^2$  and the equivalence class is  $\{0, 4\}$  picking up first and fifth variable, we calculate symmetry (at this point only **IntSequence**) corresponding to the picked variables. These are  $yu$ . Thus the constructed sequence must be  $(1, 1)$ , meaning that we picked one  $y$  and one  $u$ .

```

<IntSequence constructor code 2 57> ≡
IntSequence::IntSequence(const Symmetry &sy, const vector<int> &se)
: data(new int[sy.num()], length(sy.num()), destroy(true)) {
    TL_RAISE_IF(sy.dimen() ≤ se[se.size() - 1],
        "Sequence is not reachable by symmetry in IntSequence()");
    for (int i = 0; i < length; i++)
        operator[](i) = 0;
    for (unsigned int i = 0; i < se.size(); i++)
        operator[(sy.findClass(se[i]))++]++;
}

```

This code is used in section 55.

**58.** This constructs an ordered integer sequence from the given ordered sequence inserting the given number to the sequence.

```

<IntSequence constructor code 3 58> ≡
IntSequence::IntSequence(int i, const IntSequence &s)
: data(new int[s.size() + 1], length(s.size() + 1), destroy(true)) {
    int j = 0;
    while (j < s.size() ∧ s[j] < i) j++;
    for (int jj = 0; jj < j; jj++) operator[(jj)] = s[jj];
    operator[(j)] = i;
    for (int jj = j; jj < s.size(); jj++) operator[(jj + 1)] = s[jj];
}

```

This code is used in section 55.

59.

```

< IntSequence constructor code 4 59 > ≡
IntSequence::IntSequence(int i, const IntSequence &s, int pos)
: data(new int[s.size() + 1]), length(s.size() + 1), destroy(true) {
    TL_RAISE_IF(pos < 0 ∨ pos > s.size(),
        "Wrong_position_for_insertion_IntSequence_constructor");
    for (int jj = 0; jj < pos; jj++) operator[](jj) = s[jj];
    operator[](pos) = i;
    for (int jj = pos; jj < s.size(); jj++) operator[](jj + 1) = s[jj];
}

```

This code is used in section 55.

60.

```

< IntSequence::operator= code 60 > ≡
const IntSequence &IntSequence::operator=(const IntSequence &s)
{
    TL_RAISE_IF(¬destroy ∧ length ≠ s.length, "Wrong_length_for_in-place_IntSequence::operator=");
    if (destroy ∧ length ≠ s.length) {
        delete[] data;
        data = new int[s.length];
        destroy = true;
        length = s.length;
    }
    memcpy(data, s.data, sizeof(int) * length);
    return *this;
}

```

This code is used in section 55.

61.

```

< IntSequence::operator≡ code 61 > ≡
bool IntSequence::operator≡(const IntSequence &s) const
{
    if (size() ≠ s.size()) return false;
    int i = 0;
    while (i < size() ∧ operator[](i) ≡ s[i]) i++;
    return i ≡ size();
}

```

This code is used in section 55.

62. We need some linear irreflexive ordering, we implement it as lexicographic ordering without identity.

```

< IntSequence::operator < code 62 > ≡
bool IntSequence::operator < (const IntSequence &s) const
{
    int len = min(size(), s.size());
    int i = 0;
    while (i < len ∧ operator[](i) ≡ s[i]) i++;
    return (i < s.size() ∧ (i ≡ size() ∨ operator[](i) < s[i]));
}

```

This code is used in section 55.

63.

```

<IntSequence::lessEq code 63> ≡
bool IntSequence::lessEq(const IntSequence &s) const
{
    TL_RAISE_IF(size() ≠ s.size(), "Sequence_with_different_lengths_in_IntSequence::lessEq");
    int i = 0;
    while (i < size() ∧ operator[](i) ≤ s[i]) i++;
    return (i ≡ size());
}

```

This code is used in section 55.

64.

```

<IntSequence::less code 64> ≡
bool IntSequence::less(const IntSequence &s) const
{
    TL_RAISE_IF(size() ≠ s.size(), "Sequence_with_different_lengths_in_IntSequence::less");
    int i = 0;
    while (i < size() ∧ operator[](i) < s[i]) i++;
    return (i ≡ size());
}

```

This code is used in section 55.

65. This is a bubble sort, all sequences are usually very short, so this sin might be forgiven.

```

<IntSequence::sort code 65> ≡
void IntSequence::sort()
{
    for (int i = 0; i < length; i++) {
        int swaps = 0;
        for (int j = 0; j < length - 1; j++) {
            if (data[j] > data[j + 1]) {
                int s = data[j + 1];
                data[j + 1] = data[j];
                data[j] = s;
                swaps++;
            }
        }
        if (swaps ≡ 0) return;
    }
}

```

This code is used in section 55.

66. Here we monotonize the sequence. If an item is less then its predecessor, it is equalized.

```

<IntSequence::monotone code 66> ≡
void IntSequence::monotone()
{
    for (int i = 1; i < length; i++)
        if (data[i - 1] > data[i])
            data[i] = data[i - 1];
}

```

This code is used in section 55.

**67.** This partially monotones the sequence. The partitioning is done by a symmetry. So the subsequence given by the symmetry classes are monotonized. For example, if the symmetry is  $y^2u^3$ , and the **IntSequence** is (5, 3, 1, 6, 4), the result is (5, 5, 1, 6, 6).

```

< IntSequence::pmonotone code 67 > ≡
void IntSequence::pmonotone(const Symmetry &s)
{
    int cum = 0;
    for (int i = 0; i < s.num(); i++) {
        for (int j = cum + 1; j < cum + s[i]; j++)
            if (data[j - 1] > data[j])
                data[j] = data[j - 1];
        cum += s[i];
    }
}

```

This code is used in section 55.

**68.** This returns sum of all elements. Useful for symmetries.

```

< IntSequence::sum code 68 > ≡
int IntSequence::sum() const
{
    int res = 0;
    for (int i = 0; i < length; i++)
        res += operator[](i);
    return res;
}

```

This code is used in section 55.

**69.** This returns product of subsequent items. Useful for Kronecker product dimensions.

```

< IntSequence::mult code 69 > ≡
int IntSequence::mult(int i1, int i2) const
{
    int res = 1;
    for (int i = i1; i < i2; i++)
        res *= operator[](i);
    return res;
}

```

This code is used in section 55.

**70.** Return a number of the same items in the beginning of the sequence.

```

< IntSequence::getPrefixLength code 70 > ≡
int IntSequence::getPrefixLength() const
{
    int i = 0;
    while (i + 1 < size() & operator[](i + 1) == operator[](0)) i++;
    return i + 1;
}

```

This code is used in section 55.

**71.** This returns a number of distinct items in the sequence. It supposes that the sequence is ordered. For the empty sequence it returns zero.

```

< IntSequence::getNumDistinct code 71 > ≡
  int IntSequence::getNumDistinct() const
  {
    int res = 0;
    if (size() > 0) res++;
    for (int i = 1; i < size(); i++)
      if (operator[](i) ≠ operator[] (i - 1)) res++;
    return res;
  }

```

This code is used in section 55.

**72.** This returns a maximum of the sequence. If the sequence is empty, it returns the least possible **int** value.

```

< IntSequence::getMax code 72 > ≡
  int IntSequence::getMax() const
  {
    int res = INT_MIN;
    for (int i = 0; i < size(); i++)
      if (operator[](i) > res) res = operator[](i);
    return res;
  }

```

This code is used in section 55.

**73.**

```

< IntSequence::add code 1 73 > ≡
  void IntSequence::add(int i)
  {
    for (int j = 0; j < size(); j++) operator[](j) += i;
  }

```

This code is used in section 55.

**74.**

```

< IntSequence::add code 2 74 > ≡
  void IntSequence::add(int f, const IntSequence &s)
  {
    TL_RAISE_IF(size() ≠ s.size(), "Wrong sequence length in IntSequence::add");
    for (int j = 0; j < size(); j++) operator[](j) += f * s[j];
  }

```

This code is used in section 55.



75.

```

<IntSequence::isPositive code 75> ≡
bool IntSequence::isPositive() const
{
    int i = 0;
    while (i < size() ∧ operator[](i) ≥ 0) i++;
    return (i ≡ size());
}

```

This code is used in section 55.

76.

```

<IntSequence::isConstant code 76> ≡
bool IntSequence::isConstant() const
{
    bool res = true;
    int i = 1;
    while (res ∧ i < size()) {
        res = res ∧ operator[](0) ≡ operator[](i);
        i++;
    }
    return res;
}

```

This code is used in section 55.

77.

```

<IntSequence::isSorted code 77> ≡
bool IntSequence::isSorted() const
{
    bool res = true;
    int i = 1;
    while (res ∧ i < size()) {
        res = res ∧ operator[](i - 1) ≤ operator[](i);
        i++;
    }
    return res;
}

```

This code is used in section 55.

78. Debug print.

```

<IntSequence::print code 78> ≡
void IntSequence::print() const
{
    printf("[");
    for (int i = 0; i < size(); i++)
        printf("%2d□", operator[](i));
    printf("]\n");
}

```

This code is used in section 55.

79. End of int\_sequence.cpp file.

**80. Matrix interface.** Start of `twod_matrix.h` file.

Here we make an interface to 2-dimensional matrix defined in the Sylvester module. That abstraction provides an interface to BLAS. The main purpose of this file is to only make its subclass in order to keep the tensor library and Sylvester module independent. So here is mainly renaming of methods.

Similarly as in the Sylvester module we declare two classes **TwoDMatrix** and **ConstTwoDMatrix**. The only purpose of the latter is to allow submatrix construction from const reference arguments.

```
#ifndef TWOD_MATRIX_H
#define TWOD_MATRIX_H
#include "GeneralMatrix.h"
#include <cstdio>
#include <matio.h>
class TwoDMatrix;
    (ConstTwoDMatrix class declaration 81);
    (TwoDMatrix class declaration 82);
#endif
```

**81.** We make two obvious constructors, and then a constructor making submatrix of subsequent columns. We also rename **GeneralMatrix::numRows()** and **GeneralMatrix::numCols()**.

```
(ConstTwoDMatrix class declaration 81) ≡
class ConstTwoDMatrix : public ConstGeneralMatrix {
public:
    ConstTwoDMatrix(int m, int n, const double *d)
    : ConstGeneralMatrix(d, m, n) {}

    ConstTwoDMatrix(const TwoDMatrix &m);
    ConstTwoDMatrix(const TwoDMatrix &m, int first_col, int num);
    ConstTwoDMatrix(const ConstTwoDMatrix &m, int first_col, int num);
    ConstTwoDMatrix(int first_row, int num, const TwoDMatrix &m);
    ConstTwoDMatrix(int first_row, int num, const ConstTwoDMatrix &m);
    ConstTwoDMatrix(const ConstTwoDMatrix &m, int first_row, int first_col, int rows, int cols)
    : ConstGeneralMatrix(m, first_row, first_col, rows, cols) {}
    virtual ~ConstTwoDMatrix() {}

    int n_rows() const
    { return numRows(); }
    int n_cols() const
    { return numCols(); }
    void writeMat(mat_t *fd, const char *vname) const;
};
```

This code is cited in section 1.

This code is used in section 80.

**82.** Here we do the same as for **ConstTwoDMatrix** plus define methods for copying and adding rows and columns.

Also we have *save* method which dumps the matrix to a file with a given name. The file can be read by Scilab **fscanfMat** function.

```

< TwoDMatrix class declaration 82 > ≡
class TwoDMatrix : public GeneralMatrix {
public:
    TwoDMatrix(int r, int c)
    : GeneralMatrix(r, c) {}

    TwoDMatrix(int r, int c, double *d)
    : GeneralMatrix(d, r, c) {}

    TwoDMatrix(int r, int c, const double *d)
    : GeneralMatrix(d, r, c) {}

    TwoDMatrix(const GeneralMatrix &m)
    : GeneralMatrix(m) {}

    TwoDMatrix(const GeneralMatrix &m, const char *dummy)
    : GeneralMatrix(m, dummy) {}

    TwoDMatrix(const TwoDMatrix &m, int first_col, int num)
    : GeneralMatrix(m, 0, first_col, m.numRows(), num) {}

    TwoDMatrix(TwoDMatrix &m, int first_col, int num)
    : GeneralMatrix(m, 0, first_col, m.numRows(), num) {}

    TwoDMatrix(int first_row, int num, const TwoDMatrix &m)
    : GeneralMatrix(m, first_row, 0, num, m.ncols()) {}

    TwoDMatrix(int first_row, int num, TwoDMatrix &m)
    : GeneralMatrix(m, first_row, 0, num, m.ncols()) {}

    TwoDMatrix(TwoDMatrix &m, int first_row, int first_col, int rows, int cols)
    : GeneralMatrix(m, first_row, first_col, rows, cols) {}

    TwoDMatrix(const TwoDMatrix &m, int first_row, int first_col, int rows, int cols)
    : GeneralMatrix(m, first_row, first_col, rows, cols) {}

    TwoDMatrix(const ConstTwoDMatrix &a, const ConstTwoDMatrix &b)
    : GeneralMatrix(a, b) {}

    virtual ~TwoDMatrix() {}

    int nRows() const
    { return numRows(); }

    int nCols() const
    { return numCols(); }

    < TwoDMatrix row methods declarations 83 >;
    < TwoDMatrix column methods declarations 84 >;

    void save(const char *fname) const;

    void writeMat(mat_t *fd, const char *vname) const
    { ConstTwoDMatrix(*this).writeMat(fd, vname); }
};

```

This code is cited in section 1.

This code is used in section 80.

83.

```

< TwoDMatrix row methods declarations 83 > ≡
    void copyRow(int from, int to);
    void copyRow(const ConstTwoDMatrix &m, int from, int to);
    void copyRow(const TwoDMatrix &m, int from, int to)
    { copyRow(ConstTwoDMatrix(m), from, to); }
    void addRow(const ConstTwoDMatrix &m, int from, int to)
    { addRow(1.0, m, from, to); }
    void addRow(const TwoDMatrix &m, int from, int to)
    { addRow(1.0, ConstTwoDMatrix(m), from, to); }
    void addRow(double d, const ConstTwoDMatrix &m, int from, int to);
    void addRow(double d, const TwoDMatrix &m, int from, int to)
    { addRow(d, ConstTwoDMatrix(m), from, to); }

```

This code is used in section 82.

84.

```

< TwoDMatrix column methods declarations 84 > ≡
    void copyColumn(int from, int to);
    void copyColumn(const ConstTwoDMatrix &m, int from, int to);
    void copyColumn(const TwoDMatrix &m, int from, int to)
    { copyColumn(ConstTwoDMatrix(m), from, to); }
    void addColumn(const ConstTwoDMatrix &m, int from, int to)
    { addColumn(1.0, ConstTwoDMatrix(m), from, to); }
    void addColumn(const TwoDMatrix &m, int from, int to)
    { addColumn(1.0, ConstTwoDMatrix(m), from, to); }
    void addColumn(double d, const ConstTwoDMatrix &m, int from, int to);
    void addColumn(double d, const TwoDMatrix &m, int from, int to)
    { addColumn(d, ConstTwoDMatrix(m), from, to); }

```

This code is used in section 82.

85. End of twod\_matrix.h file.

86. Start of twod\_matrix.cpp file.

```

#include "twod_matrix.h"
#include "tl_exception.h"
    < ConstTwoDMatrix constructors 87 >;
    < ConstTwoDMatrix::writeMat code 88 >;
    < TwoDMatrix row methods code 89 >;
    < TwoDMatrix column methods code 90 >;
    < TwoDMatrix::save code 91 >;

```

87.

```

< ConstTwoDMatrix constructors 87 > ≡
  ConstTwoDMatrix::ConstTwoDMatrix(const TwoDMatrix &m)
  : ConstGeneralMatrix(m) {}
  ConstTwoDMatrix::ConstTwoDMatrix(const TwoDMatrix &m, int first_col, int num)
  : ConstGeneralMatrix(m, 0, first_col, m.nrows(), num) {}
  ConstTwoDMatrix::ConstTwoDMatrix(const ConstTwoDMatrix &m, int first_col, int num)
  : ConstGeneralMatrix(m, 0, first_col, m.nrows(), num) {}
  ConstTwoDMatrix::ConstTwoDMatrix(int first_row, int num, const TwoDMatrix &m)
  : ConstGeneralMatrix(m, first_row, 0, num, m.ncols()) {}
  ConstTwoDMatrix::ConstTwoDMatrix(int first_row, int num, const ConstTwoDMatrix &m)
  : ConstGeneralMatrix(m, first_row, 0, num, m.ncols()) {}

```

This code is used in section 86.

88.

```

< ConstTwoDMatrix::writeMat code 88 > ≡
  void ConstTwoDMatrix::writeMat(mat_t *fd, const char *vname) const
  {
  #if MATIO_MAJOR_VERSION > 1 ∨ (MATIO_MAJOR_VERSION ≡ 1 ∧ MATIO_MINOR_VERSION ≥ 5)
    size_t dims[2];
    const matio_compression compression = MAT_COMPRESSION_NONE;
  #else
    int dims[2];
    const int compression = COMPRESSION_NONE;
  #endif

    dims[0] = nrows();
    dims[1] = ncols();
    double *data = new double[nrows() * ncols()];
    for (int j = 0; j < ncols(); j++)
      for (int i = 0; i < nrows(); i++) data[j * nrows() + i] = get(i, j);
    matvar_t *v = Mat_VarCreate(vname, MAT_C_DOUBLE, MAT_T_DOUBLE, 2, dims, data, 0);
    Mat_VarWrite(fd, v, compression);
    Mat_VarFree(v);
    delete[] data;
  }

```

This code is used in section 86.

89.

```

< TwoDMatrix row methods code 89 > ≡
  void TwoDMatrix::copyRow(int from, int to)
  {
    if (from ≠ to) copyRow(ConstTwoDMatrix(*this), from, to);
  }
  void TwoDMatrix::copyRow(const ConstTwoDMatrix &m, int from, int to)
  {
    ConstVector fr_row(from, m);
    Vector to_row(to, *this);
    to_row = fr_row;
  }
  void TwoDMatrix::addRow(double d, const ConstTwoDMatrix &m, int from, int to)
  {
    ConstVector fr_row(from, m);
    Vector to_row(to, *this);
    to_row.add(d, fr_row);
  }

```

This code is used in section 86.

90.

```

< TwoDMatrix column methods code 90 > ≡
  void TwoDMatrix::copyColumn(int from, int to)
  {
    if (from ≠ to) copyColumn(ConstTwoDMatrix(*this), from, to);
  }
  void TwoDMatrix::copyColumn(const ConstTwoDMatrix &m, int from, int to)
  {
    ConstVector fr_col(m, from);
    Vector to_col(*this, to);
    to_col = fr_col;
  }
  void TwoDMatrix::addColumn(double d, const ConstTwoDMatrix &m, int from, int to)
  {
    ConstVector fr_col(m, from);
    Vector to_col(*this, to);
    to_col.add(d, fr_col);
  }

```

This code is used in section 86.

91.

```

<TwoDMatrix::save code 91> ≡
void TwoDMatrix::save(const char *fname) const
{
    FILE *fd;
    if (Λ ≡ (fd = fopen(fname, "w"))) {
        TL_RAISE("Cannot open file for writing in TwoDMatrix::save");
    }
    for (int row = 0; row < nrows(); row++) {
        for (int col = 0; col < ncols(); col++) fprintf(fd, "%20.10g", get(row, col));
        fprintf(fd, "\n");
    }
    fclose(fd);
}

```

This code is used in section 86.

92. End of `twod_matrix.cpp` file.93. **Kronecker product.** Start of `kron_prod.h` file.

Here we define an abstraction for a Kronecker product of a sequence of matrices. This is  $A_1 \otimes \dots \otimes A_n$ . Obviously we do not store the product in memory. First we need to represent a dimension of the Kronecker product. Then we represent the Kronecker product, simply it is the Kronecker product dimension with a vector of references to the matrices  $A_1, \dots, A_n$ .

The main task of this class is to calculate a matrix product  $B \cdot (A_1 \otimes A_2 \otimes \dots \otimes A_n)$  which in our application has much more moderate dimensions than  $A_1 \otimes A_2 \otimes \dots \otimes A_n$ . We calculate it as

$$B \cdot (A_1 \otimes I) \cdot \dots \cdot (I \otimes A_i \otimes I) \cdot \dots \cdot (I \otimes A_n)$$

where dimensions of identity matrices differ and are given by the chosen order. One can naturally ask, whether there is some optimal order minimizing maximum storage needed for intermediate results. The optimal ordering is implemented by class **KronProdAllOptim**.

For this multiplication, we also need to represent products of type  $A \otimes I$ ,  $I \otimes A \otimes I$ , and  $I \otimes A$ .

```

#ifndef KRON_PROD_H
#define KRON_PROD_H
#include "twod_matrix.h"
#include "permutation.h"
#include "int_sequence.h"
class KronProdAll;
class KronProdAllOptim;
class KronProdIA;
class KronProdIAI;
class KronProdAI;
<KronProdDimens class declaration 94>;
<KronProd class declaration 98>;
<KronProdAll class declaration 99>;
<KronProdAllOptim class declaration 100>;
<KronProdIA class declaration 101>;
<KronProdAI class declaration 102>;
<KronProdIAI class declaration 103>;
#endif

```

**94.** `KronProdDimens` maintains a dimension of the Kronecker product. So, it maintains two sequences, one for rows, and one for columns.

⟨ `KronProdDimens` class declaration 94 ⟩ ≡

```
class KronProdDimens {
    friend class KronProdAll;
    friend class KronProdAllOptim;
    friend class KronProdIA;
    friend class KronProdIAI;
    friend class KronProdAI;

private:
    IntSequence rows;
    IntSequence cols;

public:
    ⟨ KronProdDimens constructors 95 ⟩;
    ⟨ KronProdDimens inline operators 96 ⟩;
    ⟨ KronProdDimens inline methods 97 ⟩;
};
```

This code is used in section 93.

**95.** We define three constructors. First initializes to a given dimension, and all rows and cols are set to zeros. Second is a copy constructor. The third constructor takes dimensions of  $A_1 \otimes A_2 \otimes \dots \otimes A_n$ , and makes dimensions of  $I \otimes A_i \otimes I$ , or  $I \otimes A_n$ , or  $A_1 \otimes I$  for a given  $i$ . The dimensions of identity matrices are such that

$$A_1 \otimes A_2 \otimes \dots \otimes A_n = (A_1 \otimes I) \cdot \dots \cdot (I \otimes A_i \otimes I) \cdot \dots \cdot (I \otimes A_n)$$

Note that the matrices on the right do not commute only because sizes of identity matrices which are then given by this ordering.

⟨ `KronProdDimens` constructors 95 ⟩ ≡

```
KronProdDimens(int dim)
: rows(dim, 0), cols(dim, 0) {}

KronProdDimens(const KronProdDimens &kd)
: rows(kd.rows), cols(kd.cols) {}

KronProdDimens(const KronProdDimens &kd, int i);
```

This code is used in section 94.

**96.**

⟨ `KronProdDimens` inline operators 96 ⟩ ≡

```
const KronProdDimens &operator=(const KronProdDimens &kd)
{ rows = kd.rows; cols = kd.cols; return *this; }

bool operator==(const KronProdDimens &kd) const
{ return rows == kd.rows & cols == kd.cols; }
```

This code is used in section 94.



97.

⟨ **KronProdDimens** inline methods 97 ⟩ ≡

```

int dimen() const
{ return rows.size(); }

void setRC(int i, int r, int c)
{ rows[i] = r; cols[i] = c; }

void getRC(int i, int &r, int &c) const
{ r = rows[i]; c = cols[i]; }

void getRC(int &r, int &c) const
{ r = rows.mult(); c = cols.mult(); }

int nrows() const
{ return rows.mult(); }

int ncols() const
{ return cols.mult(); }

int nrows(int i) const
{ return rows[i]; }

int ncols(int i) const
{ return cols[i]; }

```

This code is used in section 94.

**98.** Here we define an abstract class for all Kronecker product classes, which are **KronProdAll** (the most general), **KronProdIA** (for  $I \otimes A$ ), **KronProdAI** (for  $A \otimes I$ ), and **KronProdIAI** (for  $I \otimes A \otimes I$ ). The purpose of the super class is to only define some common methods and common member *kpd* for dimensions and declare pure virtual *mult* which is implemented by the subclasses.

The class also contains a static method *kronMult*, which calculates a Kronecker product of two vectors and stores it in the provided vector. It is useful at a few points of the library.

⟨ **KronProd** class declaration 98 ⟩ ≡

```
class KronProd {
protected:
    KronProdDimens kpd;
public:
    KronProd(int dim)
    : kpd(dim) {}

    KronProd(const KronProdDimens &kd)
    : kpd(kd) {}

    KronProd(const KronProd &kp)
    : kpd(kp.kpd) {}

    virtual ~KronProd() {}

    int dimen() const
    { return kpd.dimen(); }

    virtual void mult(const ConstTwoDMatrix &in, TwoDMatrix &out) const = 0;
    void mult(const TwoDMatrix &in, TwoDMatrix &out) const
    { mult(ConstTwoDMatrix(in), out); }

    void checkDimForMult(const ConstTwoDMatrix &in, const TwoDMatrix &out) const;
    void checkDimForMult(const TwoDMatrix &in, const TwoDMatrix &out) const
    { checkDimForMult(ConstTwoDMatrix(in), out); }

    static void kronMult(const ConstVector &v1, const ConstVector &v2, Vector &res);

    int nrows() const
    { return kpd.nrows(); }

    int ncols() const
    { return kpd.ncols(); }

    int nrows(int i) const
    { return kpd.nrows(i); }

    int ncols(int i) const
    { return kpd.ncols(i); }
};
```

This code is used in section 93.

**99.** **KronProdAll** is a main class of this file. It represents the Kronecker product  $A_1 \otimes A_2 \otimes \dots \otimes A_n$ . Besides dimensions, it stores pointers to matrices in *matlist* array. If a pointer is null, then the matrix is considered to be unit. The array is set by calls to *setMat* method (for real matrices) or *setUnit* method (for unit matrices).

The object is constructed by a constructor, which allocates the *matlist* and initializes dimensions to zeros. Then a caller must feed the object with matrices by calling *setMat* and *setUnit* repeatedly for different indices.

We implement the *mult* method of **KronProd**, and a new method *multRows*, which creates a vector of kronecker product of all rows of matrices in the object. The rows are given by the **IntSequence**.

⟨ **KronProdAll** class declaration 99 ⟩  $\equiv$

```
class KronProdAll : public KronProd {
    friend class KronProdIA;
    friend class KronProdIAI;
    friend class KronProdAI;
protected:
    const TwoDMatrix **const matlist;
public:
    KronProdAll(int dim)
    : KronProd(dim), matlist(new const TwoDMatrix*[dim]) {}
    virtual ~KronProdAll()
    { delete[] matlist; }
    void setMat(int i, const TwoDMatrix &m);
    void setUnit(int i, int n);
    const TwoDMatrix &getMat(int i) const
    { return *(matlist[i]); }
    void mult(const ConstTwoDMatrix &in, TwoDMatrix &out) const;
    Vector *multRows(const IntSequence &irows) const;
private:
    bool isUnit() const;
};
```

This code is cited in section 1.

This code is used in section 93.

**100.** The class **KronProdAllOptim** minimizes memory consumption of the product  $B \cdot (A_1 \otimes A_2 \otimes \dots \otimes A_k)$ . The optimization is done by reordering of the matrices  $A_1, \dots, A_k$ , in order to minimize a sum of all storages needed for intermediate results. The optimal ordering is also nearly optimal with respect to number of flops.

Let  $(m_i, n_i)$  be dimensions of  $A_i$ . It is easy to observe, that for  $i$ -th step we need storage of  $r \cdot n_1 \cdot \dots \cdot n_i \cdot m_{i+1} \cdot \dots \cdot m_k$ , where  $r$  is a number of rows of  $B$ . To minimize the sum through all  $i$  over all permutations of matrices, it is equivalent to minimize the sum  $\sum_{i=1}^k \frac{m_{i+1} \cdot \dots \cdot m_k}{n_{i+1} \cdot \dots \cdot n_k}$ . The optimal ordering will yield  $\frac{m_k}{n_k} \leq \frac{m_{k-1}}{n_{k-1}} \leq \dots \leq \frac{m_1}{n_1}$ .

Now observe, that the number of flops for  $i$ -th step is  $r \cdot n_1 \cdot \dots \cdot n_i \cdot m_i \cdot \dots \cdot m_k$ . In order to minimize a number of flops, it is equivalent to minimize  $\sum_{i=1}^k m_i \frac{m_{i+1} \cdot \dots \cdot m_k}{n_{i+1} \cdot \dots \cdot n_k}$ . Note that, normally, the  $m_i$  does not change as much as  $n_{j+1}, \dots, n_k$ , so the ordering minimizing the memory will be nearly optimal with respect to number of flops.

The class **KronProdAllOptim** inherits from **KronProdAll**. A public method *optimizeOrder* does the reordering. The permutation is stored in *oper*. So, as long as *optimizeOrder* is not called, the class is equivalent to **KronProdAll**.

```
<KronProdAllOptim class declaration 100> ≡
class KronProdAllOptim : public KronProdAll {
protected:
    Permutation oper;
public:
    KronProdAllOptim(int dim)
    : KronProdAll(dim), oper(dim) {}
    void optimizeOrder();
    const Permutation &getPer() const
    { return oper; }
};
```

This code is cited in sections 1 and 129.

This code is used in section 93.

**101.** This class represents  $I \otimes A$ . We have only one reference to the matrix, which is set by constructor.

```
<KronProdIA class declaration 101> ≡
class KronProdIA : public KronProd {
    friend class KronProdAll;
    const TwoDMatrix &mat;
public:
    KronProdIA(const KronProdAll &kpa)
    : KronProd(KronProdDimens(kpa.kpd, kpa.dimen() - 1)), mat(kpa.getMat(kpa.dimen() - 1)) {}
    void mult(const ConstTwoDMatrix &in, TwoDMatrix &out) const;
};
```

This code is used in section 93.

**102.** This class represents  $A \otimes I$ . We have only one reference to the matrix, which is set by constructor.

```
< KronProdAI class declaration 102 > ≡
class KronProdAI : public KronProd {
    friend class KronProdIAI;
    friend class KronProdAll;
    const TwoDMatrix &mat;

public:
    KronProdAI(const KronProdAll &kpa)
    : KronProd(KronProdDimens(kpa.kpd, 0)), mat(kpa.getMat(0)) { }
    KronProdAI(const KronProdIAI &kpiai);
    void mult(const ConstTwoDMatrix &in, TwoDMatrix &out) const;
};
```

This code is used in section 93.

**103.** This class represents  $I \otimes A \otimes I$ . We have only one reference to the matrix, which is set by constructor.

```
< KronProdIAI class declaration 103 > ≡
class KronProdIAI : public KronProd {
    friend class KronProdAI;
    friend class KronProdAll;
    const TwoDMatrix &mat;

public:
    KronProdIAI(const KronProdAll &kpa, int i)
    : KronProd(KronProdDimens(kpa.kpd, i)), mat(kpa.getMat(i)) { }
    void mult(const ConstTwoDMatrix &in, TwoDMatrix &out) const;
};
```

This code is used in section 93.

**104.** End of `kron_prod.h` file.

**105.** Start of `kron_prod.cpp` file.

```
#include "kron_prod.h"
#include "tl_exception.h"
#include <cstdio>
< KronProdDimens constructor code 106 >;
< KronProd::checkDimForMult code 110 >;
< KronProd::kronMult code 111 >;
< KronProdAll::setMat code 112 >;
< KronProdAll::setUnit code 113 >;
< KronProdAll::isUnit code 114 >;
< KronProdAll::multRows code 126 >;
< KronProdIAI::mult code 115 >;
< KronProdAI constructor code 116 >;
< KronProdAI::mult code 117 >;
< KronProdIAI::mult code 118 >;
< KronProdAll::mult code 119 >;
< KronProdAllOptim::optimizeOrder code 129 >;
```

**106.** Here we construct Kronecker product dimensions from Kronecker product dimensions by picking a given matrix and all other set to identity. The constructor takes dimensions of  $A_1 \otimes A_2 \otimes \dots \otimes A_n$ , and makes dimensions of  $I \otimes A_i \otimes I$ , or  $I \otimes A_n$ , or  $A_1 \otimes I$  for a given  $i$ . The identity matrices must fit into the described order. See header file.

We first decide what is a length of the resulting dimensions. Possible length is three for  $I \otimes A \otimes I$ , and two for  $I \otimes A$ , or  $A \otimes I$ .

Then we fork according to  $i$ .

```

< KronProdDimens constructor code 106 > ≡
KronProdDimens::KronProdDimens(const KronProdDimens &kd, int i)
: rows((i ≡ 0 ∨ i ≡ kd.dimen() - 1) ? (2) : (3)), cols((i ≡ 0 ∨ i ≡ kd.dimen() - 1) ? (2) : (3)) {
    TL_RAISE_IF(i < 0 ∨ i ≥ kd.dimen(), "Wrong_index_for_pickup_in_KronProdDimens_constructor");
    int kdim = kd.dimen();
    if (i ≡ 0) {
        < set AI dimensions 107 >;
    }
    else if (i ≡ kdim - 1) {
        < set IA dimensions 108 >;
    }
    else {
        < set IAI dimensions 109 >;
    }
}

```

This code is used in section 105.

**107.** The first rows and cols are taken from  $kd$ . The dimensions of identity matrix is a number of rows in  $A_2 \otimes \dots \otimes A_n$  since the matrix  $A_1 \otimes I$  is the first.

```

< set AI dimensions 107 > ≡
    rows[0] = kd.rows[0];
    rows[1] = kd.rows.mult(1, kdim);
    cols[0] = kd.cols[0];
    cols[1] = rows[1];

```

This code is used in section 106.

**108.** The second dimension is taken from  $kd$ . The dimensions of identity matrix is a number of columns of  $A_1 \otimes \dots \otimes A_{n-1}$ , since the matrix  $I \otimes A_n$  is the last.

```

< set IA dimensions 108 > ≡
    rows[0] = kd.cols.mult(0, kdim - 1);
    rows[1] = kd.rows[kdim - 1];
    cols[0] = rows[0];
    cols[1] = kd.cols[kdim - 1];

```

This code is used in section 106.

**109.** The dimensions of the middle matrix are taken from  $kd$ . The dimensions of the first identity matrix are a number of columns of  $A_1 \otimes \dots \otimes A_{i-1}$ , and the dimensions of the last identity matrix are a number of rows of  $A_{i+1} \otimes \dots \otimes A_n$ .

```
<set IAI dimensions 109> ≡
  rows[0] = kd.cols.mult(0, i);
  cols[0] = rows[0];
  rows[1] = kd.rows[i];
  cols[1] = kd.cols[i];
  cols[2] = kd.rows.mult(i + 1, kdim);
  rows[2] = cols[2];
```

This code is used in section 106.

**110.** This raises an exception if dimensions are bad for multiplication  $out = in * this$ .

```
<KronProd::checkDimForMult code 110> ≡
  void KronProd::checkDimForMult(const ConstTwoDMatrix &in, const TwoDMatrix &out)
    const
  {
    int my_rows;
    int my_cols;
    kpd.getRC(my_rows, my_cols);
    TL_RAISE_IF(in.nrows() ≠ out.nrows() ∨ in.ncols() ≠ my_rows,
      "Wrong_dimensions_for_KronProd_in_KronProd::checkDimForMult");
  }
```

This code is used in section 105.

**111.** Here we Kronecker multiply two given vectors  $v1$  and  $v2$  and store the result in preallocated  $res$ .

```
<KronProd::kronMult code 111> ≡
  void KronProd::kronMult(const ConstVector &v1, const ConstVector &v2, Vector &res)
  {
    TL_RAISE_IF(res.length() ≠ v1.length() * v2.length(),
      "Wrong_vector_lengths_in_KronProd::kronMult");
    res.zeros();
    for (int i = 0; i < v1.length(); i++) {
      Vector sub(res, i * v2.length(), v2.length());
      sub.add(v1[i], v2);
    }
  }
```

This code is used in section 105.

**112.**

```
<KronProdAll::setMat code 112> ≡
  void KronProdAll::setMat(int i, const TwoDMatrix &m)
  {
    matlist[i] = &m;
    kpd.setRC(i, m.nrows(), m.ncols());
  }
```

This code is used in section 105.

113.

```

⟨ KronProdAll::setUnit code 113 ⟩ ≡
void KronProdAll::setUnit(int i, int n)
{
    matlist[i] =  $\Lambda$ ;
    kpd.setRC(i, n, n);
}

```

This code is used in section 105.

114.

```

⟨ KronProdAll::isUnit code 114 ⟩ ≡
bool KronProdAll::isUnit() const
{
    int i = 0;
    while (i < dimen() ∧ matlist[i] ≡  $\Lambda$ ) i++;
    return i ≡ dimen();
}

```

This code is used in section 105.

115. Here we multiply  $B \cdot (I \otimes A)$ . If  $m$  is a dimension of the identity matrix, then the product is equal to  $B \cdot \text{diag}_m(A)$ . If  $B$  is partitioned accordingly, then the result is  $[B_1A, B_2A, \dots B_mA]$ .

Here, *out<sub>i</sub>* are partitions of *out*, *ini* are const partitions of *in*, and *id\_cols* is  $m$ . We employ level-2 BLAS.

```

⟨ KronProdIA::mult code 115 ⟩ ≡
void KronProdIA::mult(const ConstTwoDMatrix &in, TwoDMatrix &out) const
{
    checkDimForMult(in, out);
    int id_cols = kpd.cols[0];
    ConstTwoDMatrix a(mat);
    for (int i = 0; i < id_cols; i++) {
        TwoDMatrix outi(out, i * a.ncols(), a.ncols());
        ConstTwoDMatrix ini(in, i * a.nrows(), a.nrows());
        outi.mult(ini, a);
    }
}

```

This code is used in section 105.

116. Here we construct **KronProdAI** from **KronProdIAI**. It is clear.

```

⟨ KronProdAI constructor code 116 ⟩ ≡
KronProdAI::KronProdAI(const KronProdIAI &kpiai)
: KronProd(KronProdDimens(2)), mat(kpiai.mat) {
    kpd.rows[0] = mat.nrows();
    kpd.cols[0] = mat.ncols();
    kpd.rows[1] = kpiai.kpd.rows[2];
    kpd.cols[1] = kpiai.kpd.cols[2];
}

```

This code is used in section 105.



**117.** Here we multiply  $B \cdot (A \otimes I)$ . Let the dimension of the matrix  $A$  be  $m \times n$ , the dimension of  $I$  be  $p$ , and a number of rows of  $B$  be  $q$ . We use the fact that  $B \cdot (A \otimes I) = \text{reshape}(\text{reshape}(B, q, mp) \cdot A, q, np)$ . This works only for matrix  $B$ , whose storage has leading dimension equal to number of rows.

For cases where the leading dimension is not equal to the number of rows, we partition the matrix  $A \otimes I$  to  $m \times n$  square partitions  $a_{ij}I$ . Therefore, we partition  $B$  to  $m$  partitions  $[B_1, B_2, \dots, B_m]$ . Each partition of  $B$  has the same number of columns as the identity matrix. If  $R$  denotes the resulting matrix, then it can be partitioned to  $n$  partitions  $[R_1, R_2, \dots, R_n]$ . Each partition of  $R$  has the same number of columns as the identity matrix. Then we have  $R_i = \sum a_{ji}B_j$ .

In code, *out<sub>i</sub>* is  $R_i$ , *ini* is  $B_j$ , and *id\_cols* is a dimension of the identity matrix

```
<KronProdAI::mult code 117> ≡
void KronProdAI::mult(const ConstTwoDMatrix &in, TwoDMatrix &out) const
{
    checkDimForMult(in, out);
    int id_cols = kpd.cols[1];
    ConstTwoDMatrix a(mat);
    if (in.getLD() == in.nrows()) {
        ConstTwoDMatrix in_resh(in.nrows() * id_cols, a.nrows(), in.getData().base());
        TwoDMatrix out_resh(in.nrows() * id_cols, a.ncols(), out.getData().base());
        out_resh.mult(in_resh, a);
    }
    else {
        out.zeros();
        for (int i = 0; i < a.ncols(); i++) {
            TwoDMatrix outi(out, i * id_cols, id_cols);
            for (int j = 0; j < a.nrows(); j++) {
                ConstTwoDMatrix ini(in, j * id_cols, id_cols);
                outi.add(a.get(j, i), ini);
            }
        }
    }
}
```

This code is used in section 105.

**118.** Here we multiply  $B \cdot (I \otimes A \otimes I)$ . If  $n$  is a dimension of the first identity matrix, then we multiply  $B \cdot \text{diag}_n(A \otimes I)$ . So we partition  $B$  and result  $R$  accordingly, and multiply  $B_i \cdot (A \otimes I)$ , which is in fact **KronProdAI::mult**. Note that number of columns of partitions of  $B$  are number of rows of  $A \otimes I$ , and number of columns of  $R$  are number of columns of  $A \otimes I$ .

In code, *id\_cols* is  $n$ , *akronid* is a Kronecker product object of  $A \otimes I$ , and *in\_bl\_width*, and *out\_bl\_width* are rows and cols of  $A \otimes I$ .

```

< KronProdIAI::mult code 118 > ≡
void KronProdIAI::mult(const ConstTwoDMatrix &in, TwoDMatrix &out) const
{
    checkDimForMult(in, out);
    int id_cols = kpd.cols[0];
    KronProdAI akronid(*this);
    int in_bl_width;
    int out_bl_width;
    akronid.kpd.getRC(in_bl_width, out_bl_width);
    for (int i = 0; i < id_cols; i++) {
        TwoDMatrix outi(out, i * out_bl_width, out_bl_width);
        ConstTwoDMatrix ini(in, i * in_bl_width, in_bl_width);
        akronid.mult(ini, outi);
    }
}

```

This code is used in section 105.

**119.** Here we multiply  $B \cdot (A_1 \otimes \dots \otimes A_n)$ . First we multiply  $B \cdot (A_1 \otimes)$ , then this is multiplied by all  $I \otimes A_i \otimes I$ , and finally by  $I \otimes A_n$ .

If the dimension of the Kronecker product is only 1, then we multiply two matrices in straight way and return.

The intermediate results are stored on heap pointed by *last*. A new result is allocated, and then the former storage is deallocated.

We have to be careful in cases when last or first matrix is unit and no calculations are performed in corresponding codes. The codes should handle *last* safely also if no calcs are done.

```

< KronProdAll::mult code 119 > ≡
void KronProdAll::mult(const ConstTwoDMatrix &in, TwoDMatrix &out) const
{
    < quick copy if product is unit 120 >;
    < quick zero if one of the matrices is zero 121 >;
    < quick multiplication if dimension is 1 122 >;
    int c;
    TwoDMatrix *last = Λ;
    < perform first multiplication AI 123 >;
    < perform intermediate multiplications IAI 124 >;
    < perform last multiplication IA 125 >;
}

```

This code is used in section 105.

**120.**

```

⟨quick copy if product is unit 120⟩ ≡
  if (isUnit()) {
    out.zeros();
    out.add(1.0, in);
    return;
  }

```

This code is used in section 119.

**121.** If one of the matrices is exactly zero or the *in* matrix is zero, set out to zero and return

```

⟨quick zero if one of the matrices is zero 121⟩ ≡
  bool is_zero = false;
  for (int i = 0; i < dimen() & !is_zero; i++) is_zero = matlist[i] & matlist[i]-isZero();
  if (is_zero ∨ in.isZero()) {
    out.zeros();
    return;
  }

```

This code is used in section 119.

**122.**

```

⟨quick multiplication if dimension is 1 122⟩ ≡
  if (dimen() == 1) {
    if (matlist[0]) /* always true */
      out.mult(in, ConstTwoDMatrix(*(matlist[0])));
    return;
  }

```

This code is used in section 119.

**123.** Here we have to construct  $A_1 \otimes I$ , allocate intermediate result *last*, and perform the multiplication.

```

⟨perform first multiplication AI 123⟩ ≡
  if (matlist[0]) {
    KronProdAI akronid(*this);
    c = akronid.kpd.ncols();
    last = new TwoDMatrix(in.nrows(), c);
    akronid.mult(in, *last);
  }
  else {
    last = new TwoDMatrix(in.nrows(), in.ncols(), in.getData().base());
  }

```

This code is used in section 119.

**124.** Here we go through all  $I \otimes A_i \otimes I$ , construct the product, allocate new storage for result *newlast*, perform the multiplication, deallocate old *last*, and set *last* to *newlast*.

```

⟨perform intermediate multiplications IAI 124⟩ ≡
  for (int i = 1; i < dimen() - 1; i++) {
    if (matlist[i]) {
      KronProdIAI interkron(*this, i);
      c = interkron.kpd.ncols();
      TwoDMatrix *newlast = new TwoDMatrix(in.nrows(), c);
      interkron.mult(*last, *newlast);
      delete last;
      last = newlast;
    }
  }

```

This code is used in section 119.

**125.** Here just construct  $I \otimes A_n$  and perform multiplication and deallocate *last*.

```

⟨perform last multiplication IA 125⟩ ≡
  if (matlist[dimen() - 1]) {
    KronProdIA idkrona(*this);
    idkrona.mult(*last, out);
  }
  else {
    out = *last;
  }
  delete last;

```

This code is used in section 119.

**126.** This calculates a Kronecker product of rows of matrices, the row indices are given by the integer sequence. The result is allocated and returned. The caller is responsible for its deallocation.

```

⟨KronProdAll::multRows code 126⟩ ≡
  Vector *KronProdAll::multRows(const IntSequence &irows) const
  {
    TL_RAISE_IF(irows.size() != dimen(),
      "Wrong length of row indices in KronProdAll::multRows");
    Vector *last = Λ;
    ConstVector *row;
    vector<Vector*> to_delete;
    for (int i = 0; i < dimen(); i++) {
      int j = dimen() - 1 - i;
      ⟨set row to the row of j-th matrix 127⟩;
      ⟨set last to product of row and last 128⟩;
      delete row;
    }
    for (unsigned int i = 0; i < to_delete.size(); i++) delete to_delete[i];
    return last;
  }

```

This code is used in section 105.

**127.** If the  $j$ -th matrix is real matrix, then the row is constructed from the matrix. If the matrix is unit, we construct a new vector, fill it with zeros, than set the unit to appropriate place, and make the *row* as **ConstVector** of this vector, which sheduled for deallocation.

```

⟨set row to the row of  $j$ -th matrix 127⟩ ≡
  if (matlist[ $j$ ]) row = new ConstVector(irows[ $j$ ],*(matlist[ $j$ ]));
  else {
    Vector *aux = new Vector(ncols( $j$ ));
    aux-zeros();
    (*aux)[irows[ $j$ ]] = 1.0;
    to_delete.push_back(aux);
    row = new ConstVector(*aux);
  }

```

This code is used in section 126.

**128.** If the *last* is exists, we allocate new storage, Kronecker multiply, deallocate the old storage. If the *last* does not exist, then we only make *last* equal to *row*.

```

⟨set last to product of row and last 128⟩ ≡
  if (last) {
    Vector *newlast;
    newlast = new Vector(last-length() * row-length());
    kronMult(*row, ConstVector(*last), *newlast);
    delete last;
    last = newlast;
  }
  else {
    last = new Vector(*row);
  }

```

This code is used in section 126.

**129.** This permutes the matrices so that the new ordering would minimize memory consumption. As shown in ⟨**KronProdAllOptim** class declaration 100⟩, we want  $\frac{m_k}{n_k} \leq \frac{m_{k-1}}{n_{k-1}} \dots \leq \frac{m_1}{n_1}$ , where  $(m_i, n_i)$  is the dimension of  $A_i$ . So we implement the bubble sort.

```

⟨KronProdAllOptim::optimizeOrder code 129⟩ ≡
  void KronProdAllOptim::optimizeOrder()
  {
    for (int  $i = 0$ ;  $i < \text{dimen}()$ ;  $i++$ ) {
      int swaps = 0;
      for (int  $j = 0$ ;  $j < \text{dimen}() - 1$ ;  $j++$ ) {
        if (((double) kpd.rows[ $j$ ])/kpd.cols[ $j$ ] < ((double) kpd.rows[ $j + 1$ ])/kpd.cols[ $j + 1$ ]) {
          ⟨swap dimensions and matrices at  $j$  and  $j + 1$  130⟩;
          ⟨project the swap to the permutation oper 131⟩;
        }
      }
      if (swaps == 0) {
        return;
      }
    }
  }

```

This code is used in section 105.

**130.**

⟨ swap dimensions and matrices at  $j$  and  $j + 1$  130 ⟩  $\equiv$

```

int  $s = kpd.rows[j + 1]$ ;
 $kpd.rows[j + 1] = kpd.rows[j]$ ;
 $kpd.rows[j] = s$ ;
 $s = kpd.cols[j + 1]$ ;
 $kpd.cols[j + 1] = kpd.cols[j]$ ;
 $kpd.cols[j] = s$ ;
const TwoDMatrix  $*m = matlist[j + 1]$ ;
 $matlist[j + 1] = matlist[j]$ ;
 $matlist[j] = m$ ;

```

This code is used in section 129.

**131.**

⟨ project the swap to the permutation *oper* 131 ⟩  $\equiv$

```

 $s = oper.getMap()[j + 1]$ ;
 $oper.getMap()[j + 1] = oper.getMap()[j]$ ;
 $oper.getMap()[j] = s$ ;
 $swaps++$ ;

```

This code is used in section 129.

**132.** End of `kron_prod.cpp` file.

**133. Combinatorics.****134. Symmetry.** This is `symmetry.h` file

Symmetry is an abstraction for a term of the form  $y^3u^2$ . It manages only indices, not the variable names. So if one uses this abstraction, he must keep in mind that  $y$  is the first, and  $u$  is the second.

In fact, the symmetry is a special case of equivalence, but its implementation is much simpler. We do not need an abstraction for the term  $yyuyu$  but due to Green theorem we can have term  $y^3u^2$ . That is why the equivalence is too general for our purposes.

One of a main purposes of the tensor library is to calculate something like:

$$[B_{y^2u^3}]_{\alpha_1\alpha_2\beta_1\beta_2\beta_3} = [g_{y^l}]_{\gamma_1\dots\gamma_l} \left( \sum_{c \in M_{l,5}} \prod_{m=1}^l [g_{c_m}]_{c_m(\alpha,\beta)}^{\gamma_m} \right)$$

If, for instance,  $l = 3$ , and  $c = \{\{0, 4\}, \{1, 2\}, \{3\}\}$ , then we have to calculate

$$[g_{y^3}]_{\gamma_1\gamma_2\gamma_3} [g_{yu}]_{\alpha_1\beta_3}^{\gamma_1} [g_{yu}]_{\alpha_2\beta_1}^{\gamma_2} [g_u]_{\beta_2}^{\gamma_3}$$

We must be able to calculate a symmetry induced by symmetry  $y^2u^3$  and by an equivalence class from equivalence  $c$ . For equivalence class  $\{0, 4\}$  the induced symmetry is  $yu$ , since we pick first and fifth variable from  $y^2u^3$ . For a given outer symmetry, the class **InducedSymmetries** does this for all classes of a given equivalence.

We need also to cycle through all possible symmetries yielding the given dimension. For this purpose we define classes **SymmetrySet** and **symiterator**.

The symmetry is implemented as **IntSequence**, in fact, it inherits from it.

```
#ifndef SYMMETRY_H
#define SYMMETRY_H
#include "equivalence.h"
#include "int_sequence.h"
#include <list>
#include <vector>
<Symmetry class declaration 135>;
<SymmetrySet class declaration 137>;
<symiterator class declaration 138>;
<InducedSymmetries class declaration 139>;
#endif
```

**135.** Clear. The method *isFull* returns true if and only if the symmetry allows for any permutation of indices.

```

<Symmetry class declaration 135> ≡
class Symmetry : public IntSequence {
public:
    <Symmetry constructors 136>;
    int num() const
    { return size(); }
    int dimen() const
    { return sum(); }
    int findClass(int i) const;
    bool isFull() const;
};

```

This code is cited in section 1.

This code is used in section 134.

**136.** We provide three constructors for symmetries of the form  $y^n, y^n u^m, y^n u^m \sigma^k$ . Also a copy constructor, and finally a constructor of implied symmetry for a symmetry and an equivalence class. It is already implemented in **IntSequence** so we only call appropriate constructor of **IntSequence**. We also provide the subsymmetry, which takes the given length of symmetry from the end.

The last constructor constructs a symmetry from an integer sequence (supposed to be ordered) as a symmetry counting successively equal items. For instance the sequence  $(a, a, a, b, c, c, d, d, d, d)$  produces symmetry  $(3, 1, 2, 4)$ .

```

<Symmetry constructors 136> ≡
Symmetry(int len, const char *dummy)
: IntSequence(len, 0) {}

Symmetry(int i1)
: IntSequence(1, i1) {}

Symmetry(int i1, int i2)
: IntSequence(2) { operator[](0) = i1; operator[](1) = i2; }

Symmetry(int i1, int i2, int i3)
: IntSequence(3) { operator[](0) = i1; operator[](1) = i2; operator[](2) = i3; }

Symmetry(int i1, int i2, int i3, int i4)
: IntSequence(4) { operator[](0) = i1; operator[](1) = i2; operator[](2) = i3;
operator[](3) = i4; }

Symmetry(const Symmetry &s)
: IntSequence(s) {}

Symmetry(const Symmetry &s, const OrdSequence &cl)
: IntSequence(s, cl.getData()) {}

Symmetry(Symmetry &s, int len)
: IntSequence(s, s.size() - len, s.size()) {}

Symmetry(const IntSequence &s);

```

This code is used in section 135.



**137.** The class **SymmetrySet** defines a set of symmetries of the given length having given dimension. It does not store all the symmetries, rather it provides a storage for one symmetry, which is changed as an adjoint iterator moves.

The iterator class is **symiterator**. It is implemented recursively. The iterator object, when created, creates subordinal iterator, which iterates over a symmetry set whose length is one less, and dimension is the former dimension. When the subordinal iterator goes to its end, the superordinal iterator increases left most index in the symmetry, resets the subordinal symmetry set with different dimension, and iterates through the subordinal symmetry set until its end, and so on. That's why we provide also **SymmetrySet** constructor for construction of a subordinal symmetry set.

The typical usage of the abstractions for **SymmetrySet** and **symiterator** is as follows:

```
for (symiterator si(SymmetrySet(6,4)); ¬si.isEnd(); ++si) {body}
```

It goes through all symmetries of size 4 having dimension 6. One can use *\*si* as the symmetry in the body.

⟨**SymmetrySet** class declaration 137⟩ ≡

```
class SymmetrySet {
    Symmetry run;
    int dim;
public:
    SymmetrySet(int d,int length)
    : run(length,""), dim(d) {}
    SymmetrySet(SymmetrySet &s,int d)
    : run(s.run,s.size()-1), dim(d) {}
    int dimen() const
    { return dim; }
    const Symmetry &sym() const
    { return run; }
    Symmetry &sym()
    { return run; }
    int size() const
    { return run.size(); }
};
```

This code is cited in section 138.

This code is used in section 134.

**138.** The logic of **symiterator** was described in  $\langle$ **SymmetrySet** class declaration 137 $\rangle$ . Here we only comment that: the class has a reference to the **SymmetrySet** only to know dimension and for access of its symmetry storage. Further we have pointers to subordinal **symiterator** and its **SymmetrySet**. These are pointers, since the recursion ends at length equal to 2, in which case these pointers are  $\Lambda$ .

The constructor creates the iterator which initializes to the first symmetry (beginning).

$\langle$ **symiterator** class declaration 138 $\rangle \equiv$

```
class symiterator {
    SymmetrySet &s;
    symiterator *subit;
    SymmetrySet *subs;
    bool end_flag;

public:
    symiterator(SymmetrySet &ss);
    ~symiterator();
    symiterator &operator ++();
    bool isEnd() const
    { return end_flag; }
    const Symmetry &operator*() const
    { return s.sym(); }
};
```

This code is used in section 134.

**139.** This simple abstraction just constructs a vector of induced symmetries from the given equivalence and outer symmetry. A permutation might optionally permute the classes of the equivalence.

$\langle$ **InducedSymmetries** class declaration 139 $\rangle \equiv$

```
class InducedSymmetries : public vector<Symmetry> {
public:
    InducedSymmetries(const Equivalence &e, const Symmetry &s);
    InducedSymmetries(const Equivalence &e, const Permutation &p, const Symmetry &s);
    void print() const;
};
```

This code is used in section 134.

**140.** End of **symmetry.h** file.

**141.** Start of **symmetry.cpp** file.

```
#include "symmetry.h"
#include "permutation.h"
#include <cstdio>
<Symmetry constructor code 142>;
<Symmetry::findClass code 143>;
<Symmetry::isFull code 144>;
<symiterator constructor code 145>;
<symiterator destructor code 146>;
<symiterator::operator ++ code 147>;
<InducedSymmetries constructor code 148>;
<InducedSymmetries permuted constructor code 149>;
<InducedSymmetries::print code 150>;
```

**142.** Construct symmetry as numbers of successively equal items in the sequence.

```

< Symmetry constructor code 142 > ≡
Symmetry::Symmetry(const IntSequence &s)
: IntSequence(s.getNumDistinct(), 0) {
    int p = 0;
    if (s.size() > 0) operator[] (p) = 1;
    for (int i = 1; i < s.size(); i++) {
        if (s[i] ≠ s[i - 1]) p++;
        operator[] (p)++;
    }
}

```

This code is used in section 141.

**143.** Find a class of the symmetry containing a given index.

```

< Symmetry::findClass code 143 > ≡
int Symmetry::findClass(int i) const
{
    int j = 0;
    int sum = 0;
    do {
        sum += operator[] (j);
        j++;
    } while (j < size() ∧ sum ≤ i);
    return j - 1;
}

```

This code is used in section 141.

**144.** The symmetry is full if it allows for any permutation of indices. It means, that there is at most one non-zero index.

```

< Symmetry::isFull code 144 > ≡
bool Symmetry::isFull() const
{
    int count = 0;
    for (int i = 0; i < num(); i++)
        if (operator[] (i) ≠ 0) count++;
    return count ≤ 1;
}

```

This code is used in section 141.

**145.** Here we construct the beginning of the **symiterator**. The first symmetry index is 0. If length is 2, the second index is the dimension, otherwise we create the subordinal symmetry set and its beginning as subordinal **symiterator**.

```

⟨symiterator constructor code 145⟩ ≡
symiterator::symiterator(SymmetrySet &ss)
: s(ss), subit(Λ), subs(Λ), end_flag(false) {
    s.sym()[0] = 0;
    if (s.size() ≡ 2) {
        s.sym()[1] = s.dimen();
    }
    else {
        subs = new SymmetrySet(s, s.dimen());
        subit = new symiterator(*subs);
    }
}

```

This code is used in section 141.

**146.**

```

⟨symiterator destructor code 146⟩ ≡
symiterator::~symiterator()
{
    if (subit) delete subit;
    if (subs) delete subs;
}

```

This code is used in section 141.

**147.** Here we move to the next symmetry. We do so only, if we are not at the end. If length is 2, we increase lower index and decrease upper index, otherwise we increase the subordinal symmetry. If we got to the end, we recreate the subordinal symmetry set and set the subordinal iterator to the beginning. At the end we test, if we are not at the end. This is recognized if the lowest index exceeded the dimension.

```

<symiterator::operator++ code 147> ≡
    symiterator &symiterator::operator++()
    {
        if (¬end_flag) {
            if (s.size() ≡ 2) {
                s.sym()[0]++;
                s.sym()[1]--;
            }
            else {
                ++(*subit);
                if (subit->isEnd()) {
                    delete subit;
                    delete subs;
                    s.sym()[0]++;
                    subs = new SymmetrySet(s, s.dimen() - s.sym()[0]);
                    subit = new symiterator(*subs);
                }
            }
            if (s.sym()[0] ≡ s.dimen() + 1) end_flag = true;
        }
        return *this;
    }

```

This code is used in section 141.

**148.**

```

<InducedSymmetries constructor code 148> ≡
    InducedSymmetries::InducedSymmetries(const Equivalence &e, const Symmetry &s)
    {
        for (Equivalence::const_seqit i = e.begin(); i ≠ e.end(); ++i) {
            push_back(Symmetry(s, *i));
        }
    }

```

This code is used in section 141.

**149.**

```

<InducedSymmetries permuted constructor code 149> ≡
    InducedSymmetries::InducedSymmetries(const Equivalence &e, const Permutation &p, const
        Symmetry &s)
    {
        for (int i = 0; i < e.numClasses(); i++) {
            Equivalence::const_seqit it = e.find(p.getMap()[i]);
            push_back(Symmetry(s, *it));
        }
    }

```

This code is used in section 141.

150. Debug print.

```
<InducedSymmetries::print code 150> ≡
void InducedSymmetries::print() const
{
    printf("Induced_symmetries: %lu\n", (unsigned long) size());
    for (unsigned int i = 0; i < size(); i++) operator[](i).print();
}
```

This code is used in section 141.

151. End of `symmetry.cpp` file.

152. **Equivalences.** Start of `equivalence.h` file

Here we define an equivalence of a set of integers  $\{0, 1, \dots, k-1\}$ . The purpose is clear, in the tensor library we often iterate through all equivalences and sum matrices. We need an abstraction for an equivalence class, equivalence and a set of all equivalences.

The equivalence class (which is basically a set of integers) is here implemented as ordered integer sequence. The ordered sequence is not implemented via **IntSequence**, but via **vector<int>** since we need insertions. The equivalence is implemented as an ordered list of equivalence classes, and equivalence set is a list of equivalences.

The ordering of the equivalence classes within an equivalence is very important. For instance, if we iterate through equivalences for  $k = 5$  and pickup some equivalence class, say  $\{\{0, 4\}, \{1, 2\}, \{3\}\}$ , we then evaluate something like:

$$[B_{y^2u^3}]_{\alpha_1\alpha_2\beta_1\beta_2\beta_3} = \dots + [g_{y^3}]_{\gamma_1\gamma_2\gamma_3} [g_{yu}]_{\alpha_1\beta_3}^{\gamma_1} [g_{yu}]_{\alpha_2\beta_1}^{\gamma_2} [g_u]_{\beta_2}^{\gamma_3} + \dots$$

If the tensors are unfolded, we can evaluate this expression as

$$g_{y^3} \cdot (g_{yu} \otimes g_{yu} \otimes g_u) \cdot P,$$

where  $P$  is a suitable permutation of columns of the expressions, which permutes them so that the index  $(\alpha_1, \beta_3, \alpha_2, \beta_1, \beta_2)$  would go to  $(\alpha_1, \alpha_2, \beta_1, \beta_2, \beta_3)$ . The permutation  $P$  can be very ineffective (copying great amount of small chunks of data) if the equivalence class ordering is chosen badly. However, we do not provide any heuristic minimizing a total time spent in all permutations. We choose an ordering which orders the classes according to their averages, and according to the smallest equivalence class element if the averages are the same.

```
#ifndef EQUIVALENCE_H
#define EQUIVALENCE_H
#include "int_sequence.h"
#include <vector>
#include <list>
using namespace std;
<OrdSequence class declaration 153>;
<Equivalence class declaration 154>;
<EquivalenceSet class declaration 155>;
<EquivalenceBundle class declaration 156>;
#endif
```

**153.** Here is the abstraction for an equivalence class. We implement it as **vector<int>**. We have a constructor for empty class, copy constructor. What is important here is the ordering operator **operator <** and methods for addition of an integer, and addition of another sequence. Also we provide method *has* which returns true if a given integer is contained.

```
< OrdSequence class declaration 153 > ≡
    class OrdSequence { vector<int> data;
public:
    OrdSequence()
    : data() {}
    OrdSequence(const OrdSequence &s)
    : data(s.data) {}
    const OrdSequence &operator=(const OrdSequence &s)
    { data = s.data; return *this; }
    bool operator==(const OrdSequence &s) const;
    int operator[](int i) const; bool operator < (const OrdSequence &s) const;
    const vector<int> &getData() const
    { return data; }
    int length() const
    { return data.size(); }
    void add(int i);
    void add(const OrdSequence &s);
    bool has(int i) const;
    void print(const char *prefix) const;
private:
    double average() const; } ;
```

This code is used in section 152.

**154.** Here is the abstraction for the equivalence. It is a list of equivalence classes. Also we remember  $n$ , which is a size of underlying set  $\{0, 1, \dots, n-1\}$ .

Method *trace* “prints” the equivalence into the integer sequence.

```

⟨ Equivalence class declaration 154 ⟩ ≡
class Permutation;
class Equivalence {
private: int n;
    list<OrdSequence> classes;
public:
    typedef list<OrdSequence>::const_iterator const_seqit;
    typedef list<OrdSequence>::iterator seqit;
    ⟨ Equivalence constructors 157 ⟩;
    const Equivalence &operator=(const Equivalence &e);
    bool operator==(const Equivalence &e) const;
    bool operator!=(const Equivalence &e) const
    { return ¬operator==(e); }
    int getN() const
    { return n; }
    int numClasses() const
    { return classes.size(); }
    void trace(IntSequence &out, int n) const;
    void trace(IntSequence &out) const
    { trace(out, numClasses()); }
    void trace(IntSequence &out, const Permutation &per) const;
    void print(const char *prefix) const;
    ⟨ Equivalence begin and end methods 158 ⟩;
    const_seqit find(int i) const;
    seqit find(int i);
protected:
    ⟨ Equivalence protected methods 159 ⟩;
};

```

This code is cited in section 1.

This code is used in section 152.



**155.** The **EquivalenceSet** is a list of equivalences. The unique constructor constructs a set of all equivalences over  $n$ -element set. The equivalences are sorted in the list so that equivalences with fewer number of classes are in the end.

The two methods *has* and *addParents* are useful in the constructor.

⟨**EquivalenceSet** class declaration 155⟩ ≡

```
class EquivalenceSet {
    int n;
    list<Equivalence> equis;
public:
    typedef list<Equivalence>::const_iterator const_iterator;
    EquivalenceSet(int num);
    void print(const char *prefix) const;
    const_iterator begin() const
    { return equis.begin(); }
    const_iterator end() const
    { return equis.end(); }
private:
    bool has(const Equivalence &e) const;
    void addParents(const Equivalence &e, list<Equivalence> &added);
};
```

This code is cited in section 1.

This code is used in section 152.

**156.** The equivalence bundle class only encapsulates **EquivalenceSets** from 1 up to a given number. It is able to retrieve the equivalence set over  $n$ -element set for a given  $n$ , and also it can generate some more sets on request.

It is fully responsible for storage needed for **EquivalenceSets**.

⟨**EquivalenceBundle** class declaration 156⟩ ≡

```
class EquivalenceBundle {
    vector<EquivalenceSet *> bundle;
public:
    EquivalenceBundle(int nmax);
    ~EquivalenceBundle();
    const EquivalenceSet &get(int n) const;
    void generateUpTo(int nmax);
};
```

This code is used in section 152.

**157.** The first constructor constructs  $\{\{0\}, \{1\}, \dots, \{n-1\}\}$ .

The second constructor constructs  $\{\{0, 1, \dots, n-1\}\}$ .

The third is the copy constructor. And the fourth is the copy constructor plus gluing  $i1$  and  $i2$  in one class.

⟨**Equivalence** constructors 157⟩ ≡

```
Equivalence(int num);
Equivalence(int num, const char *dummy);
Equivalence(const Equivalence &e);
Equivalence(const Equivalence &e, int i1, int i2);
```

See also section 173.

This code is used in sections 154 and 163.

158.

```
< Equivalence begin and end methods 158 > ≡
    seqit begin()
    { return classes.begin(); }
    const_seqit begin() const
    { return classes.begin(); }
    seqit end()
    { return classes.end(); }
    const_seqit end() const
    { return classes.end(); }
```

This code is used in section 154.

159. Here we have find methods. We can find an equivalence class having a given number or we can find an equivalence class of a given index within the ordering.

We have also an *insert* method which inserts a given class according to the class ordering.

```
< Equivalence protected methods 159 > ≡
    const_seqit findHaving(int i) const;
    seqit findHaving(int i);
    void insert(const OrdSequence &s);
```

This code is used in section 154.

160. End of equivalence.h file.

161. Start of equivalence.cpp file.

```
#include "equivalence.h"
#include "permutation.h"
#include "tl_exception.h"
#include <cstring>
< OrdSequence method codes 162 >;
< Equivalence method codes 163 >;
< EquivalenceSet method codes 164 >;
< EquivalenceBundle method codes 165 >;
```

162.

```
< OrdSequence method codes 162 > ≡
    < OrdSequence::operator[] code 166 >;
    < OrdSequence::operator < code 167 >;
    < OrdSequence::operator≡ code 168 >;
    < OrdSequence::add codes 169 >;
    < OrdSequence::has code 170 >;
    < OrdSequence::average() code 171 >;
    < OrdSequence::print code 172 >;
```

This code is used in section 161.

**163.**

```

⟨ Equivalence method codes 163 ⟩ ≡
  ⟨ Equivalence constructors 157 ⟩;
  ⟨ Equivalence copy constructors 174 ⟩;
  ⟨ Equivalence::findHaving codes 177 ⟩;
  ⟨ Equivalence::find codes 178 ⟩;
  ⟨ Equivalence::insert code 179 ⟩;
  ⟨ Equivalence::operator= code 175 ⟩;
  ⟨ Equivalence::operator≡ code 176 ⟩;
  ⟨ Equivalence::trace code 180 ⟩;
  ⟨ Equivalence::trace permuted code 181 ⟩;
  ⟨ Equivalence::print code 182 ⟩;

```

This code is used in section 161.

**164.**

```

⟨ EquivalenceSet method codes 164 ⟩ ≡
  ⟨ EquivalenceSet constructor code 183 ⟩;
  ⟨ EquivalenceSet::has code 184 ⟩;
  ⟨ EquivalenceSet::addParents code 185 ⟩;
  ⟨ EquivalenceSet::print code 186 ⟩;

```

This code is used in section 161.

**165.**

```

⟨ EquivalenceBundle method codes 165 ⟩ ≡
  ⟨ EquivalenceBundle constructor code 187 ⟩;
  ⟨ EquivalenceBundle destructor code 188 ⟩;
  ⟨ EquivalenceBundle::get code 189 ⟩;
  ⟨ EquivalenceBundle::generateUpTo code 190 ⟩;

```

This code is used in section 161.

**166.**

```

⟨ OrdSequence::operator[] code 166 ⟩ ≡
  int OrdSequence::operator[](int i) const
  {
    TL_RAISE_IF((i < 0 ∨ i ≥ length()), "Index_out_of_range_in_OrdSequence::operator[]");
    return data[i];
  }

```

This code is used in section 162.

**167.** Here we implement the ordering. It can be changed, or various orderings can be used for different problem sizes. We order them according to the average, and then according to the first item.

```

⟨ OrdSequence::operator < code 167 ⟩ ≡
  bool OrdSequence::operator < (const OrdSequence &s) const
  {
    double ta = average();
    double sa = s.average();
    return (ta < sa ∨ ((ta ≡ sa) ∧ (operator[](0) > s[0]));
  }

```

This code is used in section 162.

168.

```

< OrdSequence::operator≡ code 168 > ≡
bool OrdSequence::operator≡(const OrdSequence &s) const
{
    if (length() ≠ s.length()) return false;
    int i = 0;
    while (i < length() ∧ operator[] (i) ≡ s[i]) i++;
    return (i ≡ length());
}

```

This code is used in section 162.

169. The first *add* adds a given integer to the class, the second iterates through a given sequence and adds everything found in the given class.

```

< OrdSequence::add codes 169 > ≡
void OrdSequence::add(int i)
{
    vector<int>::iterator vit = data.begin();
    while (vit ≠ data.end() ∧ *vit < i) ++vit;
    if (vit ≠ data.end() ∧ *vit ≡ i) return;
    data.insert(vit, i);
}

void OrdSequence::add(const OrdSequence &s)
{
    vector<int>::const_iterator vit = s.data.begin();
    while (vit ≠ s.data.end()) {
        add(*vit);
        ++vit;
    }
}

```

This code is used in section 162.

170. Answers *true* if a given number is in the class.

```

< OrdSequence::has code 170 > ≡
bool OrdSequence::has(int i) const
{
    vector<int>::const_iterator vit = data.begin();
    while (vit ≠ data.end()) {
        if (*vit ≡ i) return true;
        ++vit;
    }
    return false;
}

```

This code is used in section 162.

**171.** Return an average of the class.

```

⟨ OrdSequence::average( ) code 171 ⟩ ≡
  double OrdSequence::average( ) const
  {
    double res = 0;
    for (unsigned int i = 0; i < data.size(); i++) res += data[i];
    TL_RAISE_IF(data.size() ≡ 0,
      "Attempt to take average of empty class in OrdSequence::average");
    return res/data.size();
  }

```

This code is used in section 162.

**172.** Debug print.

```

⟨ OrdSequence::print code 172 ⟩ ≡
  void OrdSequence::print(const char *prefix) const
  {
    printf("%s", prefix);
    for (unsigned int i = 0; i < data.size(); i++) printf("%d ", data[i]);
    printf("\n");
  }

```

This code is used in section 162.

**173.**

```

⟨ Equivalence constructors 157 ⟩ +≡
  Equivalence::Equivalence(int num)
  : n(num) {
    for (int i = 0; i < num; i++) {
      OrdSequence s;
      s.add(i);
      classes.push_back(s);
    }
  }

  Equivalence::Equivalence(int num, const char *dummy)
  : n(num) {
    OrdSequence s;
    for (int i = 0; i < num; i++) s.add(i);
    classes.push_back(s);
  }

```

**174.** Copy constructors. The second also glues a given couple.

```

< Equivalence copy constructors 174 > ≡
Equivalence::Equivalence(const Equivalence &e)
: n(e.n), classes(e.classes) {}

Equivalence::Equivalence(const Equivalence &e, int i1, int i2)
: n(e.n), classes(e.classes) {
    seqit s1 = find(i1);
    seqit s2 = find(i2);
    if (s1 ≠ s2) {
        OrdSequence ns(*s1);
        ns.add(*s2);
        classes.erase(s1);
        classes.erase(s2);
        insert(ns);
    }
}

```

This code is used in section 163.

**175.**

```

< Equivalence::operator= code 175 > ≡
const Equivalence &Equivalence::operator=(const Equivalence &e)
{
    classes.clear();
    n = e.n;
    classes = e.classes;
    return *this;
}

```

This code is used in section 163.

**176.**

```

< Equivalence::operator≡ code 176 > ≡
bool Equivalence::operator≡(const Equivalence &e) const
{
    if (¬std::operator≡(classes, e.classes)) return false;
    if (n ≠ e.n) return false;
    return true;
}

```

This code is used in section 163.

**177.** Return an iterator pointing to a class having a given integer.

⟨ **Equivalence::findHaving** codes 177 ⟩ ≡

```

Equivalence::const_seqit Equivalence::findHaving(int i) const
{
    const_seqit si = classes.begin();
    while (si ≠ classes.end()) {
        if ((*si).has(i)) return si;
        ++si;
    }
    TL_RAISE_IF(si ≡ classes.end(),
        "Couldn't find equivalence class in Equivalence::findHaving");
    return si;
}

Equivalence::seqit Equivalence::findHaving(int i)
{
    seqit si = classes.begin();
    while (si ≠ classes.end()) {
        if ((*si).has(i)) return si;
        ++si;
    }
    TL_RAISE_IF(si ≡ classes.end(),
        "Couldn't find equivalence class in Equivalence::findHaving");
    return si;
}

```

This code is used in section 163.

**178.** Find  $j$ -th class for a given  $j$ .

$\langle \text{Equivalence}::\text{find}$  codes 178  $\rangle \equiv$

```

Equivalence::const_seqit Equivalence::find(int  $j$ ) const
{
    const_seqit  $si = \text{classes.begin}()$ ;
    int  $i = 0$ ;
    while ( $si \neq \text{classes.end}() \wedge i < j$ ) {
        ++ $si$ ;
         $i++$ ;
    }
    TL_RAISE_IF( $si \equiv \text{classes.end}()$ , "Couldn't find equivalence class in Equivalence::find");
    return  $si$ ;
}

Equivalence::seqit Equivalence::find(int  $j$ )
{
    seqit  $si = \text{classes.begin}()$ ;
    int  $i = 0$ ;
    while ( $si \neq \text{classes.end}() \wedge i < j$ ) {
        ++ $si$ ;
         $i++$ ;
    }
    TL_RAISE_IF( $si \equiv \text{classes.end}()$ , "Couldn't find equivalence class in Equivalence::find");
    return  $si$ ;
}

```

This code is used in section 163.

**179.** Insert a new class yielding the ordering.

$\langle \text{Equivalence}::\text{insert}$  code 179  $\rangle \equiv$

```

void Equivalence::insert(const OrdSequence & $s$ )
{
    seqit  $si = \text{classes.begin}()$ ;
    while ( $si \neq \text{classes.end}() \wedge *si < s$ ) ++ $si$ ;
     $\text{classes.insert}(si, s)$ ;
}

```

This code is used in section 163.



**180.** Trace the equivalence into the integer sequence. The classes are in some order (described earlier), and items within classes are ordered, so this implies, that the data can be linearized. This method “prints” them to the sequence. We allow for tracing only a given number of classes from the beginning.

```

< Equivalence::trace code 180 > ≡
void Equivalence::trace(IntSequence &out, int num) const
{
    int i = 0;
    int nc = 0;
    for (const_seq it = begin(); it ≠ end() ∧ nc < num; ++it, ++nc)
        for (int j = 0; j < (*it).length(); j++, i++) {
            TL_RAISE_IF(i ≥ out.size(), "Wrong_size_of_output_sequence_in_Equivalence::trace");
            out[i] = (*it)[j];
        }
}

```

This code is used in section 163.

**181.**

```

< Equivalence::trace permuted code 181 > ≡
void Equivalence::trace(IntSequence &out, const Permutation &per) const
{
    TL_RAISE_IF(out.size() ≠ n, "Wrong_size_of_output_sequence_in_Equivalence::trace");
    TL_RAISE_IF(per.size() ≠ numClasses(),
        "Wrong_permutation_for_permuted_Equivalence::trace");
    int i = 0;
    for (int iclass = 0; iclass < numClasses(); iclass++) {
        const_seq itper = find(per.getMap())[iclass];
        for (int j = 0; j < (*itper).length(); j++, i++) out[i] = (*itper)[j];
    }
}

```

This code is used in section 163.

**182.** Debug print.

```

< Equivalence::print code 182 > ≡
void Equivalence::print(const char *prefix) const
{
    int i = 0;
    for (const_seq it = classes.begin(); it ≠ classes.end(); ++it, i++) {
        printf("%sclass%d:", prefix, i);
        (*it).print("");
    }
}

```

This code is used in section 163.

**183.** Here we construct a set of all equivalences over  $n$ -element set. The construction proceeds as follows. We maintain a list of added equivalences. At each iteration we pop front of the list, try to add all parents of the popped equivalence. This action adds new equivalences to the object and also to the added list. We finish the iterations when the added list is empty.

In the beginning we start with  $\{\{0\}, \{1\}, \dots, \{n-1\}\}$ . Adding of parents is an action which for a given equivalence tries to glue all possible couples and checks whether a new equivalence is already in the equivalence set. This is not effective, but we will do the construction only ones.

In this way we breath-first search a lattice of all equivalences. Note that the lattice is modular, that is why the result of a construction is a list with a property that between two equivalences with the same number of classes there are only equivalences with that number of classes. Obviously, the list is decreasing in a number of classes (since it is constructed by gluing attempts).

```

⟨ EquivalenceSet constructor code 183 ⟩ ≡
EquivalenceSet :: EquivalenceSet(int num)
:  $n(num)$ ,  $equis()$  {
  list⟨Equivalence⟩ added;
  Equivalence first( $n$ );
   $equis.push\_back(first)$ ;
   $addParents(first, added)$ ;
  while ( $\neg added.empty()$ ) {
     $addParents(added.front(), added)$ ;
     $added.pop\_front()$ ;
  }
  if ( $n > 1$ ) {
    Equivalence last( $n, ""$ );
     $equis.push\_back(last)$ ;
  }
}

```

This code is used in section 164.

**184.** This method is used in *addParents* and returns *true* if the object already has that equivalence. We trace list of equivalences in reverse order since equivalences are ordered in the list from the most primitive (nothing equivalent) to maximal (all is equivalent). Since we will have much more results of *has* method as *true*, and **operator** $\equiv$  between equivalences is quick if number of classes differ, and in time we will compare with equivalences with less classes, then it is more efficient to trace the equivalences from less classes to more classes. hence the reverse order.

```

⟨ EquivalenceSet :: has code 184 ⟩ ≡
bool EquivalenceSet :: has(const Equivalence & $e$ ) const
{
  list⟨Equivalence⟩ :: const_reverse_iterator rit =  $equis.rbegin()$ ;
  while ( $rit \neq equis.rend()$   $\wedge$   $*rit \neq e$ ) ++rit;
  if ( $rit \neq equis.rend()$ ) return true;
  return false;
}

```

This code is used in section 164.

**185.** Responsibility of this methods is to try to glue all possible couples within a given equivalence and add those which are not in the list yet. These are added also to the *added* list.

If number of classes is 2 or 1, we exit, because there is nothing to be added.

```

<EquivalenceSet::addParents code 185> ≡
void EquivalenceSet::addParents(const Equivalence &e, list<Equivalence> &added)
{
    if (e.numClasses() ≡ 2 ∨ e.numClasses() ≡ 1) return;
    for (int i1 = 0; i1 < e.numClasses(); i1++)
        for (int i2 = i1 + 1; i2 < e.numClasses(); i2++) {
            Equivalence ns(e, i1, i2);
            if (!has(ns)) {
                added.push_back(ns);
                equis.push_back(ns);
            }
        }
}

```

This code is used in section 164.

**186.** Debug print.

```

<EquivalenceSet::print code 186> ≡
void EquivalenceSet::print(const char *prefix) const
{
    char tmp[100];
    strcpy(tmp, prefix);
    strcat(tmp, "□□□□");
    int i = 0;
    for (list<Equivalence>::const_iterator it = equis.begin(); it ≠ equis.end(); ++it, i++) {
        printf("%sequivalence_□d: (classes_□d)\n", prefix, i, (*it).numClasses());
        (*it).print(tmp);
    }
}

```

This code is used in section 164.

**187.** Construct the bundle. *nmax* is a maximum size of underlying set.

```

<EquivalenceBundle constructor code 187> ≡
EquivalenceBundle::EquivalenceBundle(int nmax)
{
    nmax = max(nmax, 1);
    generateUpTo(nmax);
}

```

This code is used in section 165.

**188.** Destruct bundle. Just free all pointers.

```

<EquivalenceBundle destructor code 188> ≡
EquivalenceBundle::~EquivalenceBundle()
{
    for (unsigned int i = 0; i < bundle.size(); i++) delete bundle[i];
}

```

This code is used in section 165.

**189.** Remember, that the first item is **EquivalenceSet**(1).

```

⟨ EquivalenceBundle::get code 189 ⟩ ≡
const EquivalenceSet &EquivalenceBundle::get(int n) const
{
    if (n > (int)(bundle.size()) ∨ n < 1) {
        TL_RAISE("Equivalence_set_not_found_in_EquivalenceBundle::get");
        return *(bundle[0]);
    }
    else {
        return *(bundle[n - 1]);
    }
}

```

This code is used in section 165.

**190.** Get *curmax* which is a maximum size in the bundle, and generate for all sizes from *curmax* + 1 up to *nmax*.

```

⟨ EquivalenceBundle::generateUpTo code 190 ⟩ ≡
void EquivalenceBundle::generateUpTo(int nmax)
{
    int curmax = bundle.size();
    for (int i = curmax + 1; i ≤ nmax; i++) bundle.push_back(new EquivalenceSet(i));
}

```

This code is used in section 165.

**191.** End of `equivalence.cpp` file.

**192. Permutations.** Start of `permutation.h` file.

The permutation class is useful when describing a permutation of indices in permuted symmetry tensor. This tensor comes to existence, for instance, as a result of the following tensor multiplication:

$$[g_{y^3}]_{\gamma_1\gamma_2\gamma_3} [g_{yu}]_{\alpha_1\beta_3}^{\gamma_1} [g_{yu}]_{\alpha_2\beta_1}^{\gamma_2} [g_u]_{\beta_2}^{\gamma_3}$$

If this operation is done by a Kronecker product of unfolded tensors, the resulting tensor has permuted indices. So, in this case the permutation is implied by the equivalence:  $\{\{0, 4\}, \{1, 3\}, \{2\}\}$ . This results in a permutation which maps indices  $(0, 1, 2, 3, 4) \mapsto (0, 2, 4, 3, 1)$ .

The other application of **Permutation** class is to permute indices with the same permutation as done during sorting.

Here we only define an abstraction for the permutation defined by an equivalence. Its basic operation is to apply the permutation to the integer sequence. The application is right (or inner), in sense that it works on indices of the sequence not items of the sequence. More formally  $s \circ m \neq m \circ s$ . In here, the application of the permutation defined by map  $m$  is  $s \circ m$ .

Also, we need **PermutationSet** class which contains all permutations of  $n$  element set, and a bundle of permutations **PermutationBundle** which contains all permutation sets up to a given number.

```
#ifndef PERMUTATION_H
#define PERMUTATION_H
#include "int_sequence.h"
#include "equivalence.h"
#include <vector>
    <Permutation class declaration 193>;
    <PermutationSet class declaration 194>;
    <PermutationBundle class declaration 195>;
#endif
```

**193.** The permutation object will have a map, which defines mapping of indices  $(0, 1, \dots, n-1) \mapsto (m_0, m_1, \dots, m_{n-1})$ . The map is the sequence  $(m_0, m_1, \dots, m_{n-1})$ . When the permutation with the map  $m$  is applied on sequence  $s$ , it permutes its indices:  $s \circ \text{id} \mapsto s \circ m$ .

So we have one constructor from equivalence, then a method *apply*, and finally a method *tailIdentity* which returns a number of trailing indices which yield identity. Also we have a constructor calculating map, which corresponds to permutation in sort. This is, we want (sorted  $s$ )  $\circ m = s$ .

⟨ **Permutation** class declaration 193 ⟩  $\equiv$

```

class Permutation {
protected:
    IntSequence permap;
public:
    Permutation(int len)
    : permap(len) { for (int i = 0; i < len; i++) permap[i] = i; }
    Permutation(const Equivalence &e)
    : permap(e.getN()) { e.trace(permap); }
    Permutation(const Equivalence &e, const Permutation &per)
    : permap(e.getN()) { e.trace(permap, per); }
    Permutation(const IntSequence &s)
    : permap(s.size()) { computeSortingMap(s); }
    ;
    Permutation(const Permutation &p)
    : permap(p.permap) {}
    Permutation(const Permutation &p1, const Permutation &p2)
    : permap(p2.permap) { p1.apply(permap); }
    Permutation(const Permutation &p, int i)
    : permap(p.size(), p.permap, i) {}
    const Permutation &operator=(const Permutation &p)
    { permap = p.permap; return *this; }
    bool operator==(const Permutation &p)
    { return permap == p.permap; }
    int size() const
    { return permap.size(); }
    void print() const
    { permap.print(); }
    void apply(const IntSequence &src, IntSequence &tar) const;
    void apply(IntSequence &tar) const;
    void inverse();
    int tailIdentity() const;
    const IntSequence &getMap() const
    { return permap; }
    IntSequence &getMap()
    { return permap; }
protected:
    void computeSortingMap(const IntSequence &s);
};

```

This code is cited in section 1.

This code is used in section 192.

**194.** The **PermutationSet** maintains an array of all permutations. The default constructor constructs one element permutation set of one element sets. The second constructor constructs a new permutation set over  $n$  from all permutations over  $n - 1$ . The parameter  $n$  need not to be provided, but it serves to distinguish the constructor from copy constructor, which is not provided.

The method *getPreserving* returns a factor subgroup of permutations, which are invariants with respect to the given sequence. This are all permutations  $p$  yielding  $p \circ s = s$ , where  $s$  is the given sequence.

⟨ **PermutationSet** class declaration 194 ⟩  $\equiv$

```
class PermutationSet {
    int order;
    int size;
    const Permutation **const pers;
public:
    PermutationSet();
    PermutationSet(const PermutationSet &ps, int n);
    ~PermutationSet();
    int getNum() const
    { return size; }
    const Permutation &get(int i) const
    { return *(pers[i]); }
    vector<const Permutation *> getPreserving(const IntSequence &s) const;
};
```

This code is used in section 192.

**195.** The permutation bundle encapsulates all permutations sets up to some given dimension.

⟨ **PermutationBundle** class declaration 195 ⟩  $\equiv$

```
class PermutationBundle {
    vector<PermutationSet *> bundle;
public:
    PermutationBundle(int nmax);
    ~PermutationBundle();
    const PermutationSet &get(int n) const;
    void generateUpTo(int nmax);
};
```

This code is used in section 192.

**196.** End of permutation.h file.

197. Start of `permutation.cweb` file.

```
#include "permutation.h"
#include "tl_exception.h"
< Permutation::apply code 198 >;
< Permutation::inverse code 199 >;
< Permutation::tailIdentity code 200 >;
< Permutation::computeSortingMap code 201 >;
< PermutationSet constructor code 1 202 >;
< PermutationSet constructor code 2 203 >;
< PermutationSet destructor code 204 >;
< PermutationSet::getPreserving code 205 >;
< PermutationBundle constructor code 206 >;
< PermutationBundle destructor code 207 >;
< PermutationBundle::get code 208 >;
< PermutationBundle::generateUpTo code 209 >;
```

198. This is easy, we simply apply the map in the fashion  $s \circ m$ .

```
< Permutation::apply code 198 > ≡
void Permutation::apply(const IntSequence &src, IntSequence &tar) const
{
    TL_RAISE_IF(src.size() ≠ permap.size() ∨ tar.size() ≠ permap.size(),
        "Wrong sizes of input or output in Permutation::apply");
    for (int i = 0; i < permap.size(); i++) tar[i] = src[permap[i]];
}
void Permutation::apply(IntSequence &tar) const
{
    IntSequence tmp(tar);
    apply(tmp, tar);
}
```

This code is used in section 197.

199.

```
< Permutation::inverse code 199 > ≡
void Permutation::inverse()
{
    IntSequence former(permap);
    for (int i = 0; i < size(); i++) permap[former[i]] = i;
}
```

This code is used in section 197.

200. Here we find a number of trailing indices which are identical with the permutation.

```
< Permutation::tailIdentity code 200 > ≡
int Permutation::tailIdentity() const
{
    int i = permap.size();
    while (i > 0 ∧ permap[i - 1] ≡ i - 1) i--;
    return permap.size() - i;
}
```

This code is used in section 197.



**201.** This calculates a map which corresponds to sorting in the following sense:  $(\text{sorted } s) \circ m = s$ , where  $s$  is a given sequence.

We go through  $s$  and find an the same item in sorted  $s$ . We construct the *permap* from the found pair of indices. We have to be careful, to not assign to two positions in  $s$  the same position in sorted  $s$ , so we maintain a bitmap *flag*, in which we remember indices from the sorted  $s$  already assigned.

```

⟨ Permutation :: computeSortingMap code 201 ⟩ ≡
  void Permutation :: computeSortingMap(const IntSequence &s)
  {
    IntSequence srt(s);
    srt.sort();
    IntSequence flags(s.size(), 0);
    for (int i = 0; i < s.size(); i++) {
      int j = 0;
      while (j < s.size() ∧ (flags[j] ∨ srt[j] ≠ s[i])) j++;
      TL_RAISE_IF(j ≡ s.size(), "Internal_algorithm_error_in_Permutation::computeSortingMap");
      flags[j] = 1;
      permap[i] = j;
    }
  }

```

This code is used in section 197.

**202.**

```

⟨ PermutationSet constructor code 1 202 ⟩ ≡
  PermutationSet :: PermutationSet()
  : order(1), size(1), pers(new const Permutation*[size]) {
    pers[0] = new Permutation(1);
  }

```

This code is used in section 197.

**203.**

```

⟨ PermutationSet constructor code 2 203 ⟩ ≡
  PermutationSet :: PermutationSet(const PermutationSet &sp, int n)
  : order(n), size(n * sp.size), pers(new const Permutation*[size]) {
    for (int i = 0; i < size; i++) pers[i] = Λ;
    TL_RAISE_IF(n ≠ sp.order + 1, "Wrong_new_order_in_PermutationSet_constructor");
    int k = 0;
    for (int i = 0; i < sp.size; i++) {
      for (int j = 0; j < order; j++, k++) {
        pers[k] = new Permutation*(sp.pers[i], j);
      }
    }
  }

```

This code is used in section 197.

**204.**

```

⟨ PermutationSet destructor code 204 ⟩ ≡
PermutationSet::~PermutationSet()
{
    for (int i = 0; i < size; i++)
        if (pers[i]) delete pers[i];
    delete[] pers;
}

```

This code is used in section 197.

**205.**

```

⟨ PermutationSet::getPreserving code 205 ⟩ ≡
vector<const Permutation *⟩ PermutationSet::getPreserving(const IntSequence &s) const
{
    TL_RAISE_IF(s.size() ≠ order, "Wrong_sequence_length_in_PermutationSet::getPreserving");
    vector<const Permutation *⟩ res;
    IntSequence tmp(s.size());
    for (int i = 0; i < size; i++) {
        pers[i]→apply(s, tmp);
        if (s ≡ tmp) {
            res.push_back(pers[i]);
        }
    }
    return res;
}

```

This code is used in section 197.

**206.**

```

⟨ PermutationBundle constructor code 206 ⟩ ≡
PermutationBundle::PermutationBundle(int nmax)
{
    nmax = max(nmax, 1);
    generateUpTo(nmax);
}

```

This code is used in section 197.

**207.**

```

⟨ PermutationBundle destructor code 207 ⟩ ≡
PermutationBundle::~PermutationBundle()
{
    for (unsigned int i = 0; i < bundle.size(); i++) delete bundle[i];
}

```

This code is used in section 197.

208.

```

⟨ PermutationBundle::get code 208 ⟩ ≡
  const PermutationSet &PermutationBundle::get(int n) const
  {
    if (n > (int)(bundle.size()) ∨ n < 1) {
      TL_RAISE("Permutation_set_not_found_in_PermutationSet::get");
      return *(bundle[0]);
    }
    else {
      return *(bundle[n - 1]);
    }
  }

```

This code is used in section 197.

209.

```

⟨ PermutationBundle::generateUpTo code 209 ⟩ ≡
  void PermutationBundle::generateUpTo(int nmax)
  {
    if (bundle.size() ≡ 0) bundle.push_back(new PermutationSet());
    int curmax = bundle.size();
    for (int n = curmax + 1; n ≤ nmax; n++) {
      bundle.push_back(new PermutationSet*(bundle.back()), n);
    }
  }

```

This code is used in section 197.

210. End of permutation.cweb file.

**211. Tensors.****212. Tensor concept.** Start of `tensor.h` file.

Here we define a tensor class. Tensor is a mathematical object corresponding to a  $(n + 1)$ -dimensional array. An element of such array is denoted  $[B]_{\alpha_1 \dots \alpha_n}^\beta$ , where  $\beta$  is a special index and  $\alpha_1 \dots \alpha_n$  are other indices. The class **Tensor** and its subclasses view such array as a 2D matrix, where  $\beta$  corresponds to one dimension, and  $\alpha_1 \dots \alpha_n$  unfold to the other dimension. Whether  $\beta$  correspond to rows or columns is decided by tensor subclasses, however, most of our tensors will have rows indexed by  $\beta$ , and  $\alpha_1 \dots \alpha_n$  will unfold column-wise.

There might be some symmetries in the tensor data. For instance, if  $\alpha_1$  is interchanged with  $\alpha_3$  and the both elements equal for all possible  $\alpha_i$ , and  $\beta$ , then there is a symmetry of  $\alpha_1$  and  $\alpha_3$ .

For any symmetry, there are basically two possible storages of the data. The first is unfolded storage, which stores all elements regardless the symmetry. The other storage type is folded, which stores only elements which do not repeat. We declare abstract classes for unfolded tensor, and folded tensor.

Also, here we also define a concept of tensor index which is the  $n$ -tuple  $\alpha_1 \dots \alpha_n$ . It is an iterator, which iterates in dependence of symmetry and storage of the underlying tensor.

Although we do not decide about possible symmetries at this point, it is worth noting that we implement two kinds of symmetries. The first one is a full symmetry where all indices are interchangeable. The second one is a generalization of the first. We define tensor of a symmetry, where there are a few groups of indices interchangeable within a group and not across. Moreover, the groups are required to be consequent partitions of the index  $n$ -tuple. This is, we do not allow  $\alpha_1$  be interchangeable with  $\alpha_3$  and not with  $\alpha_2$  at the same time.

However, some intermediate results are, in fact, tensors of a symmetry not fitting to our concept. We develop the tensor abstraction for it, but these objects are not used very often. They have limited usage due to their specialized constructor.

```
#ifndef TENSOR_H
#define TENSOR_H
#include "int_sequence.h"
#include "twod_matrix.h"
    <index class definition 213>;
    <Tensor class declaration 214>;
    <UTensor class declaration 215>;
    <FTensor class declaration 216>;
#endif
```

**213.** The index represents  $n$ -tuple  $\alpha_1 \dots \alpha_n$ . Since its movement is dependent on the underlying tensor (with storage and symmetry), we maintain a pointer to that tensor, we maintain the  $n$ -tuple (or coordinates) as **IntSequence** and also we maintain the offset number (column, or row) of the index in the tensor. The pointer is const, since we do not need to change data through the index.

Here we require the *tensor* to implement *increment* and *decrement* methods, which calculate following and preceding  $n$ -tuple. Also, we need to calculate offset number from the given coordinates, so the tensor must implement method *getOffset*. This method is used only in construction of the index from the given coordinates. As the index is created, the offset is automatically incremented, and decremented together with index. The *getOffset* method can be relatively computationally complex. This must be kept in mind. Also we generally suppose that  $n$ -tuple of all zeros is the first offset (first columns or row).

What follows is a definition of index class, the only interesting point is **operator** $\equiv$  which decides only according to offset, not according to the coordinates. This is useful since there can be more than one of coordinate representations of past-the-end index.

(index class definition 213)  $\equiv$

```
template<class _Tptr> class _index {
    typedef _index<_Tptr> _Self;
    _Tptr tensor;
    int offset;
    IntSequence coor;
public:
    _index(_Tptr t, int n)
    : tensor(t), offset(0), coor(n, 0) {}
    _index(_Tptr t, const IntSequence &cr, int c)
    : tensor(t), offset(c), coor(cr) {}
    _index(_Tptr t, const IntSequence &cr)
    : tensor(t), offset(tensor->getOffset(cr)), coor(cr) {}
    _index(const _index &ind)
    : tensor(ind.tensor), offset(ind.offset), coor(ind.coor) {}
    const _Self &operator=(const _Self &in)
    { tensor = in.tensor; offset = in.offset; coor = in.coor;
      return *this; }
    _Self &operator++()
    { tensor->increment(coor); offset++; return *this; }
    _Self &operator--()
    { tensor->decrement(coor); offset--; return *this; }
    int operator*() const
    { return offset; }
    bool operator==(const _index &n) const
    { return offset == n.offset; }
    bool operator!=(const _index &n) const
    { return offset != n.offset; }
    const IntSequence &getCoor() const
    { return coor; }
    void print() const
    { printf("%4d: ", offset); coor.print(); }
};
```

This code is used in section 212.

**214.** Here is the **Tensor** class, which is nothing else than a simple subclass of **TwoDMatrix**. The unique semantically new member is *dim* which is tensor dimension (length of  $\alpha_1 \dots \alpha_n$ ). We also declare *increment*, *decrement* and *getOffset* methods as pure virtual.

We also add members for index begin and index end. This is useful, since *begin* and *end* methods do not return instance but only references, which prevent making additional copy of index (for example in for cycles as *in*  $\neq$  *end*() which would do a copy of index for each cycle). The index begin *in\_beg* is constructed as a sequence of all zeros, and *in\_end* is constructed from the sequence *last* passed to the constructor, since it depends on subclasses. Also we have to say, along what coordinate is the multidimensional index. This is used only for initialization of *in\_end*.

Also, we declare static auxiliary functions for  $\binom{n}{k}$  which is *noverk* and  $a^b$ , which is *power*.

⟨**Tensor** class declaration 214⟩  $\equiv$

```
class Tensor : public TwoDMatrix {
public:
    enum indor {
        along_row, along_col
    };
    typedef _index⟨const Tensor *⟩ index;
protected:
    const index in_beg;
    const index in_end;
    int dim;
public:
    Tensor(indor io, const IntSequence &last, int r, int c, int d)
    : TwoDMatrix(r, c), in_beg(this, d), in_end(this, last, (io  $\equiv$  along_row) ? r : c), dim(d) {}
    Tensor(indor io, const IntSequence &first, const IntSequence &last, int r, int c, int d)
    : TwoDMatrix(r, c), in_beg(this, first, 0), in_end(this, last, (io  $\equiv$  along_row) ? r : c), dim(d) {}
    Tensor(int first_row, int num, Tensor &t)
    : TwoDMatrix(first_row, num, t), in_beg(t.in_beg), in_end(t.in_end), dim(t.dim) {}
    Tensor(const Tensor &t)
    : TwoDMatrix(t), in_beg(this, t.in_beg.getCoor(), *(t.in_beg)), in_end(this, t.in_end.getCoor(),
        *(t.in_end)), dim(t.dim) {}
    virtual ~Tensor() {}
    virtual void increment(IntSequence &v) const = 0;
    virtual void decrement(IntSequence &v) const = 0;
    virtual int getOffset(const IntSequence &v) const = 0;
    int dimen() const
    { return dim; }
    const index &begin() const
    { return in_beg; }
    const index &end() const
    { return in_end; }
    static int noverk(int n, int k);
    static int power(int a, int b);
    static int noverseq(const IntSequence &s)
    {
        IntSequence seq(s);
        return noverseq_ip((IntSequence &) s);
    }
};
```

```

    }
private:
    static int noverseq_ip(IntSequence &s);
};

```

This code is cited in section 1.

This code is used in section 212.

**215.** Here is an abstraction for unfolded tensor. We provide a pure virtual method *fold* which returns a new instance of folded tensor of the same symmetry. Also we provide static methods for incrementing and decrementing an index with full symmetry and general symmetry as defined above.

(**UTensor** class declaration 215)  $\equiv$

```

class FTensor;
class UTensor : public Tensor {
public:
    UTensor(indor io, const IntSequence &last, int r, int c, int d)
    : Tensor(io, last, r, c, d) {}
    UTensor(const UTensor &ut)
    : Tensor(ut) {}
    UTensor(int first_row, int num, UTensor &t)
    : Tensor(first_row, num, t) {}
    virtual ~UTensor() {}
    virtual FTensor &fold() const = 0;
    static void increment(IntSequence &v, int nv);
    static void decrement(IntSequence &v, int nv);
    static void increment(IntSequence &v, const IntSequence &nvmx);
    static void decrement(IntSequence &v, const IntSequence &nvmx);
    static int getOffset(const IntSequence &v, int nv);
    static int getOffset(const IntSequence &v, const IntSequence &nvmx);
};

```

This code is cited in section 1.

This code is used in section 212.

**216.** This is an abstraction for folded tensor. It only provides a method *unfold*, which returns the unfolded version of the same symmetry, and static methods for decrementing indices.

We also provide static methods for decrementing the **IntSequence** in folded fashion and also calculating an offset for a given **IntSequence**. However, this is relatively complex calculation, so this should be avoided if possible.

```

< FTensor class declaration 216 > ≡
class FTensor : public Tensor {
public:
    FTensor(indor io, const IntSequence &last, int r, int c, int d)
    : Tensor(io, last, r, c, d) {}

    FTensor(const FTensor &ft)
    : Tensor(ft) {}

    FTensor(int first_row, int num, FTensor &t)
    : Tensor(first_row, num, t) {}

    virtual ~FTensor() {}

    virtual UTensor &unfold() const = 0;
    static void decrement(IntSequence &v, int nv);

    static int getOffset(const IntSequence &v, int nv)
    { IntSequence vtmp(v); return getOffsetRecurse(vtmp, nv); }

private:
    static int getOffsetRecurse(IntSequence &v, int nv);
};

```

This code is cited in section 1.

This code is used in section 212.

**217.** End of `tensor.h` file.

**218.** Start of `tensor.cpp` file.

```

#include "tensor.h"
#include "tl_exception.h"
#include "tl_static.h"

< Tensor static methods 219 >;
< Tensor::noverseq_ip static method 220 >;
< UTensor::increment code 1 221 >;
< UTensor::decrement code 1 222 >;
< UTensor::increment code 2 223 >;
< UTensor::decrement code 2 224 >;
< UTensor::getOffset code 1 225 >;
< UTensor::getOffset code 2 226 >;
< FTensor::decrement code 227 >;
< FTensor::getOffsetRecurse code 228 >;

```



**219.** Here we implement calculation of  $\binom{n}{k}$  where  $n - k$  is usually bigger than  $k$ .

Also we implement  $a^b$ .

```

< Tensor static methods 219 > ≡
int Tensor::noverk(int n, int k)
{
    return tls.ptriang-noverk(n, k);
}
int Tensor::power(int a, int b)
{
    int res = 1;
    for (int i = 0; i < b; i++) res *= a;
    return res;
}

```

This code is used in section 218.

**220.** Here we calculate a generalized combination number  $\binom{a}{b_1, \dots, b_n}$ , where  $a = b_1 + \dots + b_n$ . We use the identity

$$\binom{a}{b_1, \dots, b_n} = \binom{b_1 + b_2}{b_1} \cdot \binom{a}{b_1 + b_2, b_3, \dots, b_n}$$

This number is exactly a number of unfolded indices corresponding to one folded index, where the sequence  $b_1, \dots, b_n$  is the symmetry of the index.

```

< Tensor::noverseq-ip static method 220 > ≡
int Tensor::noverseq-ip(IntSequence &s)
{
    if (s.size() ≡ 0 ∨ s.size() ≡ 1) return 1;
    s[1] += s[0];
    return noverk(s[1], s[0]) * noverseq(IntSequence(s, 1, s.size()));
}

```

This code is used in section 218.

**221.** Here we increment a given sequence within full symmetry given by  $nv$ , which is number of variables in each dimension. The underlying tensor is unfolded, so we increase the rightmost by one, and if it is  $nv$  we zero it and increase the next one to the left.

```

< UTensor::increment code 1 221 > ≡
void UTensor::increment(IntSequence &v, int nv)
{
    if (v.size() ≡ 0) return;
    int i = v.size() - 1;
    v[i]++;
    while (i > 0 ∧ v[i] ≡ nv) {
        v[i] = 0;
        v[--i]++;
    }
}

```

This code is used in section 218.

**222.** This is dual to `UTensor::increment(IntSequence &v, int nv)`.

```

< UTensor::decrement code 1 222 > ≡
void UTensor::decrement(IntSequence &v, int nv)
{
    if (v.size() ≡ 0) return;
    int i = v.size() - 1;
    v[i]--;
    while (i > 0 ∧ v[i] ≡ -1) {
        v[i] = nv - 1;
        v[--i]--;
    }
}

```

This code is used in section 218.

**223.** Here we increment index for general symmetry for unfolded storage. The sequence *nvmax* assigns for each coordinate a number of variables. Since the storage is unfolded, we do not need information about what variables are symmetric, everything necessary is given by *nvmax*.

```

< UTensor::increment code 2 223 > ≡
void UTensor::increment(IntSequence &v, const IntSequence &nvmax)
{
    if (v.size() ≡ 0) return;
    int i = v.size() - 1;
    v[i]++;
    while (i > 0 ∧ v[i] ≡ nvmax[i]) {
        v[i] = 0;
        v[--i]++;
    }
}

```

This code is used in section 218.

**224.** This is a dual code to `UTensor::increment(IntSequence &v, const IntSequence &nvmax)`.

```

< UTensor::decrement code 2 224 > ≡
void UTensor::decrement(IntSequence &v, const IntSequence &nvmax)
{
    if (v.size() ≡ 0) return;
    int i = v.size() - 1;
    v[i]--;
    while (i > 0 ∧ v[i] ≡ -1) {
        v[i] = nvmax[i] - 1;
        v[--i]--;
    }
}

```

This code is used in section 218.

**225.** Here we return an offset for a given coordinates of unfolded full symmetry tensor. This is easy.

```

< UTensor::getOffset code 1 225 > ≡
int UTensor::getOffset(const IntSequence &v, int nv)
{
    int pow = 1;
    int res = 0;
    for (int i = v.size() - 1; i ≥ 0; i-- ) {
        res += v[i] * pow;
        pow *= nv;
    }
    return res;
}

```

This code is used in section 218.

**226.** Also easy.

```

< UTensor::getOffset code 2 226 > ≡
int UTensor::getOffset(const IntSequence &v, const IntSequence &nvmax)
{
    int pow = 1;
    int res = 0;
    for (int i = v.size() - 1; i ≥ 0; i-- ) {
        res += v[i] * pow;
        pow *= nvmax[i];
    }
    return res;
}

```

This code is used in section 218.

**227.** Decrementing of coordinates of folded index is not that easy. Note that if a trailing part of coordinates is  $(b, a, a, a)$  (for instance) with  $b < a$ , then a preceding coordinates are  $(b, a - 1, n - 1, n - 1)$ , where  $n$  is a number of variables  $nv$ . So we find the left most element which is equal to the last element, decrease it by one, and then set all elements to the right to  $n - 1$ .

```

< FTensor::decrement code 227 > ≡
void FTensor::decrement(IntSequence &v, int nv)
{
    int i = v.size() - 1;
    while (i > 0 ∧ v[i - 1] ≡ v[i]) i--;
    v[i]--;
    for (int j = i + 1; j < v.size(); j++) v[j] = nv - 1;
}

```

This code is used in section 218.

**228.** This calculates order of the given index of our ordering of indices. In order to understand how it works, let us take number of variables  $n$  and dimension  $k$ , and write down all the possible combinations of indices in our ordering. For example for  $n = 4$  and  $k = 3$ , the sequence looks as:

```

000    111    222    333
001    112    223
002    113    233
003    122
011    123
012    133
013
022
023
033

```

Now observe, that a number of sequences starting with zero is the same as total number of sequences with the same number of variables but with dimension minus one. More generally, if  $S_{n,k}$  denotes number of indices of  $n$  variables and dimension  $k$ , then the number of indices beginning with  $m$  is exactly  $S_{n-m,k-1}$ . This is because  $m$  can be subtracted from all items, and we obtain sequence of indices of  $n - m$  variables. So we have formula:

$$S_{n,k} = S_{n,k-1} + S_{n-1,k-1} + \dots + S_{1,k-1}$$

Now it is easy to calculate offset of index of the form  $(m, \dots, m)$ . It is a sum of all above it, this is  $S_{n,k-1} + \dots + S_{n-m,k-1}$ . We know that  $S_{n,k} = \binom{n+k-1}{k}$ . Using above formula, we can calculate offset of  $(m, \dots, m)$  as

$$\binom{n+k-1}{k} - \binom{n-m+k-1}{k}$$

The offset of general index  $(m_1, m_2, \dots, m_k)$  is calculated recursively, since it is offset of  $(m_1, \dots, m_1)$  for  $n$  variables plus offset of  $(m_2 - m_1, m_3 - m_1, \dots, m_k - m_1)$  for  $n - m_1$  variables.

⟨ **FTensor::getOffsetRecurse** code 228 ⟩ ≡

```

int FTensor::getOffsetRecurse(IntSequence &v, int nv)
{
    if (v.size() ≡ 0) return 0;
    int prefix = v.getPrefixLength();
    int m = v[0];
    int k = v.size();
    int s1 = noverk(nv + k - 1, k) - noverk(nv - m + k - 1, k);
    IntSequence subv(v, prefix, k);
    subv.add(-m);
    int s2 = getOffsetRecurse(subv, nv - m);
    return s1 + s2;
}

```

This code is used in section 218.

**229.** End of `tensor.cpp` file.

**230. Full symmetry tensor.** Start of `fs_tensor.h` file.

Here we define folded and unfolded tensors for full symmetry. All tensors from here are identifying the multidimensional index with columns.

```
#ifndef FS_TENSOR_H
#define FS_TENSOR_H
#include "tensor.h"
#include "symmetry.h"
class FGSTensor;
class UGSTensor;
class FRSingleTensor;
class FSSparseTensor;

⟨FFSTensor class declaration 231⟩;
⟨UFSTensor class declaration 233⟩;
#endif
```

**231.** Folded tensor with full symmetry maintains only information about number of symmetrical variables  $nv$ . Further, we implement what is left from the super class **FTensor**.

We implement *getOffset* which should be used with care since its complexity.

We implement a method adding a given general symmetry tensor to the full symmetry tensor supposing the variables of the general symmetry tensor are stacked giving only one variable of the full symmetry tensor. For instance, if  $x = [y^T, u^T]^T$ , then we can add tensor  $[g_{y^2u}]$  to tensor  $g_{x^3}$ . This is done in method *addSubTensor*. Consult ⟨**FGSTensor** class declaration 257⟩ to know what is general symmetry tensor.

```
⟨FFSTensor class declaration 231⟩ ≡
class UFSTensor;
class FFSTensor : public FTensor {
    int nv;
public:
    ⟨FFSTensor constructor declaration 232⟩;
    void increment(IntSequence &v) const;
    void decrement(IntSequence &v) const;
    UTensor &unfold() const;
    Symmetry getSym() const
    { return Symmetry(dimen()); }
    int getOffset(const IntSequence &v) const;
    void addSubTensor(const FGSTensor &t);
    int nvar() const
    { return nv; }
    static int calcMaxOffset(int nvar, int d);
};
```

This code is cited in section 1.

This code is used in section 230.

**232.** Here are the constructors. The second constructor constructs a tensor by one-dimensional contraction from the higher dimensional tensor  $t$ . This is, it constructs a tensor

$$[g_{y^n}]_{\alpha_1 \dots \alpha_n} = [t_{y^{n+1}}]_{\alpha_1 \dots \alpha_n \beta} [x]^\beta$$

See implementation  $\langle$  **FFSTensor** contraction constructor 237  $\rangle$  for details.

The next constructor converts from sparse tensor (which is fully symmetric and folded by nature).

The fourth constructs object from unfolded fully symmetric.

The fifth constructs a subtensor of selected rows.

$\langle$  **FFSTensor** constructor declaration 232  $\rangle \equiv$

```
FFSTensor(int r, int nvar, int d)
: FTensor(along_col, IntSequence(d, nvar), r, calcMaxOffset(nvar, d), d), nv(nvar) { }

FFSTensor(const FFSTensor &t, const ConstVector &x);
FFSTensor(const FSParseTensor &t);
FFSTensor(const FFSTensor &ft)
: FTensor(ft), nv(ft.nv) { }
FFSTensor(const UFSTensor &ut);
FFSTensor(int first_row, int num, FFSTensor &t)
: FTensor(first_row, num, t), nv(t.nv) { }
```

This code is used in section 231.

**233.** Unfolded fully symmetric tensor is almost the same in structure as **FFSTensor**, but the method *unfoldData*. It takes columns which also exist in folded version and copies them to all their symmetrical locations. This is useful when constructing unfolded tensor from folded one.

$\langle$  **UFSTensor** class declaration 233  $\rangle \equiv$

```
class UFSTensor : public UTensor {
    int nv;
public:
     $\langle$  UFSTensor constructor declaration 234  $\rangle$ ;
    void increment(IntSequence &v) const;
    void decrement(IntSequence &v) const;
    FTensor &fold() const;
    Symmetry getSym() const
    { return Symmetry(dimen()); }
    int getOffset(const IntSequence &v) const;
    void addSubTensor(const UGSTensor &t);
    int nvar() const
    { return nv; }
    static int calcMaxOffset(int nvar, int d)
    { return power(nvar, d); }
private:
    void unfoldData();
};
```

This code is cited in section 1.

This code is used in section 230.

**234.**

```

< UFSensor constructor declaration 234 > ≡
  UFSensor(int r, int nvar, int d)
  : UTensor(along_col, IntSequence(d, nvar), r, calcMaxOffset(nvar, d), d), nv(nvar) {}
  UFSensor(const UFSensor &t, const ConstVector &x);
  UFSensor(const UFSensor &ut)
  : UTensor(ut, nv(ut.nv)) {}
  UFSensor(const FFSTensor &ft);
  UFSensor(int first_row, int num, UFSensor &t)
  : UTensor(first_row, num, t), nv(t.nv) {}

```

This code is used in section 233.

**235.** End of fs\_tensor.h file.**236.** Start of fs\_tensor.cpp file.

```

#include "fs_tensor.h"
#include "gs_tensor.h"
#include "sparse_tensor.h"
#include "rfs_tensor.h"
#include "tl_exception.h"
< FFSTensor contraction constructor 237 >;
< FFSTensor::calcMaxOffset code 238 >;
< FFSTensor conversion from sparse 239 >;
< FFSTensor conversion from unfolded 240 >;
< FFSTensor::unfold code 241 >;
< FFSTensor::increment code 242 >;
< FFSTensor::decrement code 243 >;
< FFSTensor::getOffset code 244 >;
< FFSTensor::addSubTensor code 245 >;
< UFSensor contraction constructor 247 >;
< UFSensor conversion from folded 248 >;
< UFSensor::fold code 249 >;
< UFSensor increment and decrement 250 >;
< UFSensor::getOffset code 251 >;
< UFSensor::addSubTensor code 252 >;
< UFSensor::unfoldData code 253 >;

```

**237.** This constructs a fully symmetric tensor as given by the contraction:

$$[g_{y^n}]_{\alpha_1 \dots \alpha_n} = [t_{y^{n+1}}]_{\alpha_1 \dots \alpha_n \beta} [x]^\beta$$

We go through all columns of output tensor  $[g]$  and for each column we cycle through all variables, insert a variable to the column coordinates obtaining a column of tensor  $[t]$ . the column is multiplied by an appropriate item of  $x$  and added to the column of  $[g]$  tensor.

⟨ **FFSTensor** contraction constructor 237 ⟩ ≡

```
FFSTensor::FFSTensor(const FFSTensor &t, const ConstVector &x)
: FTensor(along_col, IntSequence(t.dimen() - 1, t.nvar()), t.nrows(), calcMaxOffset(t.nvar(),
    t.dimen() - 1), t.dimen() - 1), nv(t.nvar())) {
    TL_RAISE_IF(t.dimen() < 1, "Wrong_dimension_for_tensor_contraction_of_FFSTensor");
    TL_RAISE_IF(t.nvar() ≠ x.length(),
        "Wrong_number_of_variables_for_tensor_contraction_of_FFSTensor");
    zeros();
    for (Tensor::index to = begin(); to ≠ end(); ++to) {
        for (int i = 0; i < nvar(); i++) {
            IntSequence from_ind(i, to.getCoor());
            Tensor::index from(&t, from_ind);
            addColumn(x[i], t, *from, *to);
        }
    }
}
```

This code is cited in sections 232 and 247.

This code is used in section 236.

**238.** This returns number of indices for folded tensor with full symmetry. Let  $n$  be a number of variables  $nvar$  and  $d$  the dimension  $dim$ . Then the number of indices is  $\binom{n+d-1}{d}$ .

⟨ **FFSTensor**::calcMaxOffset code 238 ⟩ ≡

```
int FFSTensor::calcMaxOffset(int nvar, int d)
{
    if (nvar ≡ 0 ∧ d ≡ 0) return 1;
    if (nvar ≡ 0 ∧ d > 0) return 0;
    return noverk(nvar + d - 1, d);
}
```

This code is used in section 236.

**239.** The conversion from sparse tensor is clear. We go through all the tensor and write to the dense what is found.

⟨ **FFSTensor** conversion from sparse 239 ⟩ ≡

```
FFSTensor::FFSTensor(const FSSparseTensor &t)
: FTensor(along_col, IntSequence(t.dimen(), t.nvar()), t.nrows(), calcMaxOffset(t.nvar(), t.dimen()),
    t.dimen()), nv(t.nvar())) {
    zeros();
    for (FSSparseTensor::const_iterator it = t.getMap().begin(); it ≠ t.getMap().end(); ++it) {
        index ind(this, (*it).first);
        get((*it).second.first, *ind) = (*it).second.second;
    }
}
```

This code is used in section 236.



**240.** The conversion from unfolded copies only columns of respective coordinates. So we go through all the columns in the folded tensor (this), make an index of the unfolded vector from coordinates, and copy the column.

```

< FFSTensor conversion from unfolded 240 > ≡
FFSTensor::FFSTensor(const UFSTensor &ut)
: FTensor(along_col, IntSequence(ut.dimen(), ut.nvar()), ut.nrows(), calcMaxOffset(ut.nvar(),
    ut.dimen()), ut.dimen(), nv(ut.nvar())) {
    for (index in = begin(); in ≠ end(); ++in) {
        index src(&ut, in.getCoor());
        copyColumn(ut, *src, *in);
    }
}

```

This code is used in section 236.

**241.** Here just make a new instance and return the reference.

```

< FFSTensor::unfold code 241 > ≡
UTensor &FFSTensor::unfold() const
{
    return *(new UFSTensor(*this));
}

```

This code is used in section 236.

**242.** Incrementing is easy. We have to increment by calling static method **UTensor**::*increment* first. In this way, we have coordinates of unfolded tensor. Then we have to skip to the closest folded index which corresponds to monotoneizing the integer sequence.

```

< FFSTensor::increment code 242 > ≡
void FFSTensor::increment(IntSequence &v) const
{
    TL_RAISE_IF(v.size() ≠ dimen(), "Wrong_input/output_vector_size_in_FFSTensor::increment");
    UTensor::increment(v, nv);
    v.monotone();
}

```

This code is used in section 236.

**243.** Decrement calls static **FTensor**::*decrement*.

```

< FFSTensor::decrement code 243 > ≡
void FFSTensor::decrement(IntSequence &v) const
{
    TL_RAISE_IF(v.size() ≠ dimen(), "Wrong_input/output_vector_size_in_FFSTensor::decrement");
    FTensor::decrement(v, nv);
}

```

This code is used in section 236.

244.

```

<FFSTensor::getOffset code 244> ≡
int FFSTensor::getOffset(const IntSequence &v) const
{
    TL_RAISE_IF(v.size() ≠ dimen(), "Wrong_input_vector_size_in_FFSTensor::getOffset");
    return FTensor::getOffset(v, nv);
}

```

This code is used in section 236.

245. Here we add a general symmetry tensor to the (part of) full symmetry tensor provided that the unique variable of the full symmetry tensor is a stack of variables from the general symmetry tensor.

We check for the dimensions and number of variables. Then we calculate a shift of coordinates when going from the general symmetry tensor to full symmetry (it corresponds to shift of coordinates induces by stacking the variables). Then we add the appropriate columns by going through the columns in general symmetry, adding the shift and sorting.

```

<FFSTensor::addSubTensor code 245> ≡
void FFSTensor::addSubTensor(const FGSTensor &t)
{
    TL_RAISE_IF(dimen() ≠ t.getDims().dimen(),
        "Wrong_dimensions_for_FFSTensor::addSubTensor");
    TL_RAISE_IF(nvar() ≠ t.getDims().getNVS().sum(), "Wrong_nvs_for_FFSTensor::addSubTensor");
    <set shift for addSubTensor 246>;
    for (Tensor::index ind = t.begin(); ind ≠ t.end(); ++ind) {
        IntSequence c(ind.getCoor());
        c.add(1, shift);
        c.sort();
        Tensor::index tar(this, c);
        addColumn(t, *ind, *tar);
    }
}

```

This code is cited in section 252.

This code is used in section 236.

246.

```

<set shift for addSubTensor 246> ≡
IntSequence shift_pre(t.getSym().num(), 0);
for (int i = 1; i < t.getSym().num(); i++) shift_pre[i] = shift_pre[i - 1] + t.getDims().getNVS()[i - 1];
IntSequence shift(t.getSym(), shift_pre);

```

This code is used in sections 245 and 252.

**247.** This is a bit more straightforward than  $\langle \mathbf{FFSTensor}$  contraction constructor 237. We do not add column by column but we do it by submatrices due to regularity of the unfolded tensor.

```

< UFSTensor contraction constructor 247 > ≡
UFSTensor::UFSTensor(const UFSTensor &t, const ConstVector &x)
: UTensor(along_col, IntSequence(t.dimen() - 1, t.nvar()), t.nrows(), calcMaxOffset(t.nvar(),
    t.dimen() - 1), t.dimen() - 1), nv(t.nvar())) {
    TL_RAISE_IF(t.dimen() < 1, "Wrong_dimension_for_tensor_contraction_of_UFSTensor");
    TL_RAISE_IF(t.nvar() ≠ x.length(),
        "Wrong_number_of_variables_for_tensor_contraction_of_UFSTensor");
    zeros();
    for (int i = 0; i < ncols(); i++) {
        ConstTwoDMatrix tpart(t, i * nvar(), nvar());
        Vector outcol(*this, i);
        tpart.multVec(outcol, x);
    }
}

```

This code is used in section 236.

**248.** Here we convert folded full symmetry tensor to unfolded. We copy all columns of folded tensor, and then call *unfoldData()*.

```

< UFSTensor conversion from folded 248 > ≡
UFSTensor::UFSTensor(const FFSTensor &ft)
: UTensor(along_col, IntSequence(ft.dimen(), ft.nvar()), ft.nrows(), calcMaxOffset(ft.nvar(),
    ft.dimen()), ft.dimen()), nv(ft.nvar())) {
    for (index src = ft.begin(); src ≠ ft.end(); ++src) {
        index in(this, src.getCoor());
        copyColumn(ft, *src, *in);
    }
    unfoldData();
}

```

This code is used in section 236.

**249.** Here we just return a reference to new instance of folded tensor.

```

< UFSTensor::fold code 249 > ≡
FTensor &UFSTensor::fold() const
{
    return *(new FFSTensor(*this));
}

```

This code is used in section 236.

**250.** Here we just call **UTensor** respective static methods.

```

⟨ UFSTensor increment and decrement 250 ⟩ ≡
  void UFSTensor::increment(IntSequence &v) const
  {
    TL_RAISE_IF(v.size() ≠ dimen(), "Wrong_input/output_vector_size_in_UFSTensor::increment");
    UTensor::increment(v, nv);
  }
  void UFSTensor::decrement(IntSequence &v) const
  {
    TL_RAISE_IF(v.size() ≠ dimen(), "Wrong_input/output_vector_size_in_UFSTensor::decrement");
    UTensor::decrement(v, nv);
  }

```

This code is used in section 236.

**251.**

```

⟨ UFSTensor::getOffset code 251 ⟩ ≡
  int UFSTensor::getOffset(const IntSequence &v) const
  {
    TL_RAISE_IF(v.size() ≠ dimen(), "Wrong_input_vector_size_in_UFSTensor::getOffset");
    return UTensor::getOffset(v, nv);
  }

```

This code is used in section 236.

**252.** This is very similar to ⟨**FFSTensor**::*addSubTensor* code 245⟩. The only difference is the addition. We go through all columns in the full symmetry tensor and cancel the shift. If the coordinates after the cancellation are positive, we find the column in the general symmetry tensor, and add it.

```

⟨ UFSTensor::addSubTensor code 252 ⟩ ≡
  void UFSTensor::addSubTensor(const UGSTensor &t)
  {
    TL_RAISE_IF(dimen() ≠ t.getDims().dimen(),
      "Wrong_dimensions_for_UFSTensor::addSubTensor");
    TL_RAISE_IF(nvar() ≠ t.getDims().getNVS().sum(), "Wrong_nvs_for_UFSTensor::addSubTensor");
    ⟨ set shift for addSubTensor 246 ⟩;
    for (Tensor::index tar = begin(); tar ≠ end(); ++tar) {
      IntSequence c(tar.getCoor());
      c.sort();
      c.add(-1, shift);
      if (c.isPositive() ∧ c.less(t.getDims().getNVX())) {
        Tensor::index from(&t, c);
        addColumn(t, *from, *tar);
      }
    }
  }

```

This code is used in section 236.

**253.** Here we go through all columns, find a column of folded index, and then copy the column data. Finding the index is done by sorting the integer sequence.

```
< UFSTensor::unfoldData code 253 > ≡
void UFSTensor::unfoldData()
{
    for (index in = begin(); in ≠ end(); ++in) {
        IntSequence v(in.getCoor());
        v.sort();
        index tmp(this, v);
        copyColumn(*tmp, *in);
    }
}
```

This code is used in section 236.

**254.** End of `fs_tensor.cpp` file.

**255. General symmetry tensor.** Start of `gs_tensor.h` file.

Here we define tensors for general symmetry. All tensors from here are identifying the multidimensional index with columns. Thus all symmetries regard to columns. The general symmetry here is not the most general. It captures all symmetries of indices which are given by continuous partitioning of indices. Two items are symmetric if they belong to the same group. The continuity implies that if two items belong to one group, then all items between them belong to that group. This continuous partitioning of indices is described by **Symmetry** class.

The dimension of the tensors here are described (besides the symmetry) also by number of variables for each group. This is dealt in the class for tensor dimensions defined also here.

```
#ifndef GS_TENSOR_H
#define GS_TENSOR_H
#include "tensor.h"
#include "fs_tensor.h"
#include "symmetry.h"
#include "rfs_tensor.h"
class FGSTensor;
class UGSTensor;
class FSSparseTensor;
< TensorDimens class declaration 256 >;
< FGSTensor class declaration 257 >;
< UGSTensor class declaration 259 >;
#endif
```

**256.** This class encapsulates symmetry information for the general symmetry tensor. It maintains a vector of variable numbers *nvs*, and symmetry *sym*. For example, let the symmetry be  $y^2u^3$ , and variable numbers be 10 for *y*, and 5 for *u*. Then the *nvs* is (10, 5), and *sym* is (2, 3). Also it maintains *nvmax* unfolded *nvs* with respect to the symmetry, this is (10, 10, 5, 5, 5).

The constructors of **TensorDimens** are clear and pretty intuitive but the constructor which is used for slicing fully symmetric tensor. It constructs the dimensions from the partitioning of variables of fully symmetric tensor. Let the partitioning be, for instance,  $(a, b, c, d)$ , where  $(n_a, n_b, n_c, n_d)$  are lengths of the partitions. Let one want to get a slice only of the part of the fully symmetric tensor corresponding to indices of the form  $b^2d^3$ . This corresponds to the symmetry  $a^0b^2c^0d^3$ . So, the dimension of the slice would be also  $(n_a, n_b, n_c, n_d)$  for number of variables and  $(0, 2, 0, 3)$  for the symmetry. So we provide the constructor which takes sizes of partitions  $(n_a, n_b, n_c, n_d)$  as **IntSequence**, and indices of picked partitions, in our case  $(1, 1, 3, 3, 3)$ , as **IntSequence**.

The class is able to calculate number of offsets (columns or rows depending what matrix coordinate we describe) in unfolded and folded tensors with the given symmetry.

⟨ **TensorDimens** class declaration 256 ⟩ ≡

```
class TensorDimens {
protected:
    IntSequence nvs;
    Symmetry sym;
    IntSequence nvmax;
public:
    TensorDimens(const Symmetry &s, const IntSequence &nvars)
    : nvs(nvars), sym(s), nvmax(sym, nvs) {}

    TensorDimens(int nvar, int dimen)
    : nvs(1), sym(dimen), nvmax(dimen, nvar) { nvs[0] = nvar; }

    TensorDimens(const TensorDimens &td)
    : nvs(td.nvs), sym(td.sym), nvmax(td.nvmax) {}

    virtual ~TensorDimens() {}

    TensorDimens(const IntSequence &ss, const IntSequence &coor);

    const TensorDimens &operator=(const TensorDimens &td)
    { nvs = td.nvs; sym = td.sym; nvmax = td.nvmax; return *this; }

    bool operator==(const TensorDimens &td) const
    { return nvs == td.nvs & sym == td.sym; }

    bool operator!=(const TensorDimens &td) const
    { return !operator==(td); }

    int dimen() const
    { return sym.dimen(); }

    int getNVX(int i) const
    { return nvmax[i]; }

    const IntSequence &getNVS() const
    { return nvs; }

    const IntSequence &getNVX() const
    { return nvmax; }

    const Symmetry &getSym() const
    { return sym; }

    int calcUnfoldMaxOffset() const;
    int calcFoldMaxOffset() const;
    int calcFoldOffset(const IntSequence &v) const;
```

```
void decrement(IntSequence &v) const;
};
```

This code is cited in section 263.

This code is used in section 255.

**257.** Here is a class for folded general symmetry tensor. It only contains tensor dimensions, it defines types for indices, implement virtual methods of super class **FTensor**.

We add a method *contractAndAdd* which performs a contraction of one variable in the tensor. This is, for instance

$$[r_{x^i z^k}]_{\alpha_1 \dots \alpha_i \gamma_1 \dots \gamma_k} = [t_{x^i y^j z^k}]_{\alpha_1 \dots \alpha_i \beta_1 \dots \beta_j \gamma_1 \dots \gamma_k} [c]^{\beta_1 \dots \beta_j}$$

Also we add *getOffset* which should be used with care.

⟨ **FGSTensor** class declaration 257 ⟩ ≡

```
class GSSparseTensor;
class FGSTensor : public FTensor {
    friend class UGSTensor;
    const TensorDimens tdims;
public:
    ⟨ FGSTensor constructor declarations 258 ⟩;
    virtual ~FGSTensor() {}
    void increment(IntSequence &v) const;
    void decrement(IntSequence &v) const
    { tdims.decrement(v); }
    UTensor &unfold() const;
    const TensorDimens &getDims() const
    { return tdims; }
    const Symmetry &getSym() const
    { return getDims().getSym(); }
    void contractAndAdd(int i, FGSTensor &out, const FRSingleTensor &col) const;
    int getOffset(const IntSequence &v) const
    { return tdims.calcFoldOffset(v); }
};
```

This code is cited in sections 1 and 231.

This code is used in section 255.

**258.** These are standard constructors followed by two slicing. The first constructs a slice from the sparse, the second from the dense (both fully symmetric). Next constructor is just a conversion from *GSSparseTensor*. The last constructor allows for in-place conversion from **FFSTensor** to **FGSTensor**.

```

< FGSTensor constructor declarations 258 > =
FGSTensor(int r, const TensorDimens &td)
: FTensor(along_col, td.getNVX( ), r, td.calcFoldMaxOffset( ), td.dimen( ), tdims(td) { }

FGSTensor(const FGSTensor &ft)
: FTensor(ft, tdims(ft.tdims) { }

FGSTensor(const UGSTensor &ut);

FGSTensor(int first_row, int num, FGSTensor &t)
: FTensor(first_row, num, t, tdims(t.tdims) { }

FGSTensor(const FSSparseTensor &t, const IntSequence &ss, const IntSequence &coor, const
TensorDimens &td);
FGSTensor(const FFSTensor &t, const IntSequence &ss, const IntSequence &coor, const
TensorDimens &td);
FGSTensor(const GSSparseTensor &sp);
FGSTensor(FFSTensor &t)
: FTensor(0, t.nrows( ), t, tdims(t.nvar( ), t.dimen( )) { }

```

This code is used in section 257.

**259.** Besides similar things that has **FGSTensor**, we have here also method *unfoldData*, and helper method *getFirstIndexOf* which corresponds to sorting coordinates in fully symmetric case (here the action is more complicated, so we put it to the method).

```

< UGSTensor class declaration 259 > =
class UGSTensor : public UTensor {
    friend class FGSTensor;
    const TensorDimens tdims;

public:
    < UGSTensor constructor declarations 260 >;
    virtual ~UGSTensor( ) { }

    void increment(IntSequence &v) const;
    void decrement(IntSequence &v) const;
    FTensor &fold( ) const;

    const TensorDimens &getDims( ) const
    { return tdims; }

    const Symmetry &getSym( ) const
    { return getDims( ).getSym( ); }

    void contractAndAdd(int i, UGSTensor &out, const URSingleTensor &col) const;
    int getOffset(const IntSequence &v) const;

private:
    void unfoldData( );

public:
    index getFirstIndexOf(const index &in) const;
};

```

This code is cited in section 1.

This code is used in section 255.



**260.** These are standard constructors. The last two constructors are slicing. The first makes a slice from fully symmetric sparse, the second from fully symmetric dense unfolded tensor. The last constructor allows for in-place conversion from **UFSTensor** to **UGSTensor**.

```

< UGSTensor constructor declarations 260 > ≡
  UGSTensor(int r, const TensorDimens &td)
  : UTensor(along_col, td.getNVX( ), r, td.calcUnfoldMaxOffset( ), td.dimen( ), tdims(td) { }
  UGSTensor(const UGSTensor &ut)
  : UTensor(ut, tdims(ut.tdims) { }
  UGSTensor(const FGSTensor &ft);
  UGSTensor(int first_row, int num, UGSTensor &t)
  : UTensor(first_row, num, t, tdims(t.tdims) { }
  UGSTensor(const FSSparseTensor &t, const IntSequence &ss, const IntSequence &coor, const
    TensorDimens &td);
  UGSTensor(const UFSTensor &t, const IntSequence &ss, const IntSequence &coor, const
    TensorDimens &td);
  UGSTensor(UFSTensor &t)
  : UTensor(0, t.nrows( ), t, tdims(t.nvar( ), t.dimen( )) { }

```

This code is used in section 259.

**261.** End of `gs_tensor.h` file.

**262.** Start of `gs_tensor.cpp` file.

```

#include "gs_tensor.h"
#include "sparse_tensor.h"
#include "tl_exception.h"
#include "kron_prod.h"
  < TensorDimens constructor code 263 >;
  < TensorDimens::calcUnfoldMaxOffset code 264 >;
  < TensorDimens::calcFoldMaxOffset code 265 >;
  < TensorDimens::calcFoldOffset code 266 >;
  < TensorDimens::decrement code 267 >;
  < FGSTensor conversion from UGSTensor 270 >;
  < FGSTensor slicing from FSSparseTensor 271 >;
  < FGSTensor slicing from FFSTensor 273 >;
  < FGSTensor conversion from GSSparseTensor 274 >;
  < FGSTensor::increment code 275 >;
  < FGSTensor::unfold code 276 >;
  < FGSTensor::contractAndAdd code 277 >;
  < UGSTensor conversion from FGSTensor 279 >;
  < UGSTensor slicing from FSSparseTensor 280 >;
  < UGSTensor slicing from UFSTensor 281 >;
  < UGSTensor increment and decrement codes 282 >;
  < UGSTensor::fold code 283 >;
  < UGSTensor::getOffset code 284 >;
  < UGSTensor::unfoldData code 285 >;
  < UGSTensor::getFirstIndexOf code 286 >;
  < UGSTensor::contractAndAdd code 287 >;

```

**263.** This constructs the tensor dimensions for slicing. See  $\langle$  **TensorDimens** class declaration 256  $\rangle$  for details.

```

 $\langle$  TensorDimens constructor code 263  $\rangle \equiv$ 
TensorDimens::TensorDimens(const IntSequence &ss, const IntSequence &coord)
: nvs(ss), sym(ss.size(), ""), nvmax(coord.size(), 0) {
    TL_RAISE_IF( $\neg$ coord.isSorted(),
        "Coordinates_not_sorted_in_TensorDimens_slicing_constructor");
    TL_RAISE_IF(coord[0] < 0  $\vee$  coord[coord.size() - 1]  $\geq$  ss.size(),
        "A_coordinate_out_of_stack_range_in_TensorDimens_slicing_constructor");
    for (int i = 0; i < coord.size(); i++) {
        sym[coord[i]]++;
        nvmax[i] = ss[coord[i]];
    }
}

```

This code is used in section 262.

**264.** Number of unfold offsets is a product of all members of *nvmax*.

```

 $\langle$  TensorDimens::calcUnfoldMaxOffset code 264  $\rangle \equiv$ 
int TensorDimens::calcUnfoldMaxOffset() const
{
    return nvmax.mult();
}

```

This code is used in section 262.

**265.** Number of folded offsets is a product of all unfold offsets within each equivalence class of the symmetry.

```

 $\langle$  TensorDimens::calcFoldMaxOffset code 265  $\rangle \equiv$ 
int TensorDimens::calcFoldMaxOffset() const
{
    int res = 1;
    for (int i = 0; i < nvs.size(); i++) {
        if (nvs[i]  $\equiv$  0  $\wedge$  sym[i] > 0) return 0;
        if (sym[i] > 0) res *= Tensor::noverk(nvs[i] + sym[i] - 1, sym[i]);
    }
    return res;
}

```

This code is used in section 262.

**266.** Here we implement offset calculation for folded general symmetry tensor. The offset of a given sequence is calculated by breaking the sequence to subsequences according to the symmetry. The offset is orthogonal with respect to the blocks, this means that indexing within the blocks is independent. If there are two blocks, for instance, then the offset will be an offset within the outer block (the first) multiplied with all offsets of the inner block (last) plus an offset within the second block.

Generally, the resulting offset  $r$  will be

$$\sum_{i=1}^s r_i \cdot \left( \prod_{j=i+1}^s n_j \right),$$

where  $s$  is a number of blocks ( $getSym().num()$ ),  $r_i$  is an offset within  $i$ -th block, and  $n_j$  is a number of all offsets in  $j$ -th block.

In the code, we go from the innermost to the outermost, maintaining the product in  $pow$ .

```

⟨ TensorDims::calcFoldOffset code 266 ⟩ ≡
int TensorDims::calcFoldOffset(const IntSequence & $v$ ) const
{
    TL_RAISE_IF( $v.size() \neq dimen()$ , "Wrong_input_vector_size_in_TensorDims::getFoldOffset");
    int  $res = 0$ ;
    int  $pow = 1$ ;
    int  $blstart = v.size()$ ;
    for (int  $ibl = getSym().num() - 1$ ;  $ibl \geq 0$ ;  $ibl--$ ) {
        int  $bldim = getSym()[ibl]$ ;
        if ( $bldim > 0$ ) {
             $blstart -= bldim$ ;
            int  $blnvar = getNVX()[blstart]$ ;
            IntSequence  $subv(v, blstart, blstart + bldim)$ ;
             $res += \mathbf{FTensor}::getOffset(subv, blnvar) * pow$ ;
             $pow *= \mathbf{FFTENSOR}::calcMaxOffset(blnvar, bldim)$ ;
        }
    }
    TL_RAISE_IF( $blstart \neq 0$ , "Error_in_tracing_symmetry_in_TensorDims::getFoldOffset");
    return  $res$ ;
}

```

This code is used in section 262.

**267.** In order to find the predecessor of index within folded generally symmetric tensor, note, that a decrease action in  $i$ -th partition of symmetric indices can happen only if all indices in all subsequent partitions are zero. Then the decrease action of whole the index consists of decrease action of the first nonzero partition from the right, and setting these trailing zero partitions to their maximum indices.

So we set *iblock* to the number of last partitions. During the execution, *block\_first*, and *block\_last* will point to the first element of *iblock* and, first element of following block.

Then we check for all trailing zero partitions, set them to their maximums and return *iblock* to point to the first non-zero partition (or the first partition). Then for this partition, we decrease the index (fully symmetric within that partition).

```

< TensorDimens::decrement code 267 > ≡
void TensorDimens::decrement(IntSequence &v) const
{
    TL_RAISE_IF(getNVX()<size() ≠ v<size(),
        "Wrong_size_of_input/output_sequence_in_TensorDimens::decrement");
    int iblock = getSym()<num() - 1;
    int block_last = v<size();
    int block_first = block_last - getSym()[iblock];
    < check for zero trailing blocks 268 >;
    < decrease the non-zero block 269 >;
}

```

This code is used in section 262.

**268.**

```

< check for zero trailing blocks 268 > ≡
while (iblock > 0 ∧ v[block_last - 1] ≡ 0) {
    for (int i = block_first; i < block_last; i++) v[i] = getNVX(i);    /* equivalent to nvs[iblock] */
    iblock--;
    block_last = block_first;
    block_first -= getSym()[iblock];
}

```

This code is used in section 267.

**269.**

```

< decrease the non-zero block 269 > ≡
IntSequence vtmp(v, block_first, block_last);
FTensor::decrement(vtmp, getNVX(block_first));

```

This code is used in section 267.

**270.** Here we go through columns of folded, calculate column of unfolded, and copy data.

```

< FGSTensor conversion from UGSTensor 270 > ≡
FGSTensor::FGSTensor(const UGSTensor &ut)
: FTensor(along_col, ut<tdims><getNVX>(), ut<nrows>(), ut<tdims><calcFoldMaxOffset>(), ut<dimen>()),
    tdims(ut<tdims>) {
    for (index ti = begin(); ti ≠ end(); ++ti) {
        index ui(&ut, ti<getCoor>());
        copyColumn(ut, *ui, *ti);
    }
}

```

This code is used in section 262.

**271.** Here is the code of slicing constructor from the sparse tensor. We first calculate coordinates of first and last index of the slice within the sparse tensor (these are *lb* and *ub*), and then we iterate through all items between them (in lexicographical ordering of sparse tensor), and check whether an item is between the *lb* and *ub* in Cartesian ordering (this corresponds to belonging to the slices). If it belongs, then we subtract the lower bound *lb* to obtain coordinates in the **this** tensor and we copy the item.

⟨ **FGSTensor** slicing from **FSSparseTensor** 271 ⟩ ≡

```

FGSTensor::FGSTensor(const FSSparseTensor &t, const IntSequence &ss, const IntSequence
    &coord, const TensorDimens &td)
: FTensor(along_col, td.getNVX( ), t.nrows( ), td.calcFoldMaxOffset( ), td.dimen( ), tdims(td) {
    ⟨ set lb and ub to lower and upper bounds of indices 272 ⟩;
    zeros( );
    FSSparseTensor::const_iterator lbi = t.getMap( ).lower_bound(lb);
    FSSparseTensor::const_iterator ubi = t.getMap( ).upper_bound(ub);
    for (FSSparseTensor::const_iterator run = lbi; run ≠ ubi; ++run) {
        if (lb.lessEq((*run).first) ∧ (*run).first.lessEq(ub)) {
            IntSequence c((*run).first);
            c.add(-1, lb);
            Tensor::index ind(this, c);
            TL_RAISE_IF(*ind < 0 ∨ *ind ≥ ncols( ),
                "Internal_error_in_slicing_constructor_of_FGSTensor");
            get((*run).second.first, *ind) = (*run).second.second;
        }
    }
}

```

This code is cited in sections 273 and 361.

This code is used in section 262.

**272.** Here we first set *s\_offsets* to offsets of partitions whose lengths are given by *ss*. So *s\_offsets* is a cumulative sum of *ss*.

Then we create *lb* to be coordinates of the possibly first index from the slice, and *ub* to be coordinates of possibly last index of the slice.

⟨ set *lb* and *ub* to lower and upper bounds of indices 272 ⟩ ≡

```

IntSequence s_offsets(ss.size( ), 0);
for (int i = 1; i < ss.size( ); i++) s_offsets[i] = s_offsets[i - 1] + ss[i - 1];
IntSequence lb(coord.size( ));
IntSequence ub(coord.size( ));
for (int i = 0; i < coord.size( ); i++) {
    lb[i] = s_offsets[coord[i]];
    ub[i] = s_offsets[coord[i]] + ss[coord[i]] - 1;
}

```

This code is cited in section 362.

This code is used in sections 271 and 273.

**273.** The code is similar to `<FGSTensor slicing from FSSparseTensor 271>`.

```

<FGSTensor slicing from FFSTensor 273> ≡
  FGSTensor::FGSTensor(const FFSTensor &t, const IntSequence &ss, const IntSequence
    &coord, const TensorDimens &td)
: FTensor(along_col, t.getNVX(), t.nrows(), td.calcFoldMaxOffset(), td.dimen()), tdims(td) {
  if (ncols() == 0) return;
  <set lb and ub to lower and upper bounds of indices 272>;
  zeros();
  Tensor::index lbi(&t, lb);
  Tensor::index ubi(&t, ub);
  ++ubi;
  for (Tensor::index run = lbi; run != ubi; ++run) {
    if (lb.lessEq(run.getCoor()) ^ run.getCoor().lessEq(ub)) {
      IntSequence c(run.getCoor());
      c.add(-1, lb);
      Tensor::index ind(this, c);
      TL_RAISE_IF(*ind < 0 || *ind ≥ ncols(),
        "Internal_error_in_slicing_constructor_of_FGSTensor");
      copyColumn(t, *run, *ind);
    }
  }
}

```

This code is used in section 262.

**274.**

```

<FGSTensor conversion from GSSparseTensor 274> ≡
  FGSTensor::FGSTensor(const GSSparseTensor &t)
: FTensor(along_col, t.getDims().getNVX(), t.nrows(), t.getDims().calcFoldMaxOffset(), t.dimen()),
  tdims(t.getDims()) {
  zeros();
  for (FSSparseTensor::const_iterator it = t.getMap().begin(); it != t.getMap().end(); ++it) {
    index ind(this, (*it).first);
    get((*it).second.first, *ind) = (*it).second.second;
  }
}

```

This code is used in section 262.

**275.** First we increment as unfolded, then we must monotonize within partitions defined by the symmetry. This is done by `IntSequence::pmonotone`.

```

<FGSTensor::increment code 275> ≡
  void FGSTensor::increment(IntSequence &v) const
  {
    TL_RAISE_IF(v.size() != dimen(), "Wrong_input/output_vector_size_in_FGSTensor::increment");
    UTensor::increment(v, tdims.getNVX());
    v.pmonotone(tdims.getSym());
  }

```

This code is used in section 262.

**276.** Return unfolded version of the tensor.

```
< FGSTensor::unfold code 276 > ≡
  UTensor &FGSTensor::unfold() const
  {
    return *(new UGSTensor(*this));
  }
```

This code is used in section 262.

**277.** Here we implement the contraction

$$[r_{x^i z^k}]_{\alpha_1 \dots \alpha_i \gamma_1 \dots \gamma_k} = [t_{x^i y^j z^k}]_{\alpha_1 \dots \alpha_i \beta_1 \dots \beta_j \gamma_1 \dots \gamma_k} [c]^{\beta_1 \dots \beta_j}$$

More generally,  $x^i$  and  $z^k$  can represent also general symmetries.

The operation can be rewritten as a matrix product

$$[t_{x^i y^j z^k}] \cdot (I_l \otimes c \otimes I_r)$$

where  $l$  is a number of columns in tensor with symmetry on the left (i.e.  $x^i$ ), and  $r$  is a number of columns in tensor with a symmetry on the right (i.e.  $z^k$ ). The code proceeds accordingly. We first form two symmetries *sym\_left* and *sym\_right*, then calculate the number of columns *dleft*= $l$  and *dright*= $r$ , form the Kronecker product and multiply and add.

The input parameter  $i$  is the order of a variable being contracted starting from 0.

```
< FGSTensor::contractAndAdd code 277 > ≡
  void FGSTensor::contractAndAdd(int i, FGSTensor &out, const FRSingleTensor &col) const
  {
    TL_RAISE_IF(i < 0 ∨ i ≥ getSym().num(), "Wrong_index_for_FGSTensor::contractAndAdd");
    TL_RAISE_IF(getSym()[i] ≠ col.dimen() ∨ tdims.getNVS()[i] ≠ col.nvar(),
      "Wrong_dimensions_for_FGSTensor::contractAndAdd");
    < set sym_left and sym_right to symmetries around i 278 >;
    int dleft = TensorDimens(sym_left, tdims.getNVS()).calcFoldMaxOffset();
    int dright = TensorDimens(sym_right, tdims.getNVS()).calcFoldMaxOffset();
    KronProdAll kp(3);
    kp.setUnit(0, dleft);
    kp.setMat(1, col);
    kp.setUnit(2, dright);
    FGSTensor tmp(out.nrows(), out.getDims());
    kp.mult(*this, tmp);
    out.add(1.0, tmp);
  }
```

This code is cited in section 287.

This code is used in section 262.

**278.** Here we have a symmetry of **this** tensor and we have to set *sym\_left* to the subsymmetry left from the *i*-th variable and *sym\_right* to the subsymmetry right from the *i*-th variable. So we copy first all the symmetry and then put zeros to the left for *sym\_right* and to the right for *sym\_left*.

```

< set sym_left and sym_right to symmetries around i 278 > ≡
  Symmetry sym_left(getSym());
  Symmetry sym_right(getSym());
  for (int j = 0; j < getSym().num(); j++) {
    if (j ≤ i) sym_right[j] = 0;
    if (j ≥ i) sym_left[j] = 0;
  }

```

This code is used in sections 277 and 287.

**279.** Here we go through folded tensor, and each index we convert to index of the unfolded tensor and copy the data to the unfolded. Then we unfold data within the unfolded tensor.

```

< UGSTensor conversion from FGSTensor 279 > ≡
  UGSTensor::UGSTensor(const FGSTensor &ft)
  : UTensor(along_col, ft.tdims.getNVX(), ft.nrows(), ft.tdims.calcUnfoldMaxOffset(), ft.dimen()),
    tdims(ft.tdims) {
    for (index fi = ft.begin(); fi ≠ ft.end(); ++fi) {
      index ui(this, fi.getCoord());
      copyColumn(ft, *fi, *ui);
    }
    unfoldData();
  }

```

This code is used in section 262.

**280.** This makes a folded slice from the sparse tensor and unfolds it.

```

< UGSTensor slicing from FSSparseTensor 280 > ≡
  UGSTensor::UGSTensor(const FSSparseTensor &t, const IntSequence &ss, const IntSequence
    &coord, const TensorDimens &td)
  : UTensor(along_col, td.getNVX(), t.nrows(), td.calcUnfoldMaxOffset(), td.dimen()), tdims(td) {
    if (ncols() ≡ 0) return;
    FGSTensor ft(t, ss, coord, td);
    for (index fi = ft.begin(); fi ≠ ft.end(); ++fi) {
      index ui(this, fi.getCoord());
      copyColumn(ft, *fi, *ui);
    }
    unfoldData();
  }

```

This code is used in section 262.



**281.** This makes a folded slice from dense and unfolds it.

```

< UGSTensor slicing from UFTensor 281 > ≡
    UGSTensor::UGSTensor(const UFTensor &t, const IntSequence &ss, const IntSequence
        &coord, const TensorDimens &td)
: UTensor(along_col, td.getNVX(), t.nrows(), td.calcUnfoldMaxOffset(), td.dimen()), tdims(td) {
    FFTensor folded(t);
    FGSTensor ft(folded, ss, coord, td);
    for (index fi = ft.begin(); fi ≠ ft.end(); ++fi) {
        index ui(this, fi.getCoord());
        copyColumn(ft, *fi, *ui);
    }
    unfoldData();
}

```

This code is used in section 262.

**282.** Clear, just call UTensor static methods.

```

< UGSTensor increment and decrement codes 282 > ≡
    void UGSTensor::increment(IntSequence &v) const
    {
        TL_RAISE_IF(v.size() ≠ dimen(), "Wrong_input/output_vector_size_in_UGSTensor::increment");
        UTensor::increment(v, tdims.getNVX());
    }
    void UGSTensor::decrement(IntSequence &v) const
    {
        TL_RAISE_IF(v.size() ≠ dimen(), "Wrong_input/output_vector_size_in_UGSTensor::decrement");
        UTensor::decrement(v, tdims.getNVX());
    }

```

This code is used in section 262.

**283.** Return a new instance of folded version.

```

< UGSTensor::fold code 283 > ≡
    FTensor &UGSTensor::fold() const
    {
        return *(new FGSTensor(*this));
    }

```

This code is used in section 262.

**284.** Return an offset of a given index.

```

< UGSTensor::getOffset code 284 > ≡
    int UGSTensor::getOffset(const IntSequence &v) const
    {
        TL_RAISE_IF(v.size() ≠ dimen(), "Wrong_input_vector_size_in_UGSTensor::getOffset");
        return UTensor::getOffset(v, tdims.getNVX());
    }

```

This code is used in section 262.

**285.** Unfold all data. We go through all the columns and for each we obtain an index of the first equivalent, and copy the data.

```
< UGSTensor::unfoldData code 285 > ≡
void UGSTensor::unfoldData()
{
    for (index in = begin(); in ≠ end(); ++in) copyColumn(*(getFirstIndexOf(in)),*in);
}
```

This code is used in section 262.

**286.** Here we return the first index which is equivalent in the symmetry to the given index. It is a matter of sorting all the symmetry partitions of the index.

```
< UGSTensor::getFirstIndexOf code 286 > ≡
Tensor::index UGSTensor::getFirstIndexOf(const index &in) const
{
    IntSequence v(in.getCoor());
    int last = 0;
    for (int i = 0; i < tdims.getSym().num(); i++) {
        IntSequence vtmp(v, last, last + tdims.getSym()[i]);
        vtmp.sort();
        last += tdims.getSym()[i];
    }
    return index(this, v);
}
```

This code is used in section 262.

**287.** Here is perfectly same code with the same semantics as in < FGSTensor::contractAndAdd code 277 >.

```
< UGSTensor::contractAndAdd code 287 > ≡
void UGSTensor::contractAndAdd(int i, UGSTensor &out, const URSingleTensor &col) const
{
    TL_RAISE_IF(i < 0 ∨ i ≥ getSym().num(), "Wrong_index_for_UGSTensor::contractAndAdd");
    TL_RAISE_IF(getSym()[i] ≠ col.dimen() ∨ tdims.getNVS()[i] ≠ col.nvar(),
        "Wrong_dimensions_for_UGSTensor::contractAndAdd");
    < set sym_left and sym_right to symmetries around i 278 >;
    int dleft = TensorDimens(sym_left, tdims.getNVS()).calcUnfoldMaxOffset();
    int dright = TensorDimens(sym_right, tdims.getNVS()).calcUnfoldMaxOffset();
    KronProdAll kp(3);
    kp.setUnit(0, dleft);
    kp.setMat(1, col);
    kp.setUnit(2, dright);
    UGSTensor tmp(out.nrows(), out.getDims());
    kp.mult(*this, tmp);
    out.add(1.0, tmp);
}
```

This code is used in section 262.

**288.** End of gs\_tensor.cpp file.

**289. Row-wise full symmetry tensor.** Start of `rfs_tensor.h` file.

Here we define classes for full symmetry tensors with the multidimensional index identified with rows. The primary usage is for storage of data coming from (or from a sum of)

$$\prod_{m=1}^l [g_{s|c_m|}]_{c_m(\alpha)}^{\gamma_m}$$

where  $\alpha$  coming from a multidimensional index go through some set  $S$  and  $c$  is some equivalence. So we model a tensor of the form:

$$\left[ \prod_{m=1}^l [g_{s|c_m|}]_{c_m(\alpha)}^{\gamma_m} \right]_S^{\gamma_1 \dots \gamma_l}$$

Since all  $\gamma_1, \dots, \gamma_l$  correspond to the same variable, the tensor is fully symmetric. The set of indices  $S$  cannot be very large and sometimes it is only one element. This case is handled in a special subclass.

We provide both folded and unfolded versions. Their logic is perfectly the same as in **UFS****Tensor** and **FFSTensor** with two exceptions. One has been already mentioned, the multidimensional index is along the rows. The second are conversions between the two types. Since this kind of tensor is used to multiply (from the right) a tensor whose multidimensional index is identified with columns, we will need a different way of a conversion. If the multiplication of two folded tensors is to be equivalent with multiplication of two unfolded, the folding of the right tensor must sum all equivalent elements since they are multiplied with the same number from the folded tensor. (Equivalent here means all elements of unfolded tensor corresponding to one element in folded tensor.) For this reason, it is necessary to calculate a column number from the given sequence, so we implement *getOffset*. Process of unfolding is not used, so we implemented it so that unfolding and then folding a tensor would yield the same data.

```
#ifndef RFS_TENSOR_H
#define RFS_TENSOR_H
#include "tensor.h"
#include "fs_tensor.h"
#include "symmetry.h"
    < URTensor class declaration 290 >;
    < FRTensor class declaration 292 >;
    < URSingleTensor class declaration 294 >;
    < FRSingleTensor class declaration 295 >;
#endif
```

**290.** This is straightforward and very similar to **UFSTensor**.

```

⟨ URTensor class declaration 290 ⟩ ≡
class FRTensor;
class URTensor : public UTensor {
    int nv;
public:
    ⟨ URTensor constructor declaration 291 ⟩;
    virtual ~URTensor() {}
    void increment(IntSequence &v) const;
    void decrement(IntSequence &v) const;
    FTensor &fold() const;
    int getOffset(const IntSequence &v) const;
    int nvar() const
    { return nv; }
    Symmetry getSym() const
    { return Symmetry(dimen()); }
};

```

This code is cited in section 1.

This code is used in section 289.

**291.**

```

⟨ URTensor constructor declaration 291 ⟩ ≡
URTensor(int c, int nvar, int d)
: UTensor(along_row, IntSequence(d, nvar), UFSTensor::calcMaxOffset(nvar, d), c, d), nv(nvar) {}
URTensor(const URTensor &ut)
: UTensor(ut), nv(ut.nv) {}
URTensor(const FRTensor &ft);

```

This code is used in section 290.

**292.** This is straightforward and very similar to **FFSTensor**.

```

⟨ FRTensor class declaration 292 ⟩ ≡
class FRTensor : public FTensor {
    int nv;
public:
    ⟨ FRTensor constructor declaration 293 ⟩;
    virtual ~FRTensor() {}
    void increment(IntSequence &v) const;
    void decrement(IntSequence &v) const;
    UTensor &unfold() const;
    int nvar() const
    { return nv; }
    int getOffset(const IntSequence &v) const
    { return FTensor::getOffset(v, nv); }
    Symmetry getSym() const
    { return Symmetry(dimen()); }
};

```

This code is cited in section 1.

This code is used in section 289.

**293.**

```

⟨ FRTensor constructor declaration 293 ⟩ ≡
FRTensor(int c, int nvar, int d)
: FTensor(along_row, IntSequence(d, nvar), FFSTensor::calcMaxOffset(nvar, d), c, d), nv(nvar) {}

FRTensor(const FRTensor &ft)
: FTensor(ft), nv(ft.nv) {}

FRTensor(const URTensor &ut);

```

This code is used in section 292.

**294.** The following class represents specialization of **URTensor** coming from Kronecker multiplication of a few vectors. So the resulting row-oriented tensor has one column. We provide two constructors, one constructs the tensor from a few vectors stored as **vector**⟨**ConstVector**⟩. The second makes the Kronecker power of one given vector.

```

⟨ URSingleTensor class declaration 294 ⟩ ≡
class URSingleTensor : public URTensor {
public:
    URSingleTensor(int nvar, int d)
    : URTensor(1, nvar, d) {}

    URSingleTensor(const vector⟨ConstVector⟩ &cols);
    URSingleTensor(const ConstVector &v, int d);
    URSingleTensor(const URSingleTensor &ut)
    : URTensor(ut) {}

    virtual ~URSingleTensor() {}

    FTensor &fold() const;
};

```

This code is cited in section 1.

This code is used in section 289.

**295.** This class represents one column row-oriented tensor. The only way how to construct it is from the **URSingleTensor** or from the scratch. The folding algorithm is the same as folding of general **URTensor**. Only its implementation is different, since we do not copy rows, but only elements.

```

⟨ FRSingleTensor class declaration 295 ⟩ ≡
class FRSingleTensor : public FRTensor {
public:
    FRSingleTensor(int nvar, int d)
    : FRTensor(1, nvar, d) {}

    FRSingleTensor(const URSingleTensor &ut);
    FRSingleTensor(const FRSingleTensor &ft)
    : FRTensor(ft) {}

    virtual ~FRSingleTensor() {}
};

```

This code is cited in section 1.

This code is used in section 289.

**296.** End of `rfs_tensor.h` file.

**297.** Start of `rfs_tensor.cpp` file.

```
#include "rfs_tensor.h"
#include "kron_prod.h"
#include "tl_exception.h"
< FRTensor conversion from unfolded 298 >;
< FRTensor::unfold code 299 >;
< FRTensor::increment code 300 >;
< FRTensor::decrement code 301 >;
< URTensor conversion from folded 302 >;
< URTensor::fold code 303 >;
< URTensor increment and decrement 304 >;
< URTensor::getOffset code 305 >;
< URSingleTensor constructor 1 code 306 >;
< URSingleTensor constructor 2 code 307 >;
< URSingleTensor::fold code 308 >;
< FRSingleTensor conversion from unfolded 309 >;
```

**298.** The conversion from unfolded to folded sums up all data from unfolded corresponding to one folded index. So we go through all the rows in the unfolded tensor *ut*, make an index of the folded tensor by sorting the coordinates, and add the row.

```
< FRTensor conversion from unfolded 298 > ≡
FRTensor::FRTensor(const URTensor &ut)
: FTensor(along_row, IntSequence(ut.dimen(), ut.nvar()), FFSTensor::calcMaxOffset(ut.nvar(),
    ut.dimen()), ut.ncols(), ut.dimen(), nv(ut.nvar())) {
    zeros();
    for (index in = ut.begin(); in ≠ ut.end(); ++in) {
        IntSequence vtmp(in.getCoor());
        vtmp.sort();
        index tar(this, vtmp);
        addRow(ut, *in, *tar);
    }
}
```

This code is used in section 297.

**299.** Here just make a new instance and return the reference.

```
< FRTensor::unfold code 299 > ≡
URTensor &FRTensor::unfold() const
{
    return *(new URTensor(*this));
}
```

This code is used in section 297.

**300.** Incrementing is easy. The same as for **FFSTensor**.

```

< FRTensor::increment code 300 > ≡
void FRTensor::increment(IntSequence &v) const
{
    TL_RAISE_IF(v.size() ≠ dimen(), "Wrong_input/output_vector_size_in_FRTensor::increment");
    UTensor::increment(v, nv);
    v.monotone();
}

```

This code is used in section 297.

**301.** Decrement calls static **FTensor**::decrement.

```

< FRTensor::decrement code 301 > ≡
void FRTensor::decrement(IntSequence &v) const
{
    TL_RAISE_IF(v.size() ≠ dimen(), "Wrong_input/output_vector_size_in_FRTensor::decrement");
    FTensor::decrement(v, nv);
}

```

This code is used in section 297.

**302.** Here we convert folded full symmetry tensor to unfolded. We copy all columns of folded tensor to unfolded and leave other columns (duplicates) zero. In this way, if the unfolded tensor is folded back, we should get the same data.

```

< URTensor conversion from folded 302 > ≡
URTensor::URTensor(const FRTensor &ft)
: UTensor(along_row, IntSequence(ft.dimen(), ft.nvar()), UFSTensor::calcMaxOffset(ft.nvar(),
    ft.dimen()), ft.ncols(), ft.dimen()), nv(ft.nvar())) {
    zeros();
    for (index src = ft.begin(); src ≠ ft.end(); ++src) {
        index in(this, src.getCoor());
        copyRow(ft, *src, *in);
    }
}

```

This code is used in section 297.

**303.** Here we just return a reference to new instance of folded tensor.

```

< URTensor::fold code 303 > ≡
FTensor &URTensor::fold() const
{
    return *(new FRTensor(*this));
}

```

This code is used in section 297.

**304.** Here we just call **UTensor** respective static methods.

```

< UTensor increment and decrement 304 > ≡
void UTensor::increment(IntSequence &v) const
{
    TL_RAISE_IF(v.size() ≠ dimen(), "Wrong_input/output_vector_size_in_URTensor::increment");
    UTensor::increment(v, nv);
}
void UTensor::decrement(IntSequence &v) const
{
    TL_RAISE_IF(v.size() ≠ dimen(), "Wrong_input/output_vector_size_in_URTensor::decrement");
    UTensor::decrement(v, nv);
}

```

This code is used in section 297.

**305.**

```

< UTensor::getOffset code 305 > ≡
int UTensor::getOffset(const IntSequence &v) const
{
    TL_RAISE_IF(v.size() ≠ dimen(), "Wrong_input_vector_size_in_URTensor::getOffset");
    return UTensor::getOffset(v, nv);
}

```

This code is used in section 297.

**306.** Here we construct  $v_1 \otimes v_2 \otimes \dots \otimes v_n$ , where  $v_1, v_2, \dots, v_n$  are stored in **vector**<**ConstVector**>.

```

< URSingleTensor constructor 1 code 306 > ≡
URSingleTensor::URSingleTensor(const vector<ConstVector> &cols)
: UTensor(1, cols[0].length(), cols.size()) {
    if (dimen() ≡ 1) {
        getData() = cols[0];
        return;
    }
    Vector *last = new Vector(cols[cols.size() - 1]);
    for (int i = cols.size() - 2; i > 0; i--) {
        Vector *newlast = new Vector(Tensor::power(nvar(), cols.size() - i));
        KronProd::kronMult(cols[i], ConstVector(*last), *newlast);
        delete last;
        last = newlast;
    }
    KronProd::kronMult(cols[0], ConstVector(*last), getData());
    delete last;
}

```

This code is used in section 297.



**307.** Here we construct  $v \otimes \dots \otimes v$ , where the number of  $v$  copies is  $d$ .

```

< URSingleTensor constructor 2 code 307 > ≡
  URSingleTensor::URSingleTensor(const ConstVector &v, int d)
  : URTensor(1, v.length(), d) {
    if (d ≡ 1) {
      getData() = v;
      return;
    }
    Vector *last = new Vector(v);
    for (int i = d - 2; i > 0; i--) {
      Vector *newlast = new Vector(last->length() * v.length());
      KronProd::kronMult(v, ConstVector(*last), *newlast);
      delete last;
      last = newlast;
    }
    KronProd::kronMult(v, ConstVector(*last), getData());
    delete last;
  }

```

This code is used in section 297.

**308.** Here we construct **FRSingleTensor** from **URSingleTensor** and return its reference.

```

< URSingleTensor::fold code 308 > ≡
  FTensor &URSingleTensor::fold() const
  {
    return *(new FRSingleTensor(*this));
  }

```

This code is used in section 297.

**309.** The conversion from unfolded **URSingleTensor** to folded **FRSingleTensor** is completely the same as conversion from **URTensor** to **FRTensor**, only we do not copy rows but elements.

```

< FRSingleTensor conversion from unfolded 309 > ≡
  FRSingleTensor::FRSingleTensor(const URTensor &ut)
  : FRTensor(1, ut.nvar(), ut.dimen()) {
    zeros();
    for (index in = ut.begin(); in ≠ ut.end(); ++in) {
      IntSequence vtmp(in->getCoor());
      vtmp.sort();
      index tar(this, vtmp);
      get(*tar, 0) += ut.get(*in, 0);
    }
  }

```

This code is used in section 297.

**310.** End of `rfs_tensor.cpp` file.

**311. Even more general symmetry tensor.** Start of `ps_tensor.h` file.

Here we define an abstraction for a tensor, which has a general symmetry, but the symmetry is not of what is modelled by **Symmetry**. This kind of tensor comes to existence when we evaluate something like:

$$[B_{y^2u^3}]_{\alpha_1\alpha_2\beta_1\beta_2\beta_3} = \dots + [g_{y^3}]_{\gamma_1\gamma_2\gamma_3} [g_{yu}]_{\alpha_1\beta_3}^{\gamma_1} [g_{yu}]_{\alpha_2\beta_1}^{\gamma_2} [g_u]_{\beta_2}^{\gamma_3} + \dots$$

If the tensors are unfolded, we obtain a tensor

$$g_{y^3} \cdot (g_{yu} \otimes g_{yu} \otimes g_u)$$

Obviously, this tensor can have a symmetry not compatible with ordering  $\alpha_1\alpha_2\beta_1\beta_2\beta_3$ , (in other words, not compatible with symmetry  $y^2u^3$ ). In fact, the indices are permuted.

This kind of tensor must be added to  $[B_{y^2u^3}]$ . Its dimensions are the same as of  $[B_{y^2u^3}]$ , but some coordinates are permuted. The addition is the only action we need to do with the tensor.

Another application where this permuted symmetry tensor appears is a slice of a fully symmetric tensor. If the symmetric dimension of the tensor is partitioned to continuous parts, and we are interested only in data with a given symmetry (permuted) of the partitions, then we have the permuted symmetry tensor. For instance, if  $x$  is partitioned  $x = [a, b, c, d]$ , and having tensor  $[f_{x^3}]$ , one can find a slice (subtensor)  $[f_{aca}]$ . The data of this tensor are permuted of  $[f_{ac}]$ .

Here we also define the folded version of permuted symmetry tensor. It has permuted symmetry and is partially folded. One can imagine it as a product of a few dimensions, each of them is folded and having a few variables. The underlying variables are permuted. The product of such dimensions is described by **PerTensorDimens2**. The tensor holding the underlying data is **FPSTensor**.

```
#ifndef PS_TENSOR_H
#define PS_TENSOR_H
#include "tensor.h"
#include "gs_tensor.h"
#include "equivalence.h"
#include "permutation.h"
#include "kron_prod.h"
#include "sparse_tensor.h"
<SortIntSequence class declaration 312>;
<PerTensorDimens class declaration 313>;
<UPSTensor class declaration 314>;
<PerTensorDimens2 class declaration 316>;
<FPSTensor class declaration 317>;
#endif
```

**312.** This is just a helper class for ordering a sequence on call stack.

```
<SortIntSequence class declaration 312> ≡
class SortIntSequence : public IntSequence {
public:
    SortIntSequence(const IntSequence &s)
        : IntSequence(s) { sort(); }
};
```

This code is used in section 311.

**313.** Here we declare a class describing dimensions of permuted symmetry tensor. It inherits from **TensorDimens** and adds a permutation which permutes  $nvmax$ . It has two constructors, each corresponds to a context where the tensor appears.

The first constructor calculates the permutation from a given equivalence.

The second constructor corresponds to dimensions of a slice. Let us take  $[f_{aca}]$  as an example. First it calculates **TensorDimens** of  $[f_{ac}]$ , then it calculates a permutation corresponding to ordering of  $aca$  to  $a^2c$ , and applies this permutation on the dimensions as the first constructor. The constructor takes only stack sizes (lengths of  $a$ ,  $b$ ,  $c$ , and  $d$ ), and coordinates of picked partitions.

Note that inherited methods *calcUnfoldColumns* and *calcFoldColumns* work, since number of columns is independent on the permutation, and *calcFoldColumns* does not use changed  $nvmax$ , it uses  $nvs$ , so it is OK.

```

⟨ PerTensorDimens class declaration 313 ⟩ ≡
class PerTensorDimens : public TensorDimens {
protected:
    Permutation per;
public:
    PerTensorDimens(const Symmetry &s, const IntSequence &nvars, const Equivalence &e)
    : TensorDimens(s, nvars), per(e) { per.apply(nvmax); }
    PerTensorDimens(const TensorDimens &td, const Equivalence &e)
    : TensorDimens(td), per(e) { per.apply(nvmax); }
    PerTensorDimens(const TensorDimens &td, const Permutation &p)
    : TensorDimens(td), per(p) { per.apply(nvmax); }
    PerTensorDimens(const IntSequence &ss, const IntSequence &coord)
    : TensorDimens(ss, SortIntSequence(coord)), per(coord) { per.apply(nvmax); }
    PerTensorDimens(const PerTensorDimens &td)
    : TensorDimens(td), per(td.per) {}
    const PerTensorDimens &operator=(const PerTensorDimens &td)
    { TensorDimens::operator=(td); per = td.per; return *this; }
    bool operator==(const PerTensorDimens &td)
    { return TensorDimens::operator==(td) & per == td.per; }
    int tailIdentity() const
    { return per.tailIdentity(); }
    const Permutation &getPer() const
    { return per; }
};

```

This code is used in section 311.

**314.** Here we declare the permuted symmetry unfolded tensor. It has **PerTensorDimens** as a member. It inherits from **UTensor** which requires to implement *fold* method. There is no folded counterpart, so in our implementation we raise unconditional exception, and return some dummy object (just to make it compilable without warnings).

The class has two sorts of constructors corresponding to a context where it appears. The first constructs object from a given matrix, and Kronecker product. Within the constructor, all the calculations are performed. Also we need to define dimensions, these are the same of the resulting matrix (in our example  $[B_{y^2u^3}]$ ) but permuted. The permutation is done in **PerTensorDimens** constructor.

The second type of constructor is slicing. It makes a slice from **FSSparseTensor**. The slice is given by stack sizes, and coordinates of picked stacks.

There are two algorithms for filling a slice of a sparse tensor. The first *fillFromSparseOne* works well for more dense tensors, the second *fillFromSparseTwo* is better for very sparse tensors. We provide a static method, which decides what of the two algorithms is better.

⟨ **UPSTensor** class declaration 314 ⟩ ≡

```
class UPSTensor : public UTensor {
    const PerTensorDimens tdims;
public:
    ⟨ UPSTensor constructors from Kronecker product 315 ⟩;
    UPSTensor(const FSSparseTensor &t, const IntSequence &ss, const IntSequence
        &coord, const PerTensorDimens &ptd);
    UPSTensor(const UPSTensor &ut)
    : UTensor(ut), tdims(ut.tdims) {}
    void increment(IntSequence &v) const;
    void decrement(IntSequence &v) const;
    FTensor &fold() const;
    int getOffset(const IntSequence &v) const;
    void addTo(FGSTensor &out) const;
    void addTo(UGSTensor &out) const;
    enum fill_method {
        first, second
    };
    static fill_method decideFillMethod(const FSSparseTensor &t);
private:
    int tailIdentitySize() const;
    void fillFromSparseOne(const FSSparseTensor &t, const IntSequence &ss, const IntSequence
        &coord);
    void fillFromSparseTwo(const FSSparseTensor &t, const IntSequence &ss, const IntSequence
        &coord);
};
```

This code is cited in section 1.

This code is used in section 311.

**315.** Here we have four constructors making an **UPSTensor** from a product of matrix and Kronecker product. The first constructs the tensor from equivalence classes of the given equivalence in an order given by the equivalence. The second does the same but with optimized **KronProdAllOptim**, which has a different order of matrices than given by the classes in the equivalence. This permutation is projected to the permutation of the **UPSTensor**. The third, is the same as the first, but the classes of the equivalence are permuted by the given permutation. Finally, the fourth is the most general combination. It allows for a permutation of equivalence classes, and for optimized **KronProdAllOptim**, which permutes the permuted equivalence classes.

⟨ **UPSTensor** constructors from Kronecker product 315 ⟩ ≡

```

UPSTensor(const TensorDimens &td, const Equivalence &e, const ConstTwoDMatrix
    &a, const KronProdAll &kp)
: UTensor(along_col, PerTensorDimens(td, e).getNVX(), a.nrows(), kp.ncols(), td.dimen()),
    tdims(td, e) { kp.mult(a, *this); }

UPSTensor(const TensorDimens &td, const Equivalence &e, const ConstTwoDMatrix
    &a, const KronProdAllOptim &kp)
: UTensor(along_col, PerTensorDimens(td, Permutation(e, kp.getPer())).getNVX(), a.nrows(),
    kp.ncols(), td.dimen()), tdims(td, Permutation(e, kp.getPer())) { kp.mult(a, *this); }

UPSTensor(const TensorDimens &td, const Equivalence &e, const Permutation &p, const
    ConstTwoDMatrix &a, const KronProdAll &kp)
: UTensor(along_col, PerTensorDimens(td, Permutation(e, p)).getNVX(), a.nrows(), kp.ncols(),
    td.dimen()), tdims(td, Permutation(e, p)) { kp.mult(a, *this); }

UPSTensor(const TensorDimens &td, const Equivalence &e, const Permutation &p, const
    ConstTwoDMatrix &a, const KronProdAllOptim &kp)
: UTensor(along_col, PerTensorDimens(td,
    Permutation(e, Permutation(p, kp.getPer()))).getNVX(), a.nrows(), kp.ncols(), td.dimen()),
    tdims(td, Permutation(e, Permutation(p, kp.getPer())) { kp.mult(a, *this); }

```

This code is used in section 314.

**316.** Here we define an abstraction for the tensor dimension with the symmetry like  $xuv|uv|xu|y|y|x|y$ . These symmetries come as induces symmetries of equivalence and some outer symmetry. Thus the underlying variables are permuted. One can imagine the dimensions as an unfolded product of dimensions which consist of folded products of variables.

We inherit from **PerTensorDimens** since we need the permutation implied by the equivalence. The new member are the induced symmetries (symmetries of each folded dimensions) and *ds* which are sizes of the dimensions. The number of folded dimensions is return by *numSyms*.

The object is constructed from outer tensor dimensions and from equivalence with optionally permuted classes.

(**PerTensorDimens2** class declaration 316)  $\equiv$

```
class PerTensorDimens2 : public PerTensorDimens {
    InducedSymmetries syms;
    IntSequence ds;
public:
    PerTensorDimens2(const TensorDimens &td, const Equivalence &e, const Permutation &p)
    : PerTensorDimens(td, Permutation(e, p), syms(e, p, td.getSym()), ds(syms.size())) {
        setDimensionSizes(); }
    PerTensorDimens2(const TensorDimens &td, const Equivalence &e)
    : PerTensorDimens(td, e, syms(e, td.getSym()), ds(syms.size())) { setDimensionSizes(); }
    int numSyms() const
    { return (int) syms.size(); }
    const Symmetry &getSym(int i) const
    { return syms[i]; }
    int calcMaxOffset() const
    { return ds.mult(); }
    int calcOffset(const IntSequence &coord) const;
    void print() const;
protected:
    void setDimensionSizes();
};
```

This code is used in section 311.

**317.** Here we define an abstraction of the permuted symmetry folded tensor. It is needed in context of the Faa Di Bruno formula for folded stack container multiplied with container of dense folded tensors, or multiplied by one full symmetry sparse tensor.

For example, if we perform the Faa Di Bruno for  $F = f(z)$ , where  $z = [g(x, y, u, v), h(x, y, u), x, y]^T$ , we get for one concrete equivalence:

$$[F_{x^4 y^3 u^3 v^2}] = \dots + [f_{g^2 h^2 x^2 y}] \left( [g]_{xv} \otimes [g]_{u^2 v} \otimes [h]_{xu} \otimes [h]_{y^2} \otimes [I]_x \otimes [I]_x \otimes [I]_y \right) + \dots$$

The class **FPSTensor** represents the tensor at the right. Its dimension corresponds to a product of 7 dimensions with the following symmetries:  $xv|u^v|xu|y^2|x|x|y$ . Such the dimension is described by **PerTensorDimens2**.

The tensor is constructed in a context of stack container multiplication, so, it is constructed from dimensions *td* (dimensions of the output tensor), stack product *sp* (implied symmetries picking tensors from a stack container, here it is *z*), then a sorted integer sequence of the picked stacks of the stack product (it is always sorted, here it is (0, 0, 1, 1, 2, 2, 3)), then the tensor  $[f_{g^2 h^2 x^2 y}]$  (its symmetry must be the same as symmetry given by the *istacks*), and finally from the equivalence with permuted classes.

We implement *increment* and *getOffset* methods, *decrement* and *unfold* raise an exception. Also, we implement *addTo* method, which adds the tensor data (partially unfolded) to folded general symmetry tensor.

```
<FPSTensor class declaration 317> ≡
template<typename _Ttype> class StackProduct;
class FPSTensor : public FTensor {
    const PerTensorDimens2 tdims;
public:
    <FPSTensor constructors 318>;
    void increment(IntSequence &v) const;
    void decrement(IntSequence &v) const;
    UTensor &unfold() const;
    int getOffset(const IntSequence &v) const;
    void addTo(FGSTensor &out) const;
};
```

This code is cited in section 1.

This code is used in section 311.

**318.** As for **UPSTensor**, we provide four constructors allowing for combinations of permuting equivalence classes, and optimization of **KronProdAllOptim**. These constructors multiply with dense general symmetry tensor (coming from the dense container, or as a dense slice of the full symmetry sparse tensor). In addition to these 4 constructors, we have one constructor multiplying with general symmetry sparse tensor (coming as a sparse slice of the full symmetry sparse tensor).

⟨ **FPSTensor** constructors 318 ⟩ ≡

```

FPSTensor(const TensorDimens &td, const Equivalence &e, const ConstTwoDMatrix
    &a, const KronProdAll &kp)
: FTensor(along_col, PerTensorDimens(td, e).getNVX(), a.nrows(), kp.ncols(), td.dimen()),
    tdims(td, e) { kp.mult(a, *this); }

FPSTensor(const TensorDimens &td, const Equivalence &e, const ConstTwoDMatrix
    &a, const KronProdAllOptim &kp)
: FTensor(along_col, PerTensorDimens(td, Permutation(e, kp.getPer())).getNVX(), a.nrows(),
    kp.ncols(), td.dimen(), tdims(td, e, kp.getPer()) { kp.mult(a, *this); }

FPSTensor(const TensorDimens &td, const Equivalence &e, const Permutation &p, const
    ConstTwoDMatrix &a, const KronProdAll &kp)
: FTensor(along_col, PerTensorDimens(td, Permutation(e, p)).getNVX(), a.nrows(), kp.ncols(),
    td.dimen(), tdims(td, e, p) { kp.mult(a, *this); }

FPSTensor(const TensorDimens &td, const Equivalence &e, const Permutation &p, const
    ConstTwoDMatrix &a, const KronProdAllOptim &kp)
: FTensor(along_col, PerTensorDimens(td,
    Permutation(e, Permutation(p, kp.getPer()))).getNVX(), a.nrows(), kp.ncols(), td.dimen()),
    tdims(td, e, Permutation(p, kp.getPer())) { kp.mult(a, *this); }

FPSTensor(const TensorDimens &td, const Equivalence &e, const Permutation &p, const
    GSSparseTensor &t, const KronProdAll &kp);

FPSTensor(const FPSTensor &ft)
: FTensor(ft), tdims(ft.tdims) {}

```

This code is used in section 317.

**319.** End of `ps.tensor.h` file.



**320.** Start of `ps_tensor.cpp` file.

```
#include "ps_tensor.h"
#include "fs_tensor.h"
#include "tl_exception.h"
#include "tl_static.h"
#include "stack_container.h"
< UPSTensor::decideFillMethod code 321 >;
< UPSTensor slicing constructor code 322 >;
< UPSTensor increment and decrement 323 >;
< UPSTensor::fold code 324 >;
< UPSTensor::getOffset code 325 >;
< UPSTensor::addTo folded code 326 >;
< UPSTensor::addTo unfolded code 327 >;
< UPSTensor::tailIdentitySize code 328 >;
< UPSTensor::fillFromSparseOne code 329 >;
< UPSTensor::fillFromSparseTwo code 330 >;
< PerTensorDimens2::setDimensionSizes code 331 >;
< PerTensorDimens2::calcOffset code 332 >;
< PerTensorDimens2::print code 333 >;
< FPSTensor::increment code 334 >;
< FPSTensor::decrement code 335 >;
< FPSTensor::unfold code 336 >;
< FPSTensor::getOffset code 337 >;
< FPSTensor::addTo code 338 >;
< FPSTensor sparse constructor 339 >;
```

**321.** Here we decide, what method for filling a slice in slicing constructor to use. A few experiments suggest, that if the tensor is more than 8% filled, the first method (*fillFromSparseOne*) is better. For fill factors less than 1%, the second can be 3 times quicker.

```
< UPSTensor::decideFillMethod code 321 > ≡
UPSTensor::fill_method UPSTensor::decideFillMethod(const FSSparseTensor &t)
{
    if (t.getFillFactor() > 0.08) return first;
    else return second;
}
```

This code is used in section 320.

**322.** Here we make a slice. We decide what fill method to use and set it.

```
< UPSTensor slicing constructor code 322 > ≡
UPSTensor::UPSTensor(const FSSparseTensor &t, const IntSequence &ss, const IntSequence
    &coord, const PerTensorDimens &ptd)
: UTensor(along_col, ptd.getNVX(), t.nrows(), ptd.calcUnfoldMaxOffset(), ptd.dimen(), tdims(ptd) {
    TL_RAISE_IF(coord.size() ≠ t.dimen(),
        "Wrong_coordinates_length_of_stacks_for_UPSTensor_slicing_constructor");
    TL_RAISE_IF(ss.sum() ≠ t.nvar(),
        "Wrong_length_of_stacks_for_UPSTensor_slicing_constructor");
    if (first ≡ decideFillMethod(t)) fillFromSparseOne(t, ss, coord);
    else fillFromSparseTwo(t, ss, coord);
}
```

This code is used in section 320.

**323.**

```

< UPSTensor increment and decrement 323 > ≡
void UPSTensor::increment(IntSequence &v) const
{
    TL_RAISE_IF(v.size() ≠ dimen(), "Wrong_input/output_vector_size_in_UPSTensor::increment");
    UTensor::increment(v, tdims.getNVX());
}
void UPSTensor::decrement(IntSequence &v) const
{
    TL_RAISE_IF(v.size() ≠ dimen(), "Wrong_input/output_vector_size_in_UPSTensor::decrement");
    UTensor::decrement(v, tdims.getNVX());
}

```

This code is used in section 320.

**324.**

```

< UPSTensor::fold code 324 > ≡
FTensor &UPSTensor::fold() const
{
    TL_RAISE("Never_should_come_to_this_place_in_UPSTensor::fold");
    FFTensor *nothing = new FFTensor(0,0,0);
    return *nothing;
}

```

This code is used in section 320.

**325.**

```

< UPSTensor::getOffset code 325 > ≡
int UPSTensor::getOffset(const IntSequence &v) const
{
    TL_RAISE_IF(v.size() ≠ dimen(), "Wrong_input_vector_size_in_UPSTensor::getOffset");
    return UTensor::getOffset(v, tdims.getNVX());
}

```

This code is used in section 320.

**326.**

```

< UPSTensor::addTo folded code 326 > ≡
void UPSTensor::addTo(FGSTensor &out) const
{
    TL_RAISE_IF(out.getDims() ≠ tdims,
        "Tensors_have_incompatible_dims_in_UPSTensor::addTo");
    for (index in = out.begin(); in ≠ out.end(); ++in) {
        IntSequence vtmp(dimen());
        tdims.getPer().apply(in.getCoor(), vtmp);
        index tin(this, vtmp);
        out.addColumn(*this, *tin, *in);
    }
}

```

This code is used in section 320.

**327.** In here, we have to add this permuted symmetry unfolded tensor to an unfolded not permuted tensor. One easy way would be to go through the target tensor, permute each index, and add the column.

However, it may happen, that the permutation has some non-empty identity tail. In this case, we can add not only individual columns, but much bigger data chunks, which is usually more efficient. Therefore, the code is quite dirty, because we have not an iterator, which iterates over tensor at some higher levels. So we simulate it by the following code.

First we set *cols* to the length of the data chunk and *off* to its dimension. Then we need a front part of *nvmax* of *out*, which is *nvmax\_part*. Our iterator here is an integer sequence *outrun* with full length, and *outrun\_part* its front part. The *outrun* is initialized to zeros. In each step we need to increment *outrun cols*-times, this is done by incrementing its prefix *outrun\_part*.

So we loop over all *cols*wide partitions of *out*, permute *outrun* to obtain *perrun* to obtain column of this matrix. (note that the trailing part of *perrun* is the same as of *outrun*. Then we construct submatrices, add them, and increment *outrun*.

```
< UPSTensor::addTo unfolded code 327 > ≡
void UPSTensor::addTo(UGSTensor &out) const
{
    TL_RAISE_IF(out.getDims() ≠ tdims,
        "Tensors_have_incompatible_dims_in_UPSTensor::addTo");
    int cols = tailIdentitySize();
    int off = tdims.tailIdentity();
    IntSequence outrun(out.dimen(), 0);
    IntSequence outrun_part(outrun, 0, out.dimen() - off);
    IntSequence nvmax_part(out.getDims().getNVX(), 0, out.dimen() - off);
    for (int out_col = 0; out_col < out.ncols(); out_col += cols) { /* permute outrun */
        IntSequence perrun(out.dimen());
        tdims.getPer().apply(outrun, perrun);
        index from(this, perrun); /* construct submatrices */
        ConstTwoDMatrix subfrom(*this, *from, cols);
        TwoDMatrix subout(out, out_col, cols); /* add */
        subout.add(1, subfrom); /* increment outrun by cols */
        UTensor::increment(outrun_part, nvmax_part);
    }
}
```

This code is used in section 320.

**328.** This returns a product of all items in *nvmax* which make up the trailing identity part.

```
< UPSTensor::tailIdentitySize code 328 > ≡
int UPSTensor::tailIdentitySize() const
{
    return tdims.getNVX().mult(dimen() - tdims.tailIdentity(), dimen());
}
```

This code is used in section 320.

**329.** This fill method is pretty dumb. We go through all columns in **this** tensor, translate coordinates to sparse tensor, sort them and find an item in the sparse tensor. There are many not successful lookups for really sparse tensor, that is why the second method works better for really sparse tensors.

⟨ **UPSTensor** :: *fillFromSparseOne* code 329 ⟩ ≡

```
void UPSTensor::fillFromSparseOne(const FSSparseTensor &t, const IntSequence &ss, const
    IntSequence &coord)
{
    IntSequence cumtmp(ss.size());
    cumtmp[0] = 0;
    for (int i = 1; i < ss.size(); i++) cumtmp[i] = cumtmp[i - 1] + ss[i - 1];
    IntSequence cum(coord.size());
    for (int i = 0; i < coord.size(); i++) cum[i] = cumtmp[coord[i]];
    zeros();
    for (Tensor::index run = begin(); run ≠ end(); ++run) {
        IntSequence c(run.getCoord());
        c.add(1, cum);
        c.sort();
        FSSparseTensor::const_iterator sl = t.getMap().lower_bound(c);
        if (sl ≠ t.getMap().end()) {
            FSSparseTensor::const_iterator su = t.getMap().upper_bound(c);
            for (FSSparseTensor::const_iterator srun = sl; srun ≠ su; ++srun)
                get((*srun).second.first, *run) = (*srun).second.second;
        }
    }
}
```

This code is used in section 320.

**330.** This is the second way of filling the slice. For instance, let the slice correspond to partitions  $abac$ . In here we first calculate lower and upper bounds for index of the sparse tensor for the slice. These are  $lb\_srt$  and  $ub\_srt$  respectively. They corresponds to ordering  $aabc$ . Then we go through that interval, and select items which are really between the bounds. Then we take the index, subtract the lower bound to get it to coordinates of the slice. We get something like  $(i_a, j_a, k_b, l_c)$ . Then we apply the inverse permutation as of the sorting form  $abac \mapsto aabc$  to get index  $(i_a, k_b, j_a, l_c)$ . Recall that the slice is unfolded, so we have to apply all permutations preserving the stack coordinates  $abac$ . In our case we get list of indices  $(i_a, k_b, j_a, l_c)$  and  $(j_a, k_b, i_a, l_c)$ . For all we copy the item of the sparse tensor to the appropriate column.

⟨ **UPSTensor** :: *fillFromSparseTwo* code 330 ⟩ ≡

```
void UPSTensor::fillFromSparseTwo(const FSSparseTensor &t, const IntSequence &ss, const
    IntSequence &coord)
{
    IntSequence coord_srt(coord);
    coord_srt.sort();
    IntSequence cum(ss.size());
    cum[0] = 0;
    for (int i = 1; i < ss.size(); i++) cum[i] = cum[i - 1] + ss[i - 1];
    IntSequence lb_srt(coord.size());
    IntSequence ub_srt(coord.size());
    for (int i = 0; i < coord.size(); i++) {
        lb_srt[i] = cum[coord_srt[i]];
        ub_srt[i] = cum[coord_srt[i]] + ss[coord_srt[i]] - 1;
    }
    const PermutationSet &pset = t.pbundle->get(coord.size());
    vector<const Permutation *> pp = pset.getPreserving(coord);
    Permutation unsort(coord);
    zeros();
    FSSparseTensor::const_iterator lbi = t.getMap().lower_bound(lb_srt);
    FSSparseTensor::const_iterator ubi = t.getMap().upper_bound(ub_srt);
    for (FSSparseTensor::const_iterator run = lbi; run != ubi; ++run) {
        if (lb_srt.lessEq((*run).first) & (*run).first.lessEq(ub_srt)) {
            IntSequence c((*run).first);
            c.add(-1, lb_srt);
            unsort.apply(c);
            for (unsigned int i = 0; i < pp.size(); i++) {
                IntSequence cp(coord.size());
                pp[i]->apply(c, cp);
                Tensor::index ind(this, cp);
                TL_RAISE_IF(*ind < 0 ∨ *ind ≥ ncols(),
                    "Internal_error_in_slicing_constructor_of_UPSTensor");
                get((*run).second.first, *ind) = (*run).second.second;
            }
        }
    }
}
```

This code is used in section 320.

**331.** Here we calculate the maximum offsets in each folded dimension (dimension sizes, hence *ds*).

```

⟨ PerTensorDimens2::setDimensionSizes code 331 ⟩ ≡
void PerTensorDimens2::setDimensionSizes()
{
    const IntSequence &nvs = getNVS();
    for (int i = 0; i < numSyms(); i++) {
        TensorDimens td(syms[i], nvs);
        ds[i] = td.calcFoldMaxOffset();
    }
}

```

This code is used in section 320.

**332.** If there are two folded dimensions, the offset in such a dimension is offset of the second plus offset of the first times the maximum offset of the second. If there are  $n + 1$  dimensions, the offset is a sum of offsets of the last dimension plus the offset in the first  $n$  dimensions multiplied by the maximum offset of the last dimension. This is exactly what the following code does.

```

⟨ PerTensorDimens2::calcOffset code 332 ⟩ ≡
int PerTensorDimens2::calcOffset(const IntSequence &coord) const
{
    TL_RAISE_IF(coord.size() != dimen(),
        "Wrong length of coordinates in PerTensorDimens2::calcOffset");
    IntSequence cc(coord);
    int ret = 0;
    int off = 0;
    for (int i = 0; i < numSyms(); i++) {
        TensorDimens td(syms[i], getNVS());
        IntSequence c(cc, off, off + syms[i].dimen());
        int a = td.calcFoldOffset(c);
        ret = ret * ds[i] + a;
        off += syms[i].dimen();
    }
    return ret;
}

```

This code is used in section 320.

**333.**

```

⟨ PerTensorDimens2::print code 333 ⟩ ≡
void PerTensorDimens2::print() const
{
    printf("nvmax: ");
    nvmax.print();
    printf("per: ");
    per.print();
    printf("syms: ");
    syms.print();
    printf("dims: ");
    ds.print();
}

```

This code is used in section 320.

**334.** Here we increment the given integer sequence. It corresponds to `UTensor::increment` of the whole sequence, and then partial monotoning of the subsequences with respect to the symmetries of each dimension.

```

⟨ FPSTensor::increment code 334 ⟩ ≡
void FPSTensor::increment(IntSequence &v) const
{
    TL_RAISE_IF(v.size() ≠ dimen(), "Wrong_length_of_coordinates_in_FPSTensor::increment");
    UTensor::increment(v, tdims.getNVX());
    int off = 0;
    for (int i = 0; i < tdims.numSyms(); i++) {
        IntSequence c(v, off, off + tdims.getSym(i).dimen());
        c.pmonotone(tdims.getSym(i));
        off += tdims.getSym(i).dimen();
    }
}

```

This code is used in section 320.

**335.**

```

⟨ FPSTensor::decrement code 335 ⟩ ≡
void FPSTensor::decrement(IntSequence &v) const
{
    TL_RAISE("FPSTensor::decrement_not_implemented");
}

```

This code is used in section 320.

**336.**

```

⟨ FPSTensor::unfold code 336 ⟩ ≡
UTensor &FPSTensor::unfold() const
{
    TL_RAISE("Unfolding_of_FPSTensor_not_implemented");
    UFSTensor *nothing = new UFSTensor(0, 0, 0);
    return *nothing;
}

```

This code is used in section 320.

**337.** We only call `calcOffset` of the `PerTensorDimens2`.

```

⟨ FPSTensor::getOffset code 337 ⟩ ≡
int FPSTensor::getOffset(const IntSequence &v) const
{
    return tdims.calcOffset(v);
}

```

This code is used in section 320.

**338.** Here we add the tensor to *out*. We go through all columns of the *out*, apply the permutation to get index in the tensor, and add the column. Note that if the permutation is identity, then the dimensions of the tensors might not be the same (since this tensor is partially folded).

```

⟨ FPSTensor::addTo code 338 ⟩ ≡
  void FPSTensor::addTo(FGSTensor &out) const
  {
    for (index tar = out.begin(); tar ≠ out.end(); ++tar) {
      IntSequence coor(dimen());
      tdims.getPer().apply(tar.getCoor(), coor);
      index src(this, coor);
      out.addColumn(*this, *src, *tar);
    }
  }

```

This code is used in section 320.



**339.** Here is the constructor which multiplies the Kronecker product with the general symmetry sparse tensor **GSSparseTensor**. The main idea is to go through items in the sparse tensor (each item selects rows in the matrices form the Kronecker product), then to Kronecker multiply the rows and multiply with the item, and to add the resulting row to the appropriate row of the resulting **FPSTensor**.

The realization of this idea is a bit more complicated since we have to go through all items, and each item must be added as many times as it has its symmetric elements. Moreover, the permutations shuffle order of rows in their Kronecker product.

So, we through all unfolded indices in a tensor with the same dimensions as the **GSSparseTensor** (sparse slice). For each such index we calculate its folded version (corresponds to ordering of subsequences within symmetries), we test if there is an item in the sparse slice with such coordinates, and if there is, we construct the Kronecker product of the rows, and go through all of items with the coordinates, and add to appropriate rows of **this** tensor.

⟨ **FPSTensor** sparse constructor 339 ⟩ ≡

```

FPSTensor::FPSTensor(const TensorDimens &td, const Equivalence &e, const Permutation
    &p, const GSSparseTensor &a, const KronProdAll &kp)
: FTensor(along_col, PerTensorDimens(td, Permutation(e, p)).getNVX(), a.nrows(), kp.ncols(),
    td.dimen()), tdims(td, e, p) {
    zeros();
    UGSTensor dummy(0, a.getDims());
    for (Tensor::index run = dummy.begin(); run ≠ dummy.end(); ++run) {
        Tensor::index fold_ind = dummy.getFirstIndexOf(run);
        const IntSequence &c = fold_ind.getCoor();
        GSSparseTensor::const_iterator sl = a.getMap().lower_bound(c);
        if (sl ≠ a.getMap().end()) {
            Vector *row_prod = kp.multRows(run.getCoor());
            GSSparseTensor::const_iterator su = a.getMap().upper_bound(c);
            for (GSSparseTensor::const_iterator srun = sl; srun ≠ su; ++srun) {
                Vector out_row ((*srun).second.first, *this);
                out_row.add ((*srun).second.second, *row_prod);
            }
            delete row_prod;
        }
    }
}

```

This code is cited in sections 442 and 446.

This code is used in section 320.

**340.** End of `ps_tensor.cpp` file.

**341. Sparse tensor.** Start of `sparse_tensor.h` file.

Here we declare a sparse full and general symmetry tensors with the multidimensional index along columns. We implement them as a **multimap** associating to each sequence of coordinates **IntSequence** a set of pairs (row, number). This is very convenient but not optimal in terms of memory consumption. So the implementation can be changed.

The current **multimap** implementation allows insertions. Another advantage of this approach is that we do not need to calculate column numbers from the **IntSequence**, since the column is accessed directly via the key which is **IntSequence**.

The only operation we need to do with the full symmetry sparse tensor is a left multiplication of a row oriented single column tensor. The result of such operation is a column of the same size as the sparse tensor. Other important operations are slicing operations. We need to do sparse and dense slices of full symmetry sparse tensors. In fact, the only constructor of general symmetry sparse tensor is slicing from the full symmetry sparse.

```
#ifndef SPARSE_TENSOR_H
#define SPARSE_TENSOR_H
#include "symmetry.h"
#include "tensor.h"
#include "gs_tensor.h"
#include "Vector.h"
#include <map>
using namespace std;
< ltseq predicate 342 >;
< SparseTensor class declaration 343 >;
< FSSparseTensor class declaration 344 >;
< GSSparseTensor class declaration 345 >;
#endif
```

**342.**

```
< ltseq predicate 342 > ≡
struct ltseq {
    bool operator()(const IntSequence &s1, const IntSequence &s2) const
    { return s1 < s2; }
};
```

This code is used in section 341.

**343.** This is a super class of both full symmetry and general symmetry sparse tensors. It contains a **multimap** and implements insertions. It tracks maximum and minimum row, for which there is an item.

```

< SparseTensor class declaration 343 > ≡
class SparseTensor {
public:
    typedef pair<int, double> Item;
    typedef multimap<IntSequence, Item, ltseq> Map;
    typedef Map::const_iterator const_iterator;

protected:
    typedef Map::iterator iterator;
    Map m;
    const int dim;
    const int nr;
    const int nc;
    int first_nz_row;
    int last_nz_row;

public:
    SparseTensor(int d, int nnr, int nnc)
    : dim(d), nr(nnr), nc(nnc), first_nz_row(nr), last_nz_row(-1) {}

    SparseTensor(const SparseTensor &t)
    : m(t.m), dim(t.dim), nr(t.nr), nc(t.nc) {}

    virtual ~SparseTensor() {}

    void insert(const IntSequence &s, int r, double c);

    const Map &getMap() const
    { return m; }

    int dimen() const
    { return dim; }

    int nrows() const
    { return nr; }

    int ncols() const
    { return nc; }

    double getFillFactor() const
    { return ((double) m.size())/(nrows() * ncols()); }

    double getFoldIndexFillFactor() const;
    double getUnfoldIndexFillFactor() const;

    int getNumNonZero() const
    { return m.size(); }

    int getFirstNonZeroRow() const
    { return first_nz_row; }

    int getLastNonZeroRow() const
    { return last_nz_row; }

    virtual const Symmetry &getSym() const = 0;

    void print() const;
    bool isFinite() const;
}

```

This code is used in section 341.

**344.** This is a full symmetry sparse tensor. It implements *multColumnAndAdd* and in addition to *sparseTensor*, it has *nv* (number of variables), and symmetry (basically it is a dimension).

```

⟨ FSSparseTensor class declaration 344 ⟩ ≡
class FSSparseTensor : public SparseTensor {
public:
    typedef SparseTensor::const_iterator const_iterator;
private:
    const int nv;
    const Symmetry sym;
public:
    FSSparseTensor(int d, int nvar, int r);
    FSSparseTensor(const FSSparseTensor &t);
    void insert(const IntSequence &s, int r, double c);
    void multColumnAndAdd(const Tensor &t, Vector &v) const;
    const Symmetry &getSym() const
    { return sym; }
    int nvar() const
    { return nv; }
    void print() const;
};

```

This code is cited in section 1.

This code is used in section 341.

**345.** This is a general symmetry sparse tensor. It has **TensorDimens** and can be constructed as a slice of the full symmetry sparse tensor. The slicing constructor takes the same form as the slicing **FGSTensor** constructor from full symmetry sparse tensor.

```

⟨ GSSparseTensor class declaration 345 ⟩ ≡
class GSSparseTensor : public SparseTensor {
public:
    typedef SparseTensor::const_iterator const_iterator;
private:
    const TensorDimens tdims;
public:
    GSSparseTensor(const FSSparseTensor &t, const IntSequence &ss, const IntSequence
        &coord, const TensorDimens &td);
    GSSparseTensor(const GSSparseTensor &t)
    : SparseTensor(t, tdims(t.tdims) {}
    void insert(const IntSequence &s, int r, double c);
    const Symmetry &getSym() const
    { return tdims.getSym(); }
    const TensorDimens &getDims() const
    { return tdims; }
    void print() const;
};

```

This code is used in section 341.

**346.** End of `sparse_tensor.h` file.

**347.** Start of `sparse_tensor.cpp` file.

```
#include "sparse_tensor.h"
#include "fs_tensor.h"
#include "tl_exception.h"
#include <cmath>
< SparseTensor::insert code 348 >;
< SparseTensor::isFinite code 350 >;
< SparseTensor::getFoldIndexFillFactor code 351 >;
< SparseTensor::getUnfoldIndexFillFactor code 352 >;
< SparseTensor::print code 353 >;
< FSSparseTensor constructor code 354 >;
< FSSparseTensor copy constructor code 355 >;
< FSSparseTensor::insert code 356 >;
< FSSparseTensor::multColumnAndAdd code 357 >;
< FSSparseTensor::print code 360 >;
< GSSparseTensor slicing constructor 361 >;
< GSSparseTensor::insert code 363 >;
< GSSparseTensor::print code 364 >;
```

**348.** This is straightforward. Before we insert anything, we do a few checks. Then we reset *first\_nz\_row* and *last\_nz\_row* if necessary.

```
< SparseTensor::insert code 348 > ≡
void SparseTensor::insert(const IntSequence &key, int r, double c)
{
    TL_RAISE_IF(r < 0 ∨ r ≥ nr,
        "Row_number_out_of_dimension_of_tensor_in_SparseTensor::insert");
    TL_RAISE_IF(key.size() ≠ dimen(), "Wrong_length_of_key_in_SparseTensor::insert");
    TL_RAISE_IF(¬std::isfinite(c), "Insertion_of_non-finite_value_in_SparseTensor::insert");
    iterator first_pos = m.lower_bound(key);
    < check that pair key and r is unique 349 >;
    m.insert(first_pos, Map::value_type(key, Item(r, c)));
    if (first_nz_row > r) first_nz_row = r;
    if (last_nz_row < r) last_nz_row = r;
}
```

This code is used in section 347.

**349.**

```
< check that pair key and r is unique 349 > ≡
iterator last_pos = m.upper_bound(key);
for (iterator it = first_pos; it ≠ last_pos; ++it)
    if ((*it).second.first ≡ r) {
        TL_RAISE("Duplicate_<key, r>_insertion_in_SparseTensor::insert");
        return;
    }
```

This code is used in section 348.

**350.** This returns true if all items are finite (not Nan nor Inf).

```

⟨ SparseTensor::isFinite code 350 ⟩ ≡
  bool SparseTensor::isFinite() const
  {
    bool res = true;
    const_iterator run = m.begin();
    while (res & run ≠ m.end()) {
      if (¬std::isfinite((*run).second.second)) res = false;
      ++run;
    }
    return res;
  }

```

This code is used in section 347.

**351.** This returns a ratio of a number of non-zero columns in folded tensor to the total number of columns.

```

⟨ SparseTensor::getFoldIndexFillFactor code 351 ⟩ ≡
  double SparseTensor::getFoldIndexFillFactor() const
  {
    int cnt = 0;
    const_iterator start_col = m.begin();
    while (start_col ≠ m.end()) {
      cnt++;
      const IntSequence &key = (*start_col).first;
      start_col = m.upper_bound(key);
    }
    return ((double) cnt)/ncols();
  }

```

This code is used in section 347.

**352.** This returns a ratio of a number of non-zero columns in unfolded tensor to the total number of columns.

```

⟨ SparseTensor::getUnfoldIndexFillFactor code 352 ⟩ ≡
  double SparseTensor::getUnfoldIndexFillFactor() const
  {
    int cnt = 0;
    const_iterator start_col = m.begin();
    while (start_col ≠ m.end()) {
      const IntSequence &key = (*start_col).first;
      Symmetry s(key);
      cnt += Tensor::noverseq(s);
      start_col = m.upper_bound(key);
    }
    return ((double) cnt)/ncols();
  }

```

This code is used in section 347.

**353.** This prints the fill factor and all items.

```

< SparseTensor::print code 353 > ≡
void SparseTensor::print() const
{
    printf("Fill: %3.2f%%\n", 100 * getFillFactor());
    const_iterator start_col = m.begin();
    while (start_col ≠ m.end()) {
        const IntSequence &key = (*start_col).first;
        printf("Column: ");
        key.print();

        const_iterator end_col = m.upper_bound(key);
        int cnt = 1;
        for (const_iterator run = start_col; run ≠ end_col; ++run, cnt++) {
            if ((cnt/7) * 7 ≡ cnt) printf("\n");
            printf("%d(%6.2g) ", (*run).second.first, (*run).second.second);
        }
        printf("\n");
        start_col = end_col;
    }
}

```

This code is used in section 347.

**354.**

```

< FSSparseTensor constructor code 354 > ≡
FSSparseTensor::FSSparseTensor(int d, int nvar, int r)
: SparseTensor(d, r, FFSensor::calcMaxOffset(nvar, d), nv(nvar), sym(d) {}

```

This code is used in section 347.

**355.**

```

< FSSparseTensor copy constructor code 355 > ≡
FSSparseTensor::FSSparseTensor(const FSSparseTensor &t)
: SparseTensor(t, nv(t.nvar()), sym(t.sym)) {}

```

This code is used in section 347.

**356.**

```

< FSSparseTensor::insert code 356 > ≡
void FSSparseTensor::insert(const IntSequence &key, int r, double c)
{
    TL_RAISE_IF(¬key.isSorted(), "Key is not sorted in FSSparseTensor::insert");
    TL_RAISE_IF(key[key.size() - 1] ≥ nv ∨ key[0] < 0,
        "Wrong value of the key in FSSparseTensor::insert");
    SparseTensor::insert(key, r, c);
}

```

This code is used in section 347.

**357.** We go through the tensor  $t$  which is supposed to have single column. If the item of  $t$  is nonzero, we make a key by sorting the index, and then we go through all items having the same key (it is its column), obtain the row number and the element, and do the multiplication.

The test for non-zero is  $a \neq 0.0$ , since there will be items which are exact zeros.

I have also tried to make the loop through the sparse tensor outer, and find index of tensor  $t$  within the loop. Surprisingly, it is little slower (for monomial tests with probability of zeros equal 0.3). But everything depends how filled is the sparse tensor.

```

< FSSparseTensor::multColumnAndAdd code 357 > ≡
void FSSparseTensor::multColumnAndAdd(const Tensor &t, Vector &v) const
{
    < check compatibility of input parameters 358 >;
    for (Tensor::index it = t.begin(); it != t.end(); ++it) {
        int ind = *it;
        double a = t.get(ind, 0);
        if (a != 0.0) {
            IntSequence key(it.getCoor());
            key.sort();
            < check that key is within the range 359 >;
            const_iterator first_pos = m.lower_bound(key);
            const_iterator last_pos = m.upper_bound(key);
            for (const_iterator cit = first_pos; cit != last_pos; ++cit) {
                int r = (*cit).second.first;
                double c = (*cit).second.second;
                v[r] += c * a;
            }
        }
    }
}

```

This code is used in section 347.

**358.**

```

< check compatibility of input parameters 358 > ≡
TL_RAISE_IF(v.length() != nrows(),
    "Wrong_size_of_output_vector_in_FSSparseTensor::multColumnAndAdd");
TL_RAISE_IF(t.dimen() != dimen(),
    "Wrong_dimension_of_tensor_in_FSSparseTensor::multColumnAndAdd");
TL_RAISE_IF(t.ncols() != 1,
    "The_input_tensor_is_not_single-column_in_FSSparseTensor::multColumnAndAdd");

```

This code is used in section 357.

**359.**

```

< check that key is within the range 359 > ≡
TL_RAISE_IF(key[0] < 0 ∨ key[key.size() - 1] ≥ nv,
    "Wrong_coordinates_of_index_in_FSSparseTensor::multColumnAndAdd");

```

This code is used in section 357.



360.

```

< FSSparseTensor::print code 360 > ≡
void FSSparseTensor::print() const
{
    printf("FS_Sparse_tensor: dim=%d, nv=%d, (%dx%d)\n", dim, nv, nr, nc);
    SparseTensor::print();
}

```

This code is used in section 347.

361. This is the same as <FGSTensor slicing from FSSparseTensor 271>.

```

< GSSparseTensor slicing constructor 361 > ≡
GSSparseTensor::GSSparseTensor(const FSSparseTensor &t, const IntSequence &ss, const
    IntSequence &coord, const TensorDimens &td)
: SparseTensor(td.dimen(), t.nrows(), td.calcFoldMaxOffset()), tdims(td) {
    < set lb and ub to lower and upper bounds of slice indices 362 >;
    FSSparseTensor::const_iterator lbi = t.getMap().lower_bound(lb);
    FSSparseTensor::const_iterator ubi = t.getMap().upper_bound(ub);
    for (FSSparseTensor::const_iterator run = lbi; run != ubi; ++run) {
        if (lb.lessEq((*run).first) & (*run).first.lessEq(ub)) {
            IntSequence c((*run).first);
            c.add(-1, lb);
            insert(c, (*run).second.first, (*run).second.second);
        }
    }
}

```

This code is used in section 347.

362. This is the same as <set lb and ub to lower and upper bounds of indices 272> in `gs_tensor.cpp`, see that file for details.

```

< set lb and ub to lower and upper bounds of slice indices 362 > ≡
IntSequence s_offsets(ss.size(), 0);
for (int i = 1; i < ss.size(); i++) s_offsets[i] = s_offsets[i - 1] + ss[i - 1];
IntSequence lb(coord.size());
IntSequence ub(coord.size());
for (int i = 0; i < coord.size(); i++) {
    lb[i] = s_offsets[coord[i]];
    ub[i] = s_offsets[coord[i]] + ss[coord[i]] - 1;
}

```

This code is used in section 361.

363.

```

< GSSparseTensor::insert code 363 > ≡
void GSSparseTensor::insert(const IntSequence &s, int r, double c)
{
    TL_RAISE_IF(!s.less(tdims.getNVX()),
        "Wrong coordinates of index in GSSparseTensor::insert");
    SparseTensor::insert(s, r, c);
}

```

This code is used in section 347.

**364.**

```

< GSSparseTensor::print code 364 > ≡
void GSSparseTensor::print() const
{
    printf("GS_Sparse_tensor:_(%dx%d)\nSymmetry:_", nr, nc);
    tdims.getSym().print();
    printf("NVS:_");
    tdims.getNVS().print();
    SparseTensor::print();
}

```

This code is used in section 347.

**365.** End of `sparse_tensor.cpp` file.

**366. The Faa Di Bruno formula.****367. Tensor containers.** Start of `t_container.h` file.

One of primary purposes of the tensor library is to perform one step of the Faa Di Bruno formula:

$$[B_{s^k}]_{\alpha_1 \dots \alpha_k} = [h_{y^l}]_{\gamma_1 \dots \gamma_l} \sum_{c \in M_{l,k}} \prod_{m=1}^l [g_{s^{|c_m|}}]_{c_m(\alpha)}^{\gamma_m}$$

where  $h_{y^l}$  and  $g_{s^i}$  are tensors,  $M_{l,k}$  is a set of all equivalences with  $l$  classes of  $k$  element set,  $c_m$  is  $m$ -the class of equivalence  $c$ , and  $|c_m|$  is its cardinality. Further,  $c_m(\alpha)$  is a sequence of  $\alpha$ s picked by equivalence class  $c_m$ .

In order to accomplish this operation, we basically need some storage of all tensors of the form  $[g_{s^i}]$ . Note that  $s$  can be compound, for instance  $s = [y, u]$ . Then we need storage for  $[g_{y^3}]$ ,  $[g_{y^2u}]$ ,  $[g_{yu^5}]$ , etc.

We need an object holding all tensors of the same type. Here type means an information, that coordinates of the tensors can be of type  $y$ , or  $u$ . We will group only tensors, whose symmetry is described by **Symmetry** class. These are only  $y^2u^3$ , not  $yuyu^2$ . So, we are going to define a class which will hold tensors whose symmetries are of type **Symmetry** and have the same symmetry length (number of different coordinate types). Also, for each symmetry there will be at most one tensor.

The class has two purposes: The first is to provide storage (insert and retrieve). The second is to perform the above step of Faa Di Bruno. This is going through all equivalences with  $l$  classes, perform the tensor product and add to the result.

We define a template class *TensorContainer*. From different instantiations of the template class we will inherit to create concrete classes, for example container of unfolded general symmetric tensors. The one step of the Faa Di Bruno (we call it *multAndAdd*) is implemented in the concrete subclasses, because the implementation depends on storage. Note even, that *multAndAdd* has not a template common declaration. This is because sparse tensor  $h$  is multiplied by folded tensors  $g$  yielding folded tensor  $B$ , but unfolded tensor  $h$  is multiplied by unfolded tensors  $g$  yielding unfolded tensor  $B$ .

```
#ifndef T_CONTAINER_H
#define T_CONTAINER_H
#include "symmetry.h"
#include "gs_tensor.h"
#include "tl_exception.h"
#include "tl_static.h"
#include "sparse_tensor.h"
#include "equivalence.h"
#include "rfs_tensor.h"
#include "Vector.h"
#include <map>
#include <string>
#include <sstream>
#include <map>
    { ltsym predicate 368 };
    { TensorContainer class definition 369 };
    { UGSContainer class declaration 383 };
    { FGSContainer class declaration 384 };
#endif
```

**368.** We need a predicate on strict weak ordering of symmetries.

```
⟨ ltsym predicate 368 ⟩ ≡  
  struct ltsym {  
    bool operator()(const Symmetry &s1, const Symmetry &s2) const  
    { return s1 < s2; }  
  };
```

This code is used in section 367.

**369.** Here we define the template class for tensor container. We implement it as *stl* ::**map**. It is a unique container, no two tensors with same symmetries can coexist. Keys of the map are symmetries, values are pointers to tensor. The class is responsible for deallocating all tensors. Creation of the tensors is done outside.

The class has integer *n* as its member. It is a number of different coordinate types of all contained tensors. Besides intuitive insert and retrieve interface, we define a method *fetchTensors*, which for a given symmetry and given equivalence calculates symmetries implied by the symmetry and all equivalence classes, and fetches corresponding tensors in a vector.

Also, each instance of the container has a reference to **EquivalenceBundle** which allows an access to equivalences.

```

⟨ TensorContainer class definition 369 ⟩ ≡
  template⟨class _Ttype⟩ class TensorContainer {
  protected:
    typedef const _Ttype *_const_ptr;
    typedef _Ttype *_ptr;
    typedef map⟨Symmetry, _ptr, ltsym⟩ _Map;
    typedef typename _Map::value_type mvtype;
  public:
    typedef typename _Map::iterator iterator;
    typedef typename _Map::const_iterator const_iterator;
  private:
    int n;
    _Map m;
  protected:
    const EquivalenceBundle &ebundle;
  public:
    TensorContainer(int nn)
    : n(nn), ebundle(*(tls.ebundle)) { }
    ⟨ TensorContainer copy constructor 371 ⟩;
    ⟨ TensorContainer subtensor constructor 372 ⟩;
    ⟨ TensorContainer : get code 373 ⟩;
    ⟨ TensorContainer::check code 374 ⟩;
    ⟨ TensorContainer::insert code 375 ⟩;
    ⟨ TensorContainer::remove code 376 ⟩;
    ⟨ TensorContainer::clear code 377 ⟩;
    ⟨ TensorContainer::fetchTensors code 382 ⟩;
    ⟨ TensorContainer::getMaxDim code 378 ⟩;
    ⟨ TensorContainer::print code 379 ⟩;
    ⟨ TensorContainer::writeMat code 380 ⟩;
    ⟨ TensorContainer::writeMMap code 381 ⟩;
    virtual ~TensorContainer()
    { clear(); }
    ⟨ TensorContainer inline methods 370 ⟩;
  };

```

This code is used in section 367.

**370.**

```

< TensorContainer inline methods 370 > ≡
    int num() const
    { return n; }

    const EquivalenceBundle &getEqBundle() const
    { return ebundle; }

    const_iterator begin() const
    { return m.begin(); }

    const_iterator end() const
    { return m.end(); }

    iterator begin()
    { return m.begin(); }

    iterator end()
    { return m.end(); }

```

This code is used in section 369.

**371.** This is just a copy constructor. This makes a hard copy of all tensors.

```

< TensorContainer copy constructor 371 > ≡
    TensorContainer(const TensorContainer<_Ttype> &c)
    : n(c.n), m(), ebundle(c.ebundle) {
        for (const_iterator it = c.m.begin(); it ≠ c.m.end(); ++it) {
            _Ttype *ten = new _Ttype(*(*it).second);
            insert(ten);
        }
    }

```

This code is used in section 369.

**372.** This constructor constructs a new tensor container, whose tensors are in-place subtensors of the given container.

```

< TensorContainer subtensor constructor 372 > ≡
    TensorContainer(int first_row, int num, TensorContainer<_Ttype> &c)
    : n(c.n), ebundle(*(tls.ebundle)) {
        for (iterator it = c.m.begin(); it ≠ c.m.end(); ++it) {
            _Ttype *t = new _Ttype(first_row, num, *(*it).second);
            insert(t);
        }
    }

```

This code is used in section 369.

**373.**

```

⟨ TensorContainer : get code 373 ⟩ ≡
  _const_ptr get(const Symmetry &s) const
  {
    TL_RAISE_IF(s.num() ≠ num(), "Incompatible_symmetry_lookup_in_TensorContainer::get");
    const_iterator it = m.find(s);
    if (it ≡ m.end()) {
      TL_RAISE("Symmetry_not_found_in_TensorContainer::get");
      return Λ;
    }
    else {
      return (*it).second;
    }
  }
_ptr get(const Symmetry &s)
{
  TL_RAISE_IF(s.num() ≠ num(), "Incompatible_symmetry_lookup_in_TensorContainer::get");
  iterator it = m.find(s);
  if (it ≡ m.end()) {
    TL_RAISE("Symmetry_not_found_in_TensorContainer::get");
    return Λ;
  }
  else {
    return (*it).second;
  }
}

```

This code is used in section 369.

**374.**

```

⟨ TensorContainer::check code 374 ⟩ ≡
  bool check(const Symmetry &s) const
  {
    TL_RAISE_IF(s.num() ≠ num(), "Incompatible_symmetry_lookup_in_TensorContainer::check");
    const_iterator it = m.find(s);
    return it ≠ m.end();
  }

```

This code is used in section 369.

**375.**

```

< TensorContainer::insert code 375 > ≡
void insert(_ptr t)
{
    TL_RAISE_IF(t->getSym().num() ≠ num(),
        "Incompatible_symmetry_insertion_in_TensorContainer::insert");
    TL_RAISE_IF(check(t->getSym()), "Tensor_already_in_container_in_TensorContainer::insert");
    m.insert(_mvtype(t->getSym(),t));
    if (¬t->isFinite()) {
        throw TLEException(__FILE__, __LINE__,
            "NaN_or_Inf_asserted_in_TensorContainer::insert");
    }
}

```

This code is used in section 369.

**376.**

```

< TensorContainer::remove code 376 > ≡
void remove(const Symmetry &s)
{
    iterator it = m.find(s);
    if (it ≠ m.end()) {
        _ptr t = (*it).second;
        m.erase(it);
        delete t;
    }
}

```

This code is used in section 369.

**377.**

```

< TensorContainer::clear code 377 > ≡
void clear()
{
    while (¬m.empty()) {
        delete (*(m.begin())).second;
        m.erase(m.begin());
    }
}

```

This code is used in section 369.



**378.**

```

< TensorContainer::getMaxDim code 378 > ≡
int getMaxDim() const
{
    int res = -1;
    for (const_iterator run = m.begin(); run ≠ m.end(); ++run) {
        int dim = (*run).first.dimen();
        if (dim > res) res = dim;
    }
    return res;
}

```

This code is used in section 369.

**379.** Debug print.

```

< TensorContainer::print code 379 > ≡
void print() const
{
    printf("Tensor_container: nvars=%d, tensors=%D\n", n, m.size());
    for (const_iterator it = m.begin(); it ≠ m.end(); ++it) {
        printf("Symmetry: ");
        (*it).first.print();
        ((*it).second)→print();
    }
}

```

This code is used in section 369.

**380.** Output to the MAT file.

```

< TensorContainer::writeMat code 380 > ≡
void writeMat(mat_t *fd, const char *prefix) const
{
    for (const_iterator it = begin(); it ≠ end(); ++it) {
        char lname[100];
        sprintf(lname, "%s_g", prefix);
        const Symmetry &sym = (*it).first;
        for (int i = 0; i < sym.num(); i++) {
            char tmp[10];
            sprintf(tmp, "%d", sym[i]);
            strcat(lname, tmp);
        }
        ConstTwoDMatrix m((*(it).second));
        m.writeMat(fd, lname);
    }
}

```

This code is used in section 369.

**381.** Output to the Memory Map.

```
<TensorContainer::writeMMap code 381> ≡
void writeMMap(map<string, ConstTwoDMatrix> &mm, const string &prefix) const
{
    ostringstream lname;
    for (const_iterator it = begin(); it ≠ end(); ++it) {
        lname.str(prefix);
        lname << "_g";
        const Symmetry &sym = (*it).first;
        for (int i = 0; i < sym.num(); i++) lname << "-" << sym[i];
        mm.insert(make_pair(lname.str(), ConstTwoDMatrix(*((*it).second))));
    }
}
```

This code is used in section 369.

**382.** Here we fetch all tensors given by symmetry and equivalence. We go through all equivalence classes, calculate implied symmetry, and fetch its tensor storing it in the same order to the vector.

```
<TensorContainer::fetchTensors code 382> ≡
vector<_const_ptr> fetchTensors(const Symmetry &rsym, const Equivalence &e) const
{
    vector<_const_ptr> res(e.numClasses());
    int i = 0;
    for (Equivalence::const_seq it = e.begin(); it ≠ e.end(); ++it, i++) {
        Symmetry s(rsym, *it);
        res[i] = get(s);
    }
    return res;
}
```

This code is used in section 369.

**383.** Here is a container storing **UGSTensors**. We declare *multAndAdd* method.

```
<UGSContainer class declaration 383> ≡
class FGSContainer;
class UGSContainer : public TensorContainer<UGSTensor> {
public:
    UGSContainer(int nn)
    : TensorContainer<UGSTensor>(nn) {}
    UGSContainer(const UGSContainer &uc)
    : TensorContainer<UGSTensor>(uc) {}
    UGSContainer(const FGSContainer &c);
    void multAndAdd(const UGSTensor &t, UGSTensor &out) const;
};
```

This code is cited in section 1.

This code is used in section 367.

**384.** Here is a container storing **FGSTensors**. We declare two versions of *multAndAdd* method. The first works for folded  $B$  and folded  $h$  tensors, the second works for folded  $B$  and unfolded  $h$ . There is no point to do it for unfolded  $B$  since the algorithm go through all the indices of  $B$  and calculates corresponding columns. So, if  $B$  is needed unfolded, it is more effective to calculate its folded version and then unfold by conversion.

The static member *num\_one\_time* is a number of columns formed from product of  $g$  tensors at one time. This is subject to change, probably we will have to do some tuning and decide about this number based on symmetries, and dimensions in the runtime.

(**FGSContainer** class declaration 384)  $\equiv$

```
class FGSContainer : public TensorContainer<FGSTensor> {
    static const int num_one_time;
public:
    FGSContainer(int nn)
    : TensorContainer<FGSTensor>(nn) {}
    FGSContainer(const FGSContainer &fc)
    : TensorContainer<FGSTensor>(fc) {}
    FGSContainer(const UGSContainer &c);
    void multAndAdd(const FGSTensor &t, FGSTensor &out) const;
    void multAndAdd(const UGSTensor &t, FGSTensor &out) const;
private:
    static Tensor::index getIndices(int num, vector<IntSequence> &out, const Tensor::index
        &start, const Tensor::index &end);
};
```

This code is cited in section 1.

This code is used in section 367.

**385.** End of `t_container.h` file.

**386.** Start of `t_container.cpp` file.

```
#include "t_container.h"
#include "kron_prod.h"
#include "ps_tensor.h"
#include "pyramid_prod.h"
const int FGSContainer::num_one_time = 10;
<UGSContainer conversion from FGSContainer 387>;
<UGSContainer::multAndAdd code 388>;
<FGSContainer conversion from UGSContainer 389>;
<FGSContainer::multAndAdd folded code 390>;
<FGSContainer::multAndAdd unfolded code 391>;
<FGSContainer::getIndices code 392>;
```

387.

```

⟨ UGSContainer conversion from FGSContainer 387 ⟩ ≡
  UGSContainer::UGSContainer(const FGSContainer &c)
  : TensorContainer<UGSTensor>(c.num()) {
    for (FGSContainer::const_iterator it = c.begin(); it ≠ c.end(); ++it) {
      UGSTensor *unfolded = new UGSTensor(*(*it).second);
      insert(unfolded);
    }
  }

```

This code is used in section 386.

**388.** We set  $l$  to dimension of  $t$ , this is a tensor which multiplies tensors from the container from the left. Also we set  $k$  to a dimension of the resulting tensor. We go through all equivalences on  $k$  element set and pickup only those which have  $l$  classes.

In each loop, we fetch all necessary tensors for the product to the vector  $ts$ . Then we form Kronecker product **KronProdAll** and feed it with tensors from  $ts$ . Then we form unfolded permuted symmetry tensor **UPSTensor** as matrix product of  $t$  and Kronecker product  $kp$ . Then we add the permuted data to  $out$ . This is done by **UPSTensor** method *addTo*.

```

⟨ UGSContainer::multAndAdd code 388 ⟩ ≡
  void UGSContainer::multAndAdd(const UGSTensor &t, UGSTensor &out) const
  {
    int l = t.dimen();
    int k = out.dimen();
    const EquivalenceSet &eset = ebundle.get(k);
    for (EquivalenceSet::const_iterator it = eset.begin(); it ≠ eset.end(); ++it) {
      if ((*it).numClasses() ≡ l) {
        vector<const UGSTensor*> ts = fetchTensors(out.getSym(), *it);
        KronProdAllOptim kp(l);
        for (int i = 0; i < l; i++) kp.setMat(i, *(ts[i]));
        kp.optimizeOrder();
        UPSTensor ups(out.getDims(), *it, t, kp);
        ups.addTo(out);
      }
    }
  }

```

This code is cited in section 391.

This code is used in section 386.

389.

```

⟨ FGSContainer conversion from UGSContainer 389 ⟩ ≡
  FGSContainer::FGSContainer(const UGSContainer &c)
  : TensorContainer<FGSTensor>(c.num()) {
    for (UGSContainer::const_iterator it = c.begin(); it ≠ c.end(); ++it) {
      FGSTensor *folded = new FGSTensor(*(*it).second);
      insert(folded);
    }
  }

```

This code is used in section 386.

**390.** Here we perform one step of the Faa Di Bruno operation. We call the *multAndAdd* for unfolded tensor.

```

⟨ FGSContainer :: multAndAdd folded code 390 ⟩ ≡
  void FGSContainer :: multAndAdd(const FGSTensor &t, FGSTensor &out) const
  {
    UGSTensor ut(t);
    multAndAdd(ut, out);
  }

```

This code is cited in section 470.

This code is used in section 386.

**391.** This is the same as ⟨ **UGSContainer** :: *multAndAdd* code 388 ⟩ but we do not construct **UPSTensor** from the Kronecker product, but **FPSTensor**.

```

⟨ FGSContainer :: multAndAdd unfolded code 391 ⟩ ≡
  void FGSContainer :: multAndAdd(const UGSTensor &t, FGSTensor &out) const
  {
    int l = t.dimen();
    int k = out.dimen();
    const EquivalenceSet &eset = ebundle.get(k);
    for (EquivalenceSet :: const_iterator it = eset.begin(); it ≠ eset.end(); ++it) {
      if ((*it).numClasses() ≡ l) {
        vector<const FGSTensor *> ts = fetchTensors(out.getSym(), *it);
        KronProdAllOptim kp(l);
        for (int i = 0; i < l; i++) kp.setMat(i, *(ts[i]));
        kp.optimizeOrder();
        FPSTensor fps(out.getDims(), *it, t, kp);
        fps.addTo(out);
      }
    }
  }

```

This code is cited in section 470.

This code is used in section 386.

**392.** This fills a given vector with integer sequences corresponding to first *num* indices from interval *start* (including) to *end* (excluding). If there are not *num* of such indices, the shorter vector is returned.

$\langle \text{FGSContainer}::\text{getIndices}$  code 392  $\rangle \equiv$

```
Tensor::index FGSContainer::getIndices(int num, vector<IntSequence> &out, const
    Tensor::index &start, const Tensor::index &end)
{
    out.clear();
    int i = 0;
    Tensor::index run = start;
    while (i < num ^ run != end) {
        out.push_back(run.getCoor());
        i++;
        ++run;
    }
    return run;
}
```

This code is used in section 386.

**393.** End of `t_container.cpp` file.

**394. Stack of containers.** Start of `stack_container.h` file.

Here we develop abstractions for stacked containers of tensors. For instance, in perturbation methods for SDGE we need function

$$z(y, u, u', \sigma) = \begin{bmatrix} G(y, u, u', \sigma) \\ g(y, u, \sigma) \\ y \\ u \end{bmatrix}$$

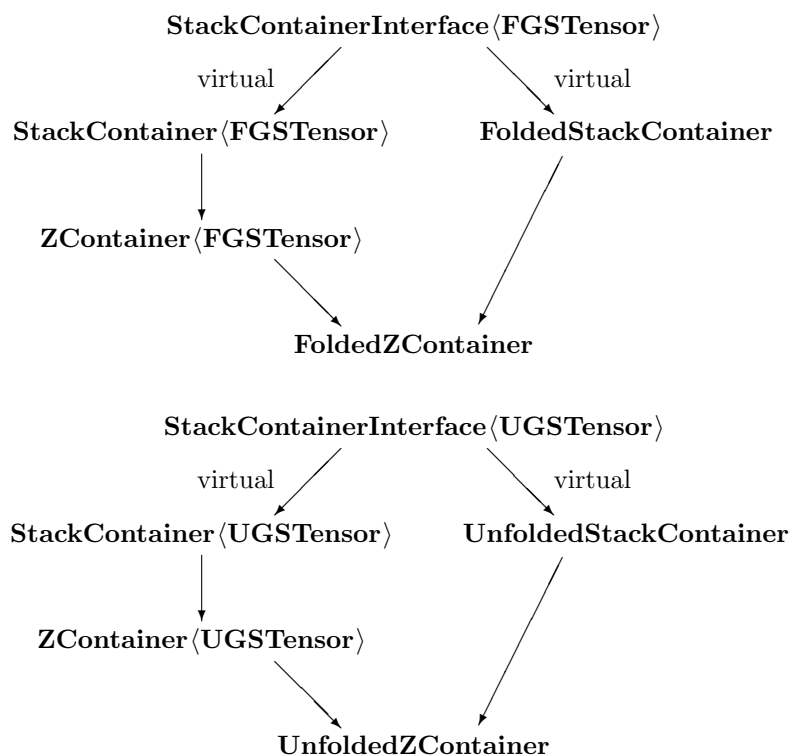
and we need to calculate one step of Faa Di Bruno formula

$$[B_{s^k}]_{\alpha_1 \dots \alpha_l} = [f_{z^l}]_{\beta_1 \dots \beta_l} \sum_{c \in M_{l,k}} \prod_{m=1}^l [z_{s^k(c_m)}]_{c_m(\alpha)}^{\beta_m}$$

where we have containers for derivatives of  $G$  and  $g$ .

The main purpose of this file is to define abstractions for stack of containers and possibly raw variables, and code *multAndAdd* method calculating (one step of) the Faa Di Bruno formula for folded and unfolded tensors. Note also, that tensors  $[f_{z^l}]$  are sparse.

The abstractions are built as follows. At the top, there is an interface describing stack of columns. It contains pure virtual methods needed for manipulating the container stack. For technical reasons it is a template. Both versions (folded, and unfolded) provide all interface necessary for implementation of *multAndAdd*. The second way of inheritance is first general implementation of the interface **StackContainer**, and then specific (**ZContainer** for our specific  $z$ ). The only method which is virtual also after **StackContainer** is *getType*, which is implemented in the specialization and determines behaviour of the stack. The complete classes are obtained by inheriting from the both branches, as it is drawn below:



We have also two supporting classes **StackProduct** and **KronProdStack** and a number of worker classes used as threads.

```
#ifndef STACK_CONTAINER_H
```

```

#define STACK_CONTAINER_H
#include "int_sequence.h"
#include "equivalence.h"
#include "tl_static.h"
#include "t_container.h"
#include "kron_prod.h"
#include "permutation.h"
#include "pthread.h"
  < StackContainerInterface class declaration 395 >;
  < StackContainer class declaration 396 >;
  < FoldedStackContainer class declaration 403 >;
  < UnfoldedStackContainer class declaration 404 >;
  < ZContainer class declaration 405 >;
  < FoldedZContainer class declaration 407 >;
  < UnfoldedZContainer class declaration 408 >;
  < GContainer class declaration 409 >;
  < FoldedGContainer class declaration 411 >;
  < UnfoldedGContainer class declaration 412 >;
  < StackProduct class declaration 413 >;
  < KronProdStack class declaration 420 >;
  < WorkerFoldMAADense class declaration 422 >;
  < WorkerFoldMAASparse1 class declaration 423 >;
  < WorkerFoldMAASparse2 class declaration 424 >;
  < WorkerFoldMAASparse4 class declaration 425 >;
  < WorkerUnfoldMAADense class declaration 426 >;
  < WorkerUnfoldMAASparse1 class declaration 427 >;
  < WorkerUnfoldMAASparse2 class declaration 428 >;
#endif

```



**395.** Here is the general interface to stack container. The subclasses maintain **IntSequence** of stack sizes, i.e. size of  $G$ ,  $g$ ,  $y$ , and  $u$ . Then a convenience **IntSequence** of stack offsets. Then vector of pointers to containers, in our example  $G$ , and  $g$ .

A non-virtual subclass must implement *getType* which determines dependency of stack items on symmetries. There are three possible types for a symmetry. Either the stack item derivative wrt. the symmetry is a matrix, or a unit matrix, or zero.

Method *isZero* returns true if the derivative of a given stack item wrt. to given symmetry is zero as defined by *getType* or the derivative is not present in the container. In this way, we can implement the formula conditional some of the tensors are zero, which is not true (they are only missing).

Method *createPackedColumn* returns a vector of stack derivatives with respect to the given symmetry and of the given column, where all zeros from zero types, or unit matrices are deleted. See `kron_prod2.hweb` for explanation.

⟨ **StackContainerInterface** class declaration 395 ⟩ ≡

```
template<class _Ttype>
class StackContainerInterface {
public:
    typedef TensorContainer<_Ttype> _Ctype;
    typedef enum { matrix, unit, zero } itype;
protected:
    const EquivalenceBundle &ebundle;
public:
    StackContainerInterface()
    : ebundle(*(tls.ebundle)) {}
    virtual ~StackContainerInterface() {}
    virtual const IntSequence &getStackSizes() const = 0;
    virtual IntSequence &getStackSizes() = 0;
    virtual const IntSequence &getStackOffsets() const = 0;
    virtual IntSequence &getStackOffsets() = 0;
    virtual int numConts() const = 0;
    virtual const _Ctype *getCont(int i) const = 0;
    virtual itype getType(int i, const Symmetry &s) const = 0;
    virtual int numStacks() const = 0;
    virtual bool isZero(int i, const Symmetry &s) const = 0;
    virtual const _Ttype *getMatrix(int i, const Symmetry &s) const = 0;
    virtual int getLengthOfMatrixStacks(const Symmetry &s) const = 0;
    virtual int getUnitPos(const Symmetry &s) const = 0;
    virtual Vector *createPackedColumn(const Symmetry &s, const IntSequence &coord, int &iu)
        const = 0;
    int getAllSize() const
    { return getStackOffsets()[numStacks() - 1] + getStackSizes()[numStacks() - 1]; }
};
```

This code is cited in section 1.

This code is used in section 394.

**396.** Here is **StackContainer**, which implements almost all interface **StackContainerInterface** but one method *getType* which is left for implementation to specializations.

```

<StackContainer class declaration 396> ≡
    template<class _Ttype>
    class StackContainer : virtual public StackContainerInterface<_Ttype> {
    public:
        typedef StackContainerInterface<_Ttype> _Stype;
        typedef typename StackContainerInterface<_Ttype>::_Ctype _Ctype;
        typedef typename StackContainerInterface<_Ttype>::itype itype;
    protected:
        int num_conts;
        IntSequence stack_sizes;
        IntSequence stack_offsets;
        const _Ctype **const conts;
    public:
        StackContainer(int ns,int nc)
        : num_conts(nc), stack_sizes(ns,0), stack_offsets(ns,0), conts(new const _Ctype*[nc]) { }
        virtual ~StackContainer() {
            delete[] conts;
        }
        const IntSequence &getStackSizes() const
        { return stack_sizes; }
        IntSequence &getStackSizes()
        { return stack_sizes; }
        const IntSequence &getStackOffsets() const
        { return stack_offsets; }
        IntSequence &getStackOffsets()
        { return stack_offsets; }
        int numConts() const
        { return num_conts; }
        const _Ctype *getCont(int i) const
        { return conts[i]; }
        virtual itype getType(int i,const Symmetry &s) const = 0;
        int numStacks() const
        { return stack_sizes.size(); }
        <StackContainer::isZero code 397>;
        <StackContainer::getMatrix code 398>;
        <StackContainer::getLengthOfMatrixStacks code 399>;
        <StackContainer::getUnitPos code 400>;
        <StackContainer::createPackedColumn code 401>;
    protected:
        <StackContainer::calculateOffsets code 402>;
    };

```

This code is used in section 394.

**397.**

```

⟨ StackContainer :: isZero code 397 ⟩ ≡
  bool isZero(int i, const Symmetry &s) const
  {
    TL_RAISE_IF(i < 0 ∨ i ≥ numStacks(), "Wrong_index_to_stack_in_StackContainer::isZero.");
    return (getType(i, s) ≡ _Stype::zero ∨ (getType(i, s) ≡ _Stype::matrix ∧ ¬conts[i]→check(s)));
  }

```

This code is used in section 396.

**398.**

```

⟨ StackContainer :: getMatrix code 398 ⟩ ≡
  const _Ttype *getMatrix(int i, const Symmetry &s) const
  {
    TL_RAISE_IF(isZero(i, s) ∨ getType(i, s) ≡ _Stype::unit,
      "Matrix_is_not_returned_in_StackContainer::getMatrix");
    return conts[i]→get(s);
  }

```

This code is used in section 396.

**399.**

```

⟨ StackContainer :: getLengthOfMatrixStacks code 399 ⟩ ≡
  int getLengthOfMatrixStacks(const Symmetry &s) const
  {
    int res = 0;
    int i = 0;
    while (i < numStacks() ∧ getType(i, s) ≡ _Stype::matrix) res += stack_sizes[i++];
    return res;
  }

```

This code is used in section 396.

**400.**

```

⟨ StackContainer :: getUnitPos code 400 ⟩ ≡
  int getUnitPos(const Symmetry &s) const
  {
    if (s.dimen() ≠ 1) return -1;
    int i = numStacks() - 1;
    while (i ≥ 0 ∧ getType(i, s) ≠ _Stype::unit) i--;
    return i;
  }

```

This code is used in section 396.

401.

```

< StackContainer::createPackedColumn code 401 > ≡
Vector *createPackedColumn(const Symmetry &s, const IntSequence &coord, int &iu) const
{
    TL_RAISE_IF(s.dimen() ≠ coord.size(),
        "Incompatible coordinates for symmetry in StackContainer::createPackedColumn");
    int len = getLengthOfMatrixStacks(s);
    iu = -1;
    int i = 0;
    if (-1 ≠ (i = getUnitPos(s))) {
        iu = stack_offsets[i] + coord[0];
        len++;
    }
    Vector *res = new Vector(len);
    i = 0;
    while (i < numStacks() ∧ getType(i, s) ≡ _Stype::matrix) {
        const _Ttype *t = getMatrix(i, s);
        Tensor::index ind(t, coord);
        Vector subres(*res, stack_offsets[i], stack_sizes[i]);
        subres = ConstVector(ConstGeneralMatrix(*t), *ind);
        i++;
    }
    if (iu ≠ -1) (*res)[len - 1] = 1;
    return res;
}

```

This code is used in section 396.

402.

```

< StackContainer::calculateOffsets code 402 > ≡
void calculateOffsets()
{
    stack_offsets[0] = 0;
    for (int i = 1; i < stack_offsets.size(); i++) stack_offsets[i] = stack_offsets[i - 1] + stack_sizes[i - 1];
}

```

This code is used in section 396.

403.

```

<FoldedStackContainer class declaration 403> ≡
class WorkerFoldMAADense;
class WorkerFoldMAASparse1;
class WorkerFoldMAASparse2;
class WorkerFoldMAASparse4;
class FoldedStackContainer : virtual public StackContainerInterface<FGSTensor> {
    friend class WorkerFoldMAADense;
    friend class WorkerFoldMAASparse1;
    friend class WorkerFoldMAASparse2;
    friend class WorkerFoldMAASparse4;
public:
    static double fill_threshold;
    void multAndAdd(int dim, const TensorContainer<FSSparseTensor> &c, FGSTensor &out)
        const
    { if (c.check(Symmetry(dim))) multAndAdd(*(c.get(Symmetry(dim))), out); }
    void multAndAdd(const FSSparseTensor &t, FGSTensor &out) const;
    void multAndAdd(int dim, const FGSTensor &c, FGSTensor &out) const;
protected:
    void multAndAddSparse1(const FSSparseTensor &t, FGSTensor &out) const;
    void multAndAddSparse2(const FSSparseTensor &t, FGSTensor &out) const;
    void multAndAddSparse3(const FSSparseTensor &t, FGSTensor &out) const;
    void multAndAddSparse4(const FSSparseTensor &t, FGSTensor &out) const;
    void multAndAddStacks(const IntSequence &fi, const FGSTensor &g, FGSTensor &out, const
        void *ad) const;
    void multAndAddStacks(const IntSequence &fi, const GSSparseTensor &g, FGSTensor
        &out, const void *ad) const;
};

```

This code is cited in section 1.

This code is used in section 394.

404.

```

< UnfoldedStackContainer class declaration 404 > ≡
class WorkerUnfoldMAADense;
class WorkerUnfoldMAASparse1;
class WorkerUnfoldMAASparse2;
class UnfoldedStackContainer : virtual public StackContainerInterface<UGSTensor> {
    friend class WorkerUnfoldMAADense;
    friend class WorkerUnfoldMAASparse1;
    friend class WorkerUnfoldMAASparse2;
public:
    static double fill_threshold;
    void multAndAdd(int dim, const TensorContainer<FSSparseTensor> &c, UGSTensor &out)
        const
    { if (c.check(Symmetry(dim))) multAndAdd(*(c.get(Symmetry(dim))), out); }
    void multAndAdd(const FSSparseTensor &t, UGSTensor &out) const;
    void multAndAdd(int dim, const UGSContainer &c, UGSTensor &out) const;
protected:
    void multAndAddSparse1(const FSSparseTensor &t, UGSTensor &out) const;
    void multAndAddSparse2(const FSSparseTensor &t, UGSTensor &out) const;
    void multAndAddStacks(const IntSequence &fi, const UGSTensor &g, UGSTensor &out, const
        void *ad) const;
};

```

This code is cited in section 1.

This code is used in section 394.

**405.** Here is the specialization of the **StackContainer**. We implement here the  $z$  needed in SDGE context. We implement *getType* and define a constructor feeding the data and sizes.

Note that it has two containers, the first is dependent on four variables  $G(y^*, u, u', \sigma)$ , and the second dependent on three variables  $g(y^*, u, \sigma)$ . So that we would be able to stack them, we make the second container  $g$  be dependent on four variables, the third being  $u'$  a dummy and always returning zero if dimension of  $u'$  is positive.

```

<ZContainer class declaration 405> ≡
template<class _Ttype>
class ZContainer : public StackContainer<_Ttype> {
public:
    typedef StackContainer<_Ttype> _Tparent;
    typedef StackContainerInterface<_Ttype> _Stype;
    typedef typename _Tparent::_Ctype _Ctype;
    typedef typename _Tparent::itype itype;

    ZContainer(const _Ctype *gss, int ngss, const _Ctype *g, int ng, int ny, int nu)
    : _Tparent(4, 2) {
        _Tparent::stack_sizes[0] = ngss;
        _Tparent::stack_sizes[1] = ng;
        _Tparent::stack_sizes[2] = ny;
        _Tparent::stack_sizes[3] = nu;
        _Tparent::conts[0] = gss;
        _Tparent::conts[1] = g;
        _Tparent::calculateOffsets();
    }
    <ZContainer::getType code 406>;
};

```

This code is cited in sections 1 and 409.

This code is used in section 394.

**406.** Here we say, what happens if we derive  $z$ . recall the top of the file, how  $z$  looks, and code is clear.

```

<ZContainer::getType code 406> ≡
itype getType(int i, const Symmetry &s) const
{
    if (i ≡ 0) return _Stype::matrix;
    if (i ≡ 1)
        if (s[2] > 0) return _Stype::zero;
        else return _Stype::matrix;
    if (i ≡ 2)
        if (s ≡ Symmetry(1, 0, 0, 0)) return _Stype::unit;
        else return _Stype::zero;
    if (i ≡ 3)
        if (s ≡ Symmetry(0, 1, 0, 0)) return _Stype::unit;
        else return _Stype::zero;
    TL_RAISE("Wrong_stack_index_in_ZContainer::getType");
    return _Stype::zero;
}

```

This code is used in section 405.

407.

⟨ **FoldedZContainer** class declaration 407 ⟩ ≡

```
class FoldedZContainer : public ZContainer<FGSTensor>, public FoldedStackContainer {
public:
    typedef TensorContainer<FGSTensor> _Ctype;
    FoldedZContainer(const _Ctype *gss, int ngss, const _Ctype *g, int ng, int ny, int nu)
        : ZContainer<FGSTensor>(gss, ngss, g, ng, ny, nu) {}
};
```

This code is cited in section 1.

This code is used in section 394.

408.

⟨ **UnfoldedZContainer** class declaration 408 ⟩ ≡

```
class UnfoldedZContainer : public ZContainer<UGSTensor>, public UnfoldedStackContainer
{
public:
    typedef TensorContainer<UGSTensor> _Ctype;
    UnfoldedZContainer(const _Ctype *gss, int ngss, const _Ctype *g, int ng, int ny, int nu)
        : ZContainer<UGSTensor>(gss, ngss, g, ng, ny, nu) {}
};
```

This code is cited in section 1.

This code is used in section 394.



**409.** Here we have another specialization of container used in context of SDGE. We define a container for

$$G(y, u, u', \sigma) = g^{**}(g^*(y, u, \sigma), u', \sigma)$$

For some reason, the symmetry of  $g^{**}$  has length 4 although it is really dependent on three variables. (To now the reason, consult  $\langle \mathbf{ZContainer}$  class declaration 405.) So, it has four stack, the third one is dummy, and always returns zero. The first stack corresponds to a container of  $g^*$ .

```

 $\langle \mathbf{GContainer}$  class declaration 409  $\rangle \equiv$ 
  template $\langle$ class  $\_Ttype$  $\rangle$ 
  class GContainer : public StackContainer $\langle\_Ttype\rangle$  {
  public:
    typedef StackContainer $\langle\_Ttype\rangle$   $\_Tparent$ ;
    typedef StackContainerInterface $\langle\_Ttype\rangle$   $\_Stype$ ;
    typedef typename StackContainer $\langle\_Ttype\rangle$ :: $\_Ctype$   $\_Ctype$ ;
    typedef typename StackContainer $\langle\_Ttype\rangle$ ::itype itype;
    GContainer(const  $\_Ctype$  *gs, int ngs, int nu)
    : StackContainer $\langle\_Ttype\rangle$ (4, 1) {
       $\_Tparent$ ::stack_sizes[0] = ngs;
       $\_Tparent$ ::stack_sizes[1] = nu;
       $\_Tparent$ ::stack_sizes[2] = nu;
       $\_Tparent$ ::stack_sizes[3] = 1;
       $\_Tparent$ ::conts[0] = gs;
       $\_Tparent$ ::calculateOffsets();
    }
     $\langle \mathbf{GContainer}$ ::getType code 410  $\rangle$ ;
  };

```

This code is cited in section 1.

This code is used in section 394.

**410.** Here we define the dependencies in  $g^{**}(g^*(y, u, \sigma), u', \sigma)$ . Also note, that first derivative of  $g^*$  wrt  $\sigma$  is always zero, so we also add this information.

```

 $\langle \mathbf{GContainer}$ ::getType code 410  $\rangle \equiv$ 
  itype getType(int i, const Symmetry &s) const
  {
    if (i  $\equiv$  0)
      if (s[2] > 0  $\vee$  s  $\equiv$  Symmetry(0, 0, 0, 1)) return  $\_Stype$ ::zero;
      else return  $\_Stype$ ::matrix;
    if (i  $\equiv$  1)
      if (s  $\equiv$  Symmetry(0, 0, 1, 0)) return  $\_Stype$ ::unit;
      else return  $\_Stype$ ::zero;
    if (i  $\equiv$  2) return  $\_Stype$ ::zero;
    if (i  $\equiv$  3)
      if (s  $\equiv$  Symmetry(0, 0, 0, 1)) return  $\_Stype$ ::unit;
      else return  $\_Stype$ ::zero;
    TL_RAISE("Wrong_stack_index_in_GContainer::getType");
    return  $\_Stype$ ::zero;
  }

```

This code is used in section 409.

411.

〈**FoldedGContainer** class declaration 411〉 $\equiv$ 

```

class FoldedGContainer : public GContainer<FGSTensor>, public FoldedStackContainer {
public:
    typedef TensorContainer<FGSTensor> _Ctype;
    FoldedGContainer(const _Ctype *gs, int ngs, int nu)
    : GContainer<FGSTensor>(gs, ngs, nu) {}
};

```

This code is cited in section 1.

This code is used in section 394.

412.

〈**UnfoldedGContainer** class declaration 412〉 $\equiv$ 

```

class UnfoldedGContainer : public GContainer<UGSTensor>, public UnfoldedStackContainer
{
public:
    typedef TensorContainer<UGSTensor> _Ctype;
    UnfoldedGContainer(const _Ctype *gs, int ngs, int nu)
    : GContainer<UGSTensor>(gs, ngs, nu) {}
};

```

This code is cited in section 1.

This code is used in section 394.

**413.** Here we have a support class for product of **StackContainers**. It only adds a dimension to **StackContainer**. It selects the symmetries according to equivalence classes passed to the constructor. The equivalence can have permuted classes by some given permutation. Nothing else is interesting.

⟨**StackProduct** class declaration 413⟩ ≡

```
template<class _Ttype>
class StackProduct {
public:
    typedef StackContainerInterface<_Ttype> _Stype;
    typedef typename _Stype::_Ctype _Ctype;
    typedef typename _Stype::itype itype;

protected:
    const _Stype &stack_cont;
    InducedSymmetries syms;
    Permutation per;

public:
    StackProduct(const _Stype &sc, const Equivalence &e, const Symmetry &os)
        : stack_cont(sc), syms(e, os), per(e) {}

    StackProduct(const _Stype &sc, const Equivalence &e, const Permutation &p, const
                Symmetry &os)
        : stack_cont(sc), syms(e, p, os), per(e, p) {}

    int dimen() const
    { return syms.size(); }

    int getAllSize() const
    { return stack_cont.getAllSize(); }

    const Symmetry &getProdSym(int ip) const
    { return syms[ip]; }

    <StackProduct::isZero code 414>;
    <StackProduct::getType code 415>;
    <StackProduct::getMatrix code 416>;
    <StackProduct::createPackedColumns code 417>;
    <StackProduct::getSize code 418>;
    <StackProduct::numMatrices code 419>;
};
```

This code is used in section 394.

**414.**

⟨**StackProduct::isZero** code 414⟩ ≡

```
bool isZero(const IntSequence &istacks) const
{
    TL_RAISE_IF(istacks.size() != dimen(), "Wrong_istacks_coordinates_for_StackProduct::isZero");
    bool res = false;
    int i = 0;
    while (i < dimen() & ! (res = stack_cont.isZero(istacks[i], syms[i]))) i++;
    return res;
}
```

This code is used in section 413.

415.

```

⟨ StackProduct::getType code 415 ⟩ ≡
  itype getType(int is, int ip) const
  {
    TL_RAISE_IF(is < 0 ∨ is ≥ stack_cont.numStacks()),
      "Wrong_index_to_stack_in_StackProduct::getType");
    TL_RAISE_IF(ip < 0 ∨ ip ≥ dimen()),
      "Wrong_index_to_stack_container_in_StackProduct::getType");
    return stack_cont.getType(is, syms[ip]);
  }

```

This code is used in section 413.

416.

```

⟨ StackProduct::getMatrix code 416 ⟩ ≡
  const _Ttype *getMatrix(int is, int ip) const
  {
    return stack_cont.getMatrix(is, syms[ip]);
  }

```

This code is used in section 413.

417.

```

⟨ StackProduct::createPackedColumns code 417 ⟩ ≡
  void createPackedColumns(const IntSequence &coor, Vector **vs, IntSequence &iu) const
  {
    TL_RAISE_IF(iu.size() ≠ dimen()),
      "Wrong_storage_length_for_unit_flags_in_StackProduct::createPackedColumn");
    TL_RAISE_IF(coor.size() ≠ per.size()),
      "Wrong_size_of_index_coor_in_StackProduct::createPackedColumn");
    IntSequence perindex(coor.size());
    per.apply(coor, perindex);
    int off = 0;
    for (int i = 0; i < dimen(); i++) {
      IntSequence percoor(perindex, off, syms[i].dimen() + off);
      vs[i] = stack_cont.createPackedColumn(syms[i], percoor, iu[i]);
      off += syms[i].dimen();
    }
  }

```

This code is used in section 413.

418.

```

⟨ StackProduct::getSize code 418 ⟩ ≡
  int getSize(int is) const
  {
    return stack_cont.getStackSizes() [is];
  }

```

This code is used in section 413.

419.

```

⟨ StackProduct :: numMatrices code 419 ⟩ ≡
  int numMatrices(const IntSequence &istacks) const
  {
    TL_RAISE_IF(istacks.size() ≠ dimen(),
      "Wrong_size_of_stack_coordinates_in_StackContainer::numMatrices");
    int ret = 0;
    int ip = 0;
    while (ip < dimen() ∧ getType(istacks[ip], ip) ≡ _Stype::matrix) {
      ret++;
      ip++;
    }
    return ret;
  }

```

This code is used in section 413.

420. Here we only inherit from Kronecker product **KronProdAllOptim**, only to allow for a constructor constructing from **StackProduct**.

```

⟨ KronProdStack class declaration 420 ⟩ ≡
  template<class _Ttype> class KronProdStack : public KronProdAllOptim {
  public:
    typedef StackProduct<_Ttype> _Ptype;
    typedef StackContainerInterface<_Ttype> _Stype;
    ⟨ KronProdStack constructor code 421 ⟩;
  };

```

This code is used in section 394.

421. Here we construct **KronProdAllOptim** from **StackContainer** and given selections of stack items from stack containers in the product. We only decide whether to insert matrix, or unit matrix.

At this point, we do not call **KronProdAllOptim**::*optimizeOrder*, so the **KronProdStack** behaves like **KronProdAll** (i.e. no optimization is done).

```

⟨ KronProdStack constructor code 421 ⟩ ≡
  KronProdStack(const _Ptype &sp, const IntSequence &istack)
  : KronProdAllOptim(sp.dimen()) {
    TL_RAISE_IF(sp.dimen() ≠ istack.size(),
      "Wrong_stack_product_dimension_for_KronProdStack_constructor");
    for (int i = 0; i < sp.dimen(); i++) {
      TL_RAISE_IF(sp.getType(istack[i], i) ≡ _Stype::zero,
        "Attempt_to_construct_KronProdStack_from_zero_matrix");
      if (sp.getType(istack[i], i) ≡ _Stype::unit) setUnit(i, sp.getSize(istack[i]));
      if (sp.getType(istack[i], i) ≡ _Stype::matrix) {
        const TwoDMatrix *m = sp.getMatrix(istack[i], i);
        TL_RAISE_IF(m-nrows() ≠ sp.getSize(istack[i]),
          "Wrong_size_of_returned_matrix_in_KronProdStack_constructor");
        setMat(i, *m);
      }
    }
  }

```

This code is used in section 420.

422.

```

< WorkerFoldMAADense class declaration 422 > ≡
class WorkerFoldMAADense : public THREAD
{
    const FoldedStackContainer &cont;
    Symmetry sym;
    const FGSContainer &dense_cont;
    FGSTensor &out;
public:
    WorkerFoldMAADense(const FoldedStackContainer &container, const Symmetry &s, const
        FGSContainer &dcontainer, FGSTensor &outten);
    void operator()();
}
;

```

This code is used in section 394.

423.

```

< WorkerFoldMAASparse1 class declaration 423 > ≡
class WorkerFoldMAASparse1 : public THREAD
{
    const FoldedStackContainer &cont;
    const FSSparseTensor &t;
    FGSTensor &out;
    IntSequence coor;
    const EquivalenceBundle &ebundle;
public:
    WorkerFoldMAASparse1(const FoldedStackContainer &container, const FSSparseTensor
        &ten, FGSTensor &outten, const IntSequence &c);
    void operator()();
}
;

```

This code is used in section 394.

424.

```

< WorkerFoldMAASparse2 class declaration 424 > ≡
class WorkerFoldMAASparse2 : public THREAD
{
    const FoldedStackContainer &cont;
    const FSSparseTensor &t;
    FGSTensor &out;
    IntSequence coor;
public:
    WorkerFoldMAASparse2(const FoldedStackContainer &container, const FSSparseTensor
        &ten, FGSTensor &outten, const IntSequence &c);
    void operator()();
}
;

```

This code is used in section 394.

425.

```

< WorkerFoldMAASparse4 class declaration 425 > ≡
class WorkerFoldMAASparse4 : public THREAD
{
    const FoldedStackContainer &cont;
    const FSSparseTensor &t;
    FGSTensor &out;
    IntSequence coor;
public:
    WorkerFoldMAASparse4(const FoldedStackContainer &container, const FSSparseTensor
        &ten, FGSTensor &outten, const IntSequence &c);
    void operator()();
}
;

```

This code is used in section 394.

426.

```

< WorkerUnfoldMAADense class declaration 426 > ≡
class WorkerUnfoldMAADense : public THREAD
{
    const UnfoldedStackContainer &cont;
    Symmetry sym;
    const UGSContainer &dense_cont;
    UGSTensor &out;
public:
    WorkerUnfoldMAADense(const UnfoldedStackContainer &container, const Symmetry
        &s, const UGSContainer &dcontainer, UGSTensor &outten);
    void operator()();
}
;

```

This code is used in section 394.

427.

```

< WorkerUnfoldMAASparse1 class declaration 427 > ≡
class WorkerUnfoldMAASparse1 : public THREAD
{
    const UnfoldedStackContainer &cont;
    const FSSparseTensor &t;
    UGSTensor &out;
    IntSequence coor;
    const EquivalenceBundle &ebundle;
public:
    WorkerUnfoldMAASparse1(const UnfoldedStackContainer &container, const
        FSSparseTensor &ten, UGSTensor &outten, const IntSequence &c);
    void operator()();
}
;

```

This code is used in section 394.

428.

```

< WorkerUnfoldMAASparse2 class declaration 428 > ≡
class WorkerUnfoldMAASparse2 : public THREAD
{
    const UnfoldedStackContainer &cont;
    const FSSparseTensor &t;
    UGSTensor &out;
    IntSequence coor;
public:
    WorkerUnfoldMAASparse2(const UnfoldedStackContainer &container, const
        FSSparseTensor &ten, UGSTensor &outten, const IntSequence &c);
    void operator()();
}
;

```

This code is used in section 394.

429. End of `stack_container.h` file.



430. Start of `stack_container.cpp` file.

```
#include "stack_container.h"
#include "pyramid_prod2.h"
#include "ps_tensor.h"
double FoldedStackContainer::fill_threshold = 0.00005;
double UnfoldedStackContainer::fill_threshold = 0.00005;
< FoldedStackContainer::multAndAdd sparse code 431 >;
< FoldedStackContainer::multAndAdd dense code 432 >;
< WorkerFoldMAADense::operator>()() code 433 >;
< WorkerFoldMAADense constructor code 434 >;
< FoldedStackContainer::multAndAddSparse1 code 435 >;
< WorkerFoldMAASparse1::operator>()() code 436 >;
< WorkerFoldMAASparse1 constructor code 437 >;
< FoldedStackContainer::multAndAddSparse2 code 438 >;
< WorkerFoldMAASparse2::operator>()() code 439 >;
< WorkerFoldMAASparse2 constructor code 440 >;
< FoldedStackContainer::multAndAddSparse3 code 441 >;
< FoldedStackContainer::multAndAddSparse4 code 442 >;
< WorkerFoldMAASparse4::operator>()() code 443 >;
< WorkerFoldMAASparse4 constructor code 444 >;
< FoldedStackContainer::multAndAddStacks dense code 445 >;
< FoldedStackContainer::multAndAddStacks sparse code 446 >;
< UnfoldedStackContainer::multAndAdd sparse code 447 >;
< UnfoldedStackContainer::multAndAdd dense code 448 >;
< WorkerUnfoldMAADense::operator>()() code 449 >;
< WorkerUnfoldMAADense constructor code 450 >;
< UnfoldedStackContainer::multAndAddSparse1 code 451 >;
< WorkerUnfoldMAASparse1::operator>()() code 452 >;
< WorkerUnfoldMAASparse1 constructor code 453 >;
< UnfoldedStackContainer::multAndAddSparse2 code 454 >;
< WorkerUnfoldMAASparse2::operator>()() code 455 >;
< WorkerUnfoldMAASparse2 constructor code 456 >;
< UnfoldedStackContainer::multAndAddStacks code 457 >;
```

431. Here we multiply the sparse tensor with the **FoldedStackContainer**. We have four implementations, *multAndAddSparse1*, *multAndAddSparse2*, *multAndAddSparse3*, and *multAndAddSparse4*. The third is not threaded yet and I expect that it is certainly the slowest. The *multAndAddSparse4* exploits the sparsity, however, it seems to be still worse than *multAndAddSparse2* even for really sparse matrices. On the other hand, it can be more efficient than *multAndAddSparse2* for large problems, since it does not need that much of memory and can avoid much swapping. Very preliminary examination shows that *multAndAddSparse2* is the best in terms of time.

```
< FoldedStackContainer::multAndAdd sparse code 431 > ≡
void FoldedStackContainer::multAndAdd(const FSSparseTensor &t, FGSTensor &out) const
{
    TL_RAISE_IF(t.nvar() ≠ getAllSize(),
        "Wrong_number_of_variables_of_tensor_for_FoldedStackContainer::multAndAdd");
    multAndAddSparse2(t, out);
}
```

This code is used in section 430.

**432.** Here we perform the Faa Di Bruno step for a given dimension  $dim$ , and for the dense fully symmetric tensor which is scattered in the container of general symmetric tensors. The implementation is pretty the same as  $\langle \text{UnfoldedStackContainer}::multAndAdd$  dense code 448  $\rangle$ .

$\langle \text{FoldedStackContainer}::multAndAdd$  dense code 432  $\rangle \equiv$

```

void FoldedStackContainer::multAndAdd(int dim, const FGSContainer &c, FGSTensor &out)
    const
{
    TL_RAISE_IF(c.num() != numStacks(),
        "Wrong symmetry length of container for FoldedStackContainer::multAndAdd");
    THREAD_GROUP gr;
    SymmetrySet ss(dim, c.num());
    for (symiterator si(ss); !si.isEnd(); ++si) {
        if (c.check(*si)) {
            THREAD *worker = new WorkerFoldMAADense(*this, *si, c, out);
            gr.insert(worker);
        }
    }
    gr.run();
}

```

This code is used in section 430.

**433.** This is analogous to  $\langle \text{WorkerUnfoldMAADense}::operator()() \text{ code 449} \rangle$ .

$\langle \text{WorkerFoldMAADense}::operator()() \text{ code 433} \rangle \equiv$

```

void WorkerFoldMAADense::operator()()
{
    Permutation iden(dense_cont.num());
    IntSequence coor(sym, iden.getMap());
    const FGSTensor *g = dense_cont.get(sym);
    cont.multAndAddStacks(coor, *g, out, &out);
}

```

This code is used in section 430.

**434.**

$\langle \text{WorkerFoldMAADense}$  constructor code 434  $\rangle \equiv$

```

WorkerFoldMAADense::WorkerFoldMAADense(const FoldedStackContainer
    &container, const Symmetry &s, const FGSContainer &dcontainer, FGSTensor &outten)
    : cont(container), sym(s), dense_cont(dcontainer), out(outten) {}

```

This code is used in section 430.

**435.** This is analogous to  $\langle \text{UnfoldedStackContainer}::\text{multAndAddSparse1}$  code 451  $\rangle$ .

```

 $\langle \text{FoldedStackContainer}::\text{multAndAddSparse1}$  code 435  $\rangle \equiv$ 
    void FoldedStackContainer::multAndAddSparse1(const FSSparseTensor &t, FGSTensor &out)
        const
    {
        THREAD_GROUP gr;
        UFSensor dummy(0, numStacks(), t.dimen());
        for (Tensor::index ui = dummy.begin(); ui  $\neq$  dummy.end(); ++ui) {
            THREAD * worker = new WorkerFoldMAASparse1(*this, t, out, ui.getCoor());
            gr.insert(worker);
        }
        gr.run();
    }

```

This code is used in section 430.

**436.** This is analogous to `< WorkerUnfoldMAASparse1 :: operator>()() code 452 >`. The only difference is that instead of **UPSTensor** as a result of multiplication of unfolded tensor and tensors from containers, we have **FPSTensor** with partially folded permuted symmetry.

todo: make slice vertically narrowed according to the fill of t, vertically narrow out accordingly.

```
< WorkerFoldMAASparse1 :: operator>()() code 436 > ≡
void WorkerFoldMAASparse1 :: operator>()()
{
    const EquivalenceSet &eset = ebundle.get(out.dimen());
    const PermutationSet &pset = tls.pbundle->get(t.dimen());
    Permutation iden(t.dimen());
    UPSTensor slice(t, cont.getStackSizes(), coor, PerTensorDimens(cont.getStackSizes(), coor));
    for (int iper = 0; iper < pset.getNum(); iper++) {
        const Permutation &per = pset.get(iper);
        IntSequence percoor(coor.size());
        per.apply(coor, percoor);
        for (EquivalenceSet::const_iterator it = eset.begin(); it ≠ eset.end(); ++it) {
            if ((*it).numClasses() ≡ t.dimen()) {
                StackProduct<FGSTensor> sp(cont, *it, out.getSym());
                if (¬sp.isZero(percoor)) {
                    KronProdStack<FGSTensor> kp(sp, percoor);
                    kp.optimizeOrder();
                    const Permutation &oper = kp.getPer();
                    if (Permutation(oper, per) ≡ iden) {
                        FPSTensor fps(out.getDims(), *it, slice, kp);
                        {
                            SYNCHRO syn(&out, "WorkerUnfoldMAASparse1");
                            fps.addTo(out);
                        }
                    }
                }
            }
        }
    }
}
```

This code is used in section 430.

**437.**

```
< WorkerFoldMAASparse1 constructor code 437 > ≡
WorkerFoldMAASparse1 :: WorkerFoldMAASparse1(const FoldedStackContainer
    &container, const FSSparseTensor &ten, FGSTensor &outten, const IntSequence &c)
: cont(container), t(ten), out(outten), coor(c), ebundle(*(tls.ebundle)) {}
```

This code is used in section 430.

**438.** Here is the second implementation of sparse folded *multAndAdd*. It is pretty similar to implementation of `< UnfoldedStackContainer :: multAndAddSparse2` code 454. We make a dense folded *slice*, and then call folded *multAndAddStacks*, which multiplies all the combinations compatible with the slice.

```
< FoldedStackContainer :: multAndAddSparse2 code 438 > ≡
void FoldedStackContainer :: multAndAddSparse2 (const FSSparseTensor &t, FGSTensor &out)
    const
{
    THREAD_GROUP gr;
    FFSTensor dummy_f(0, numStacks(), t.dimen());
    for (Tensor::index fi = dummy_f.begin(); fi ≠ dummy_f.end(); ++fi) {
        THREAD * worker = new WorkerFoldMAASparse2(*this, t, out, fi.getCoor());
        gr.insert(worker);
    }
    gr.run();
}
```

This code is used in section 430.

**439.** Here we make a sparse slice first and then call *multAndAddStacks* if the slice is not empty. If the slice is really sparse, we call sparse version of *multAndAddStacks*. What means “really sparse” is given by *fill\_threshold*. It is not tuned yet, a practice shows that it must be a really low number, since sparse *multAndAddStacks* is much slower than the dense version.

Further, we take only nonzero rows of the slice, and accordingly of the out tensor. We jump over zero initial rows and drop zero tailing rows.

```
< WorkerFoldMAASparse2 :: operator()() code 439 > ≡
void WorkerFoldMAASparse2 :: operator()()
{
    GSSparseTensor slice(t, cont.getStackSizes(), coor, TensorDimens(cont.getStackSizes(), coor));
    if (slice.getNumNonZero()) {
        if (slice.getUnfoldIndexFillFactor() > FoldedStackContainer :: fill_threshold) {
            FGSTensor dense_slice(slice);
            int r1 = slice.getFirstNonZeroRow();
            int r2 = slice.getLastNonZeroRow();
            FGSTensor dense_slice1(r1, r2 - r1 + 1, dense_slice);
            FGSTensor out1(r1, r2 - r1 + 1, out);
            cont.multAndAddStacks(coor, dense_slice1, out1, &out);
        }
        else cont.multAndAddStacks(coor, slice, out, &out);
    }
}
```

This code is cited in section 455.

This code is used in section 430.

**440.**

```
< WorkerFoldMAASparse2 constructor code 440 > ≡
WorkerFoldMAASparse2 :: WorkerFoldMAASparse2(const FoldedStackContainer
    &container, const FSSparseTensor &ten, FGSTensor &outten, const IntSequence &c)
: cont(container), t(ten), out(outten), coor(c) {}
```

This code is used in section 430.

**441.** Here is the third implementation of the sparse folded *multAndAdd*. It is column-wise implementation, and thus is not a good candidate for the best performer.

We go through all columns from the output. For each column we calculate folded *sumcol* which is a sum of all appropriate columns for all suitable equivalences. So we go through all suitable equivalences, for each we construct a **StackProduct** object and construct **IrregTensor** for a corresponding column of *z*. The **IrregTensor** is an abstraction for Kronecker multiplication of stacked columns of the two containers without zeros. Then the column is added to *sumcol*. Finally, the *sumcol* is multiplied by the sparse tensor.

```

⟨ FoldedStackContainer :: multAndAddSparse3 code 441 ⟩ ≡
void FoldedStackContainer :: multAndAddSparse3 (const FSSparseTensor &t, FGSTensor &out)
    const
{
    const EquivalenceSet &eset = ebundle.get(out.dimen());
    for (Tensor :: index run = out.begin(); run ≠ out.end(); ++run) {
        Vector outcol(out, *run);
        FRSingleTensor sumcol(t.nvar(), t.dimen());
        sumcol.zeros();
        for (EquivalenceSet :: const_iterator it = eset.begin(); it ≠ eset.end(); ++it) {
            if ((*it).numClasses() ≡ t.dimen()) {
                StackProduct ⟨FGSTensor⟩ sp(*this, *it, out.getSym());
                IrregTensorHeader header(sp, run.getCoor());
                IrregTensor irten(header);
                irten.addTo(sumcol);
            }
        }
        t.multColumnAndAdd(sumcol, outcol);
    }
}

```

This code is used in section 430.

**442.** Here is the fourth implementation of sparse **FoldedStackContainer** :: *multAndAdd*. It is almost equivalent to *multAndAddSparse2* with the exception that the **FPSTensor** as a result of a product of a slice and Kronecker product of the stack derivatives is calculated in the sparse fashion. For further details, see ⟨**FoldedStackContainer** :: *multAndAddStacks* sparse code 446⟩ and ⟨**FPSTensor** sparse constructor 339⟩.

```

⟨ FoldedStackContainer :: multAndAddSparse4 code 442 ⟩ ≡
void FoldedStackContainer :: multAndAddSparse4 (const FSSparseTensor &t, FGSTensor &out)
    const
{
    THREAD_GROUP gr;
    FFSTensor dummy_f(0, numStacks(), t.dimen());
    for (Tensor :: index fi = dummy_f.begin(); fi ≠ dummy_f.end(); ++fi) {
        THREAD * worker = new WorkerFoldMAASparse4(*this, t, out, fi.getCoor());
        gr.insert(worker);
    }
    gr.run();
}

```

This code is used in section 430.

**443.** The **WorkerFoldMAASparse4** is the same as **WorkerFoldMAASparse2** with the exception that we call a sparse version of *multAndAddStacks*.

```

< WorkerFoldMAASparse4::operator()() code 443 > ≡
  void WorkerFoldMAASparse4::operator()()
  {
    GSSparseTensor slice(t, cont.getStackSizes(), coord, TensorDimens(cont.getStackSizes(), coord));
    if (slice.getNumNonZero()) cont.multAndAddStacks(coord, slice, out, &out);
  }

```

This code is used in section 430.

**444.**

```

< WorkerFoldMAASparse4 constructor code 444 > ≡
  WorkerFoldMAASparse4::WorkerFoldMAASparse4(const FoldedStackContainer
    &container, const FSSparseTensor &ten, FGSTensor &outten, const IntSequence &c)
    : cont(container), t(ten), out(outten), coord(c) {}

```

This code is used in section 430.

**445.** This is almost the same as  $\langle \text{UnfoldedStackContainer}::\text{multAndAddStacks}$  code 457. The only difference is that we do not construct a **UPSTensor** from **KronProdStack**, but we construct partially folded permuted symmetry **FPSTensor**. Note that the tensor  $g$  must be unfolded in order to be able to multiply with unfolded rows of Kronecker product. However, columns of such a product are partially folded giving a rise to the **FPSTensor**.

```

 $\langle \text{FoldedStackContainer}::\text{multAndAddStacks}$  dense code 445  $\rangle \equiv$ 
  void FoldedStackContainer::multAndAddStacks(const IntSequence &coor, const FGSTensor
    &g, FGSTensor &out, const void *ad) const
  {
    const EquivalenceSet &eset = ebundle.get(out.dimen());
    UGSTensor ug(g);
    UFSTensor dummy_u(0, numStacks(), g.dimen());
    for (Tensor::index ui = dummy_u.begin(); ui  $\neq$  dummy_u.end(); ++ui) {
      IntSequence tmp(ui.getCoor());
      tmp.sort();
      if (tmp  $\equiv$  coor) {
        Permutation sort_per(ui.getCoor());
        sort_per.inverse();
        for (EquivalenceSet::const_iterator it = eset.begin(); it  $\neq$  eset.end(); ++it) {
          if ((*it).numClasses()  $\equiv$  g.dimen()) {
            StackProduct<FGSTensor> sp(*this, *it, sort_per, out.getSym());
            if ( $\neg$ sp.isZero(coor)) {
              KronProdStack<FGSTensor> kp(sp, coor);
              if (ug.getSym().isFull()) kp.optimizeOrder();
              FPSTensor fps(out.getDims(), *it, sort_per, ug, kp);
              {
                SYNCHRO syn(ad, "multAndAddStacks");
                fps.addTo(out);
              }
            }
          }
        }
      }
    }
  }
}

```

This code is cited in section 446.

This code is used in section 430.



**446.** This is almost the same as `<FoldedStackContainer::multAndAddStacks` dense code 445. The only difference is that the Kronecker product of the stacks is multiplied with sparse slice **GSSparseTensor** (not dense slice **FGSTensor**). The multiplication is done in `<FPSTensor` sparse constructor 339.

```
<FoldedStackContainer::multAndAddStacks sparse code 446> ≡
void FoldedStackContainer::multAndAddStacks(const IntSequence &coor, const
      GSSparseTensor &g, FGSTensor &out, const void *ad) const
{
    const EquivalenceSet &eset = ebundle.get(out.dimen());
    UFSTensor dummy_u(0, numStacks(), g.dimen());
    for (Tensor::index ui = dummy_u.begin(); ui ≠ dummy_u.end(); ++ui) {
        IntSequence tmp(ui.getCoor());
        tmp.sort();
        if (tmp ≡ coor) {
            Permutation sort_per(ui.getCoor());
            sort_per.inverse();
            for (EquivalenceSet::const_iterator it = eset.begin(); it ≠ eset.end(); ++it) {
                if ((*it).numClasses() ≡ g.dimen()) {
                    StackProduct<FGSTensor> sp(*this, *it, sort_per, out.getSym());
                    if (¬sp.isZero(coor)) {
                        KronProdStack<FGSTensor> kp(sp, coor);
                        FPSTensor fps(out.getDims(), *it, sort_per, g, kp);
                        {
                            SYNCHRO syn(ad, "multAndAddStacks");
                            fps.addTo(out);
                        }
                    }
                }
            }
        }
    }
}
```

This code is cited in sections 442 and 455.

This code is used in section 430.

**447.** Here we simply call either `multAndAddSparse1` or `multAndAddSparse2`. The first one allows for optimization of Kronecker products, so it seems to be more efficient.

```
<UnfoldedStackContainer::multAndAdd sparse code 447> ≡
void UnfoldedStackContainer::multAndAdd(const FSSparseTensor &t, UGSTensor &out) const
{
    TL_RAISE_IF(t.nvar() ≠ getAllSize(),
        "Wrong number of variables of tensor for UnfoldedStackContainer::multAndAdd");
    multAndAddSparse2(t, out);
}
```

This code is used in section 430.

**448.** Here we implement the formula for stacks for fully symmetric tensor scattered in a number of general symmetry tensors contained in a given container. The implementations is pretty the same as in *multAndAddSparse2* but we do not do the slices of sparse tensor, but only a lookup to the container.

This means that we do not iterate through a dummy folded tensor to obtain folded coordinates of stacks, rather we iterate through all symmetries contained in the container and the coordinates of stacks are obtained as unfolded identity sequence via the symmetry. The reason of doing this is that we are unable to calculate symmetry from stack coordinates as easily as stack coordinates from the symmetry.

```

< UnfoldedStackContainer :: multAndAdd dense code 448 > ≡
  void UnfoldedStackContainer :: multAndAdd(int dim, const UGSContainer &c, UGSTensor
    &out) const
  {
    TL_RAISE_IF(c.num() ≠ numStacks(),
      "Wrong symmetry length of container for UnfoldedStackContainer::multAndAdd");
    THREAD_GROUP gr;
    SymmetrySet ss(dim, c.num());
    for (symiterator si(ss); ¬si.isEnd(); ++si) {
      if (c.check(*si)) {
        THREAD * worker = new WorkerUnfoldMAADense(*this, *si, c, out);
        gr.insert(worker);
      }
    }
    gr.run();
  }

```

This code is cited in section 432.

This code is used in section 430.

**449.**

```

< WorkerUnfoldMAADense :: operator()() code 449 > ≡
  void WorkerUnfoldMAADense :: operator()()
  {
    Permutation iden(dense_cont.num());
    IntSequence coor(sym, iden.getMap());
    const UGSTensor *g = dense_cont.get(sym);
    cont.multAndAddStacks(coor, *g, out, &out);
  }

```

This code is cited in section 433.

This code is used in section 430.

**450.**

```

< WorkerUnfoldMAADense constructor code 450 > ≡
  WorkerUnfoldMAADense :: WorkerUnfoldMAADense(const UnfoldedStackContainer
    &container, const Symmetry &s, const UGSContainer &dcontainer, UGSTensor
    &outten)
  : cont(container), sym(s), dense_cont(dcontainer), out(outten) {}

```

This code is used in section 430.

**451.** Here we implement the formula for unfolded tensors. If, for instance, a coordinate  $z$  of a tensor  $[f_{z^2}]$  is partitioned as  $z = [a, b]$ , then we perform the following:

$$[f_{z^2}] \left( \sum_c \begin{bmatrix} a_{c(x)} \\ b_{c(y)} \end{bmatrix} \otimes \begin{bmatrix} a_{c(y)} \\ b_{c(y)} \end{bmatrix} \right) = [f_{aa}] \left( \sum_c a_{c(x)} \otimes a_{c(y)} \right) + [f_{ab}] \left( \sum_c a_{c(x)} \otimes b_{c(y)} \right) + \\ [f_{ba}] \left( \sum_c b_{c(x)} \otimes a_{c(y)} \right) + [f_{bb}] \left( \sum_c b_{c(x)} \otimes b_{c(y)} \right)$$

This is exactly what happens here. The code is clear. It goes through all combinations of stacks, and each thread is responsible for operation for the slice corresponding to the combination of the stacks.

```

< UnfoldedStackContainer :: multAndAddSparse1 code 451 > ≡
  void UnfoldedStackContainer :: multAndAddSparse1 (const FSSparseTensor &t, UGSTensor
    &out) const
  {
    THREAD_GROUP gr;
    UFSTensor dummy(0, numStacks(), t.dimen());
    for (Tensor::index ui = dummy.begin(); ui ≠ dummy.end(); ++ui) {
      THREAD * worker = new WorkerUnfoldMAASparse1(*this, t, out, ui.getCoor());
      gr.insert(worker);
    }
    gr.run();
  }

```

This code is cited in sections 435 and 452.

This code is used in section 430.

**452.** This does a step of `<UnfoldedStackContainer::multAndAddSparse1>` code 451 for a given coordinates. First it makes the slice of the given stack coordinates. Then it multiplies everything what should be multiplied with the slice. That is it goes through all equivalences, creates **StackProduct**, then **KronProdStack**, which is added to *out*. So far everything is clear.

However, we want to use optimized **KronProdAllOptim** to minimize a number of flops and memory needed in the Kronecker product. So we go through all permutations *per*, permute the coordinates to get *percoor*, go through all equivalences, and make **KronProdStack** and optimize it. The result of optimization is a permutation *oper*. Now, we multiply the Kronecker product with the slice, only if the slice has the same ordering of coordinates as the Kronecker product **KronProdStack**. However, it is not perfectly true. Since we go through **all** permutations *per*, there might be two different permutations leading to the same ordering in **KronProdStack** and thus the same ordering in the optimized **KronProdStack**. The two cases would be counted twice, which is wrong. That is why we do not condition on  $\text{coor} \circ \text{oper} \circ \text{per} = \text{coor}$ , but we condition on  $\text{oper} \circ \text{per} = \text{id}$ . In this way, we rule out permutations *per* leading to the same ordering of stacks when applied on *coor*.

todo: vertically narrow slice and out according to the fill in *t*.

```
<WorkerUnfoldMAASparse1::operator>()() code 452> ≡
void WorkerUnfoldMAASparse1::operator>()()
{
    const EquivalenceSet &eset = ebundle.get(out.dimen());
    const PermutationSet &pset = tls.pbundle-get(t.dimen());
    Permutation iden(t.dimen());
    UPSTensor slice(t, cont.getStackSizes(), coor, PerTensorDimens(cont.getStackSizes(), coor));
    for (int iper = 0; iper < pset.getNum(); iper++) {
        const Permutation &per = pset.get(iper);
        IntSequence percoor(coor.size());
        per.apply(coor, percoor);
        for (EquivalenceSet::const_iterator it = eset.begin(); it ≠ eset.end(); ++it) {
            if ((*it).numClasses() ≡ t.dimen()) {
                StackProduct<UGSTensor> sp(cont, *it, out.getSym());
                if (¬sp.isZero(percoor)) {
                    KronProdStack<UGSTensor> kp(sp, percoor);
                    kp.optimizeOrder();
                    const Permutation &oper = kp.getPer();
                    if (Permutation(oper, per) ≡ iden) {
                        UPSTensor ups(out.getDims(), *it, slice, kp);
                        {
                            SYNCHRO syn(&out, "WorkerUnfoldMAASparse1");
                            ups.addTo(out);
                        }
                    }
                }
            }
        }
    }
}
```

This code is cited in section 436.

This code is used in section 430.

453.

```

< WorkerUnfoldMAASparse1 constructor code 453 > ≡
  WorkerUnfoldMAASparse1 :: WorkerUnfoldMAASparse1 (const UnfoldedStackContainer
    &container, const FSSparseTensor &ten, UGSTensor &outten, const IntSequence &c)
    : cont(container), t(ten), out(outten), coor(c), ebundle(*(tls.ebundle)) {}

```

This code is used in section 430.

454. In here we implement the formula by a bit different way. We use the fact, using notation of `< UnfoldedStackContainer :: multAndAddSparse2 code 454 >`, that

$$[f_{ba}] \left( \sum_c b_{c(x)} \otimes a_{c(y)} \right) = [f_{ab}] \left( \sum_c a_{c(y)} \otimes b_{c(b)} \right) \cdot P$$

where  $P$  is a suitable permutation of columns. The permutation corresponds to (in this example) a swap of  $a$  and  $b$ . An advantage of this approach is that we do not need **UPSTensor** for  $f_{ba}$ , and thus we decrease the number of needed slices.

So we go through all folded indices of stack coordinates, then for each such index  $fi$  we make a slice and call `multAndAddStacks`. This goes through all corresponding unfolded indices to perform the formula. Each unsorted (unfold) index implies a sorting permutation `sort_per` which must be used to permute stacks in **StackProduct**, and permute equivalence classes when **UPSTensor** is formed. In this way the column permutation  $P$  from the formula is factored to the permutation of **UPSTensor**.

```

< UnfoldedStackContainer :: multAndAddSparse2 code 454 > ≡
  void UnfoldedStackContainer :: multAndAddSparse2 (const FSSparseTensor &t, UGSTensor
    &out) const
  {
    THREAD_GROUP gr;
    FFSTensor dummy_f(0, numStacks(), t.dimen());
    for (Tensor::index fi = dummy_f.begin(); fi < dummy_f.end(); ++fi) {
      THREAD * worker = new WorkerUnfoldMAASparse2(*this, t, out, fi.getCoor());
      gr.insert(worker);
    }
    gr.run();
  }

```

This code is cited in sections 438, 454, and 455.

This code is used in section 430.

**455.** This does a step of  $\langle \text{UnfoldedStackContainer}::\text{multAndAddSparse2}$  code 454  $\rangle$  for a given coordinates.

todo: implement *multAndAddStacks* for sparse slice as  $\langle \text{FoldedStackContainer}::\text{multAndAddStacks}$  sparse code 446  $\rangle$  and do this method as  $\langle \text{WorkerFoldMAASparse2}::\text{operator}()()$  code 439  $\rangle$ .

```

 $\langle \text{WorkerUnfoldMAASparse2}::\text{operator}()()$  code 455  $\rangle \equiv$ 
void WorkerUnfoldMAASparse2::operator()()
{
    GSSparseTensor slice(t, cont.getStackSizes(), coor, TensorDimens(cont.getStackSizes(), coor));
    if (slice.getNumNonZero()) {
        FGSTensor fslice(slice);
        UGSTensor dense_slice(fslice);
        int r1 = slice.getFirstNonZeroRow();
        int r2 = slice.getLastNonZeroRow();
        UGSTensor dense_slice1(r1, r2 - r1 + 1, dense_slice);
        UGSTensor out1(r1, r2 - r1 + 1, out);
        cont.multAndAddStacks(coor, dense_slice1, out1, &out);
    }
}

```

This code is used in section 430.

**456.**

```

 $\langle \text{WorkerUnfoldMAASparse2}$  constructor code 456  $\rangle \equiv$ 
WorkerUnfoldMAASparse2::WorkerUnfoldMAASparse2(const UnfoldedStackContainer
    &container, const FSSparseTensor &ten, UGSTensor &outten, const IntSequence &c)
: cont(container), t(ten), out(outten), coor(c) {}

```

This code is used in section 430.

**457.** For a given unfolded coordinates of stacks  $fi$ , and appropriate tensor  $g$ , whose symmetry is a symmetry of  $fi$ , the method contributes to  $out$  all tensors in unfolded stack formula involving stacks chosen by  $fi$ .

We go through all  $ui$  coordinates which yield  $fi$  after sorting. We construct a permutation  $sort\_per$  which sorts  $ui$  to  $fi$ . We go through all appropriate equivalences, and construct **StackProduct** from equivalence classes permuted by  $sort\_per$ , then **UPSTensor** with implied permutation of columns by the permuted equivalence by  $sort\_per$ . The **UPSTensor** is then added to  $out$ .

We cannot use here the optimized **KronProdStack**, since the symmetry of **UGSTensor**  $&g$  prescribes the ordering of the stacks. However, if  $g$  is fully symmetric, we can do the optimization harmlessly.

```

< UnfoldedStackContainer::multAndAddStacks code 457 > ≡
void UnfoldedStackContainer::multAndAddStacks(const IntSequence &fi, const UGSTensor
    &g, UGSTensor &out, const void *ad) const
{
    const EquivalenceSet &eset = ebundle.get(out.dimen());
    UFSTensor dummy_u(0, numStacks(), g.dimen());
    for (Tensor::index ui = dummy_u.begin(); ui ≠ dummy_u.end(); ++ui) {
        IntSequence tmp(ui.getCoor());
        tmp.sort();
        if (tmp ≡ fi) {
            Permutation sort_per(ui.getCoor());
            sort_per.inverse();
            for (EquivalenceSet::const_iterator it = eset.begin(); it ≠ eset.end(); ++it) {
                if ((*it).numClasses() ≡ g.dimen()) {
                    StackProduct<UGSTensor> sp(*this, *it, sort_per, out.getSym());
                    if (¬sp.isZero(fi)) {
                        KronProdStack<UGSTensor> kp(sp, fi);
                        if (g.getSym().isFull()) kp.optimizeOrder();
                        UPSTensor ups(out.getDims(), *it, sort_per, g, kp);
                        {
                            SYNCHRO syn(ad, "multAndAddStacks");
                            ups.addTo(out);
                        }
                    }
                }
            }
        }
    }
}

```

This code is cited in section 445.

This code is used in section 430.

**458.** End of `stack_container.cpp` file.

**459. Refined stack of containers.** Start of `fine_container.h` file.

This file defines a refinement of the stack container. It makes a vertical refinement of a given stack container, it refines only matrix items, the items which are always zero, or can be identity matrices are not refined.

The refinement is done by a simple construction from the stack container being refined. A parameter is passed meaning a maximum size of each stack in the refined container. The resulting object is stack container, so everything works seamlessly.

We define here a class for refinement of sizes **SizeRefinement**, this is purely an auxiliary class allowing us to write a code more concisely. The main class of this file is **FineContainer**, which corresponds to refining. The two more classes **FoldedFineContainer** and **UnfoldedFineContainer** are its specializations.

NOTE: This code was implemented with a hope that it will help to cut down memory allocations during the Faa Di Bruno formula evaluation. However, it seems that this needs to be accompanied with a similar thing for tensor multidimensional index. Thus, the abstraction is not currently used, but it might be useful in future.

```
#ifndef FINE_CONTAINER_H
#define FINE_CONTAINER_H
#include "stack_container.h"
#include <vector>
    <SizeRefinement class declaration 460>;
    <FineContainer class declaration 461>;
    <FoldedFineContainer class declaration 464>;
    <UnfoldedFineContainer class declaration 465>;
#endif
```

**460.** This class splits the first  $nc$  elements of the given sequence  $s$  to a sequence not having items greater than given  $max$ . The remaining elements (those behind  $nc$ ) are left untouched. It also remembers the mapping, i.e. for a given index in a new sequence, it is able to return a corresponding index in old sequence.

```
<SizeRefinement class declaration 460> ≡
class SizeRefinement {
    vector<int> rsizes;
    vector<int> ind_map;
    int new_nc;
public:
    SizeRefinement(const IntSequence &s, int nc, int max);
    int getRefSize(int i) const
    { return rsizes[i]; }
    int numRefinements() const
    { return rsizes.size(); }
    int getOldIndex(int i) const
    { return ind_map[i]; }
    int getNC() const
    { return new_nc; }
};
```

This code is used in section 459.



**461.** This main class of this class refines a given stack container, and inherits from the stack container. It also defines the *getType* method, which returns a type for a given stack as the type of the corresponding (old) stack of the former stack container.

```

< FineContainer class declaration 461 > ≡
    template<class _Ttype>
    class FineContainer : public SizeRefinement, public StackContainer<_Ttype> {
    protected:
        typedef StackContainer<_Ttype> _Stype;
        typedef typename StackContainerInterface<_Ttype>::_Ctype _Ctype;
        typedef typename StackContainerInterface<_Ttype>::itype itype;
        _Ctype **const ref.conts;
        const _Stype &stack_cont;
    public:
        < FineContainer constructor 462 >;
        < FineContainer destructor 463 >;
        itype getType(int i, const Symmetry &s) const
        { return stack_cont.getType(getOldIndex(i), s); }
    };

```

This code is used in section 459.

**462.** Here we construct the **SizeRefinement** and allocate space for the refined containers. Then, the containers are created and put to *conts* array. Note that the containers do not claim any further space, since all the tensors of the created containers are in-place submatrices.

Here we use a dirty trick of converting **const** pointer to non-**const** pointer and passing it to a subtensor container constructor. The containers are stored in *ref\_conts* and then in *conts* from **StackContainer**. However, this is safe since neither *ref\_conts* nor *conts* are used in non-**const** contexts. For example, **StackContainer** has only a **const** method to return a member of *conts*.

```

< FineContainer constructor 462 > ≡
FineContainer(const _Stype &sc, int max)
: SizeRefinement(sc.getStackSizes(), sc.numConts(), max),
  StackContainer<_Ttype>(numRefinements(), getNC()), ref_conts(new _Ctype*[getNC()]),
  stack_cont(sc) {
  for (int i = 0; i < numRefinements(); i++) _Stype::stack_sizes[i] = getRefSize(i);
  _Stype::calculateOffsets();
  int last_cont = -1;
  int last_row = 0;
  for (int i = 0; i < getNC(); i++) {
    if (getOldIndex(i) ≠ last_cont) {
      last_cont = getOldIndex(i);
      last_row = 0;
    }
    union {
      const _Ctype *c;
      _Ctype *n;
    } convert;
    convert.c = stack_cont.getCont(last_cont);
    ref_conts[i] = new _Ctype(last_row, _Stype::stack_sizes[i], *(convert.n));
    _Stype::conts[i] = ref_conts[i];
    last_row += _Stype::stack_sizes[i];
  }
}

```

This code is used in section 461.

**463.** Here we deallocate the refined containers, and deallocate the array of refined containers.

```

< FineContainer destructor 463 > ≡
virtual ~FineContainer()
{
  for (int i = 0; i < _Stype::numConts(); i++) delete ref_conts[i];
  delete[] ref_conts;
}

```

This code is used in section 461.

**464.** Here is **FineContainer** specialization for folded tensors.

```
< FoldedFineContainer class declaration 464 > ≡
class FoldedFineContainer : public FineContainer<FGSTensor>, public FoldedStackContainer
{
public:
    FoldedFineContainer(const StackContainer<FGSTensor> &sc, int max)
        : FineContainer<FGSTensor>(sc, max) {}
};
```

This code is used in section 459.

**465.** Here is **FineContainer** specialization for unfolded tensors.

```
< UnfoldedFineContainer class declaration 465 > ≡
class UnfoldedFineContainer : public FineContainer<UGSTensor>, public
    UnfoldedStackContainer {
public:
    UnfoldedFineContainer(const StackContainer<UGSTensor> &sc, int max)
        : FineContainer<UGSTensor>(sc, max) {}
};
```

This code is used in section 459.

**466.** End of `fine_container.h` file.

**467.** Start of `stack_container.cpp` file.

```
#include "fine_container.h"
#include <cmath>
< SizeRefinement constructor code 468 >;
```

**468.** Here we construct the vector of new sizes of containers (before *nc*) and copy all remaining sizes behind *nc*.

⟨ **SizeRefinement** constructor code 468 ⟩ ≡

```

SizeRefinement::SizeRefinement(const IntSequence &s, int nc, int max)
{
    new_nc = 0;
    for (int i = 0; i < nc; i++) {
        int nr = s[i]/max;
        if (s[i] % max ≠ 0) nr++;
        int ss = (nr > 0) ? (int) round((double) s[i]/nr) : 0;
        for (int j = 0; j < nr - 1; j++) {
            rsizes.push_back(ss);
            ind_map.push_back(i);
            new_nc++;
        }
        rsizes.push_back(s[i] - (nr - 1) * ss);
        ind_map.push_back(i);
        new_nc++;
    }
    for (int i = nc; i < s.size(); i++) {
        rsizes.push_back(s[i]);
        ind_map.push_back(i);
    }
}

```

This code is used in section 467.

**469.** End of `stack_container.cpp` file.

**470. Multiplying tensor columns.** Start of `pyramid_prod.h` file.

In here, we implement the Faa Di Bruno for folded tensors. Recall, that one step of the Faa Di Bruno is a formula:

$$[B_{s^k}]_{\alpha_1 \dots \alpha_k} = [h_{y^l}]_{\gamma_1 \dots \gamma_l} \prod_{m=1}^l [g_{s^l c_m}]_{c_m(\alpha)}^{\gamma_m}$$

In contrast to unfolded implementation of `UGSContainer::multAndAdd` with help of `KronProdAll` and `UPSTensor`, we take a completely different strategy. We cannot afford full instantiation of

$$\sum_{c \in M_{l,k}} \prod_{m=1}^l [g_{s^l c_m}]_{c_m(\alpha)}^{\gamma_m}$$

and therefore we do it per partes. We select some number of columns, for instance 10, calculate 10 continuous iterators of tensor  $B$ . Then we form unfolded tensor

$$[G]_S^{\gamma_1 \dots \gamma_l} = \left[ \sum_{c \in M_{l,k}} \prod_{m=1}^l [g_{s^l c_m}]_{c_m(\alpha)}^{\gamma_m} \right]_S$$

where  $S$  is the selected set of 10 indices. This is done as Kronecker product of vectors corresponding to selected columns. Note that, in general, there is no symmetry in  $G$ , its type is special class for this purpose.

If  $g$  is folded, then we have to form folded version of  $G$ . There is no symmetry in  $G$  data, so we sum all unfolded indices corresponding to folded index together. This is perfectly OK, since we multiply these groups of (equivalent) items with the same number in fully symmetric  $g$ .

After this, we perform ordinary matrix multiplication to obtain a selected set of columns of  $B$ .

In here, we define a class for forming and representing  $[G]_S^{\gamma_1 \dots \gamma_l}$ . Basically, this tensor is row-oriented (multidimensional index is along rows), and it is fully symmetric. So we inherit from `URTensor`. If we need its folded version, we simply use a suitable conversion. The new abstraction will have only a new constructor allowing a construction from the given set of indices  $S$ , and given set of tensors  $g$ . The rest of the process is implemented in `<FGSContainer::multAndAdd` unfolded code 391 or `<FGSContainer::multAndAdd` folded code 390.

```
#ifndef PYRAMID_PROD_H
#define PYRAMID_PROD_H
#include "int_sequence.h"
#include "rfs_tensor.h"
#include "gs_tensor.h"
#include "t_container.h"
#include <vector>
using namespace std;
< USubTensor class declaration 471 >;
#endif
```

**471.** Here we define the new tensor for representing  $[G]_S^{\gamma_1 \dots \gamma_n}$ . It allows a construction from container of folded general symmetry tensors *cont*, and set of indices *ts*. Also we have to supply dimensions of resulting tensor *B*, and dimensions of tensor *h*.

```
< USubTensor class declaration 471 > ≡
class USubTensor : public URTensor {
public:
    USubTensor(const TensorDimens &bdims, const TensorDimens &hdims, const FGSContainer
        &cont, const vector<IntSequence> &lst);
    void addKronColumn(int i, const vector<const FGSTensor*> &ts, const IntSequence &pindex);
};
```

This code is cited in section 1.

This code is used in section 470.

**472.** End of pyramid\_prod.h file.

**473.** Start of pyramid\_prod.cpp file.

```
#include "pyramid_prod.h"
#include "permutation.h"
#include "tl_exception.h"
< USubTensor constructor code 474 >;
< USubTensor::addKronColumn code 475 >;
```

**474.** Here we construct the **USubTensor** object. We allocate space via the parent **URTensor**. Number of columns is a length of the list of indices *lst*, number of variables and dimensions are of the tensor *h*, this is given by *hdims*.

We go through all equivalences with number of classes equal to dimension of *B*. For each equivalence we make a permutation *per*. Then we fetch all the necessary tensors *g* with symmetries implied by symmetry of *B* and the equivalence. Then we go through the list of indices, permute them by the permutation and add the Kronecker product of the selected columns. This is done by *addKronColumn*.

```
< USubTensor constructor code 474 > ≡
USubTensor::USubTensor(const TensorDimens &bdims, const TensorDimens &hdims, const
    FGSContainer &cont, const vector<IntSequence> &lst)
: URTensor(lst.size(), hdims.getNVX()[0], hdims.dimen()) {
    TL_RAISE_IF(!hdims.getNVX().isConstant(),
        "Tensor_has_not_full_symmetry_in_USubTensor()");
    const EquivalenceSet &eset = cont.getEqBundle().get(bdims.dimen());
    zeros();
    for (EquivalenceSet::const_iterator it = eset.begin(); it != eset.end(); ++it) {
        if ((*it).numClasses() == hdims.dimen()) {
            Permutation per(*it);
            vector<const FGSTensor*> ts = cont.fetchTensors(bdims.getSym(), *it);
            for (int i = 0; i < (int) lst.size(); i++) {
                IntSequence perindex(lst[i].size());
                per.apply(lst[i], perindex);
                addKronColumn(i, ts, perindex);
            }
        }
    }
}
```

This code is used in section 473.

**475.** This makes a Kronecker product of appropriate columns from tensors in *fs* and adds such data to *i*-th column of this matrix. The appropriate columns are defined by *pindex* sequence. A column of a tensor has index created from a corresponding part of *pindex*. The sizes of these parts are given by dimensions of the tensors in *ts*.

Here we break the given index *pindex* according to the dimensions of the tensors in *ts*, and for each subsequence of the *pindex* we find an index of the folded tensor, which involves calling *getOffset* for folded tensor, which might be costly. We gather all columns to a vector *tmpcols* which are Kronecker multiplied in constructor of **URSingleTensor**. Finally we add data of **URSingleTensor** to the *i*-th column.

```
< USubTensor::addKronColumn code 475 > ≡
void USubTensor::addKronColumn(int i, const vector<const FGSTensor*> &ts, const
    IntSequence &pindex)
{
    vector<ConstVector> tmpcols;
    int lastdim = 0;
    for (unsigned int j = 0; j < ts.size(); j++) {
        IntSequence ind(pindex, lastdim, lastdim + ts[j]-dimen());
        lastdim += ts[j]-dimen();
        index in(ts[j], ind);
        tmpcols.push_back(ConstVector(*(ts[j]), *in));
    }
    URSingleTensor kronmult(tmpcols);
    Vector coli(*this, i);
    coli.add(1.0, kronmult.getData());
}
```

This code is used in section 473.

**476.** End of `pyramid_prod.cpp` file.

**477. Multiplying stacked tensor columns.** Start of `pyramid_prod2.h` file.

We need to calculate the following tensor product:

$$[f_{s^j}]_{\alpha_1 \dots \alpha_j} = \sum_{l=1}^j [f_{z^l}]_{\beta_1 \dots \beta_l} \sum_{c \in M_{l,j}} \prod_{m=1}^l [z_{c_m}]_{c_m(\alpha)}^{\beta_m}$$

where  $s = [y, u, u', \sigma]$ , and  $z$  is a composition of four variables, say  $[v, w, y, u]$ . Note that  $z$  ends with  $y$  and  $u$ , and the only non-zero derivative of the trailing part of  $z$  involving  $y$  or  $u$  is the first derivative and is the unit matrix  $y_y = [1]$  or  $u_u = [1]$ . Also, we suppose that the dependence of  $v$ , and  $w$  on  $s$  is such that whenever derivative of  $w$  is nonzero, then also of  $v$ . This means that there for any derivative and any index there is a continuous part of derivatives of  $v$  and optionally of  $w$  followed by column of zeros containing at most one 1.

This structure can be modelled and exploited with some costs at programming. For example, let us consider the following product:

$$[B_{y^2 u^3}]_{\alpha_1 \alpha_2 \beta_1 \beta_2 \beta_3} = \dots [f_{z^3}]_{\gamma_1 \gamma_2 \gamma_3} [z_{yu}]_{\alpha_1 \beta_1}^{\gamma_1} [z_y]_{\alpha_2}^{\gamma_2} [z_{uu}]_{\beta_2 \beta_3}^{\gamma_3} \dots$$

The term corresponds to equivalence  $\{\{0, 2\}, \{1\}, \{3, 4\}\}$ . For the fixed index  $\alpha_1 \alpha_2 \beta_1 \beta_2 \beta_3$  we have to make a Kronecker product of the columns

$$[z_{yu}]_{\alpha_1 \beta_1} \otimes [z_y]_{\alpha_2} \otimes [z_{uu}]_{\beta_2 \beta_3}$$

which can be written as

$$\begin{bmatrix} [v_{yu}]_{\alpha_1 \beta_1} \\ [w_{yu}]_{\alpha_1 \beta_1} \\ 0 \\ 0 \end{bmatrix} \otimes \begin{bmatrix} [v_y]_{\alpha_2} \\ [w_y]_{\alpha_2} \\ 1_{\alpha_2} \\ 0 \end{bmatrix} \otimes \begin{bmatrix} [v_{uu}]_{\beta_2 \beta_3} \\ [w_{uu}]_{\beta_2 \beta_3} \\ 0 \\ 0 \end{bmatrix}$$

where  $1_{\alpha_2}$  is a column of zeros having the only 1 at  $\alpha_2$  index.

This file develops the abstraction for this Kronecker product column without multiplication of the zeros at the top. Basically, it will be a column which is a Kronecker product of the columns without the zeros:

$$\begin{bmatrix} [v_{yu}]_{\alpha_1 \beta_1} \\ [w_{yu}]_{\alpha_1 \beta_1} \end{bmatrix} \otimes \begin{bmatrix} [v_y]_{\alpha_2} \\ [w_y]_{\alpha_2} \\ 1 \end{bmatrix} \otimes \begin{bmatrix} [v_{uu}]_{\beta_2 \beta_3} \\ [w_{uu}]_{\beta_2 \beta_3} \end{bmatrix}$$

The class will have a tensor infrastructure introducing **index** which iterates over all items in the column with  $\gamma_1 \gamma_2 \gamma_3$  as coordinates in  $[f_{z^3}]$ . The data of such a tensor is not suitable for any matrix operation and will have to be accessed only through the **index**. Note that this does not matter, since  $[f_{z^l}]$  are sparse.

```
#ifndef PYRAMID_PROD2_H
#define PYRAMID_PROD2_H
#include "permutation.h"
#include "tensor.h"
#include "tl_exception.h"
#include "rfs_tensor.h"
#include "stack_container.h"
#include "Vector.h"
<IrregTensorHeader class declaration 478>;
<IrregTensor class declaration 479>;
#endif
```



**478.** First we declare a helper class for the tensor. Its purpose is to gather the columns which are going to be Kronecker multiplied. The input of this helper class is **StackProduct**⟨**FGSTensor**⟩ and coordinate  $c$  of the column.

It maintains *unit\_flag* array which says for what columns we must stack 1 below  $v$  and  $w$ . In this case, the value of *unit\_flag* is an index of the 1, otherwise the value of *unit\_flag* is -1.

Also we have storage for the stacked columns *cols*. The object is responsible for memory management associated to this storage. That is why we do not allow any copy constructor, since we need to be sure that no accidental copies take place. We declare the copy constructor as private and not implement it.

⟨**IrregTensorHeader** class declaration 478⟩ ≡

```
class IrregTensor;
class IrregTensorHeader {
    friend class IrregTensor;
    int nv;
    IntSequence unit_flag;
    Vector **const cols;
    IntSequence end_seq;
public:
    IrregTensorHeader(const StackProduct⟨FGSTensor⟩ &sp, const IntSequence &c);
    ~IrregTensorHeader();
    int dimen() const
    { return unit_flag.size(); }
    void increment(IntSequence &v) const;
    int calcMaxOffset() const;
private:
    IrregTensorHeader(const IrregTensorHeader &);
};
```

This code is used in section 477.

**479.** Here we declare the irregular tensor. There is no special logic here. We inherit from **Tensor** and we must implement three methods, *increment*, *decrement* and *getOffset*. The last two are not implemented now, since they are not needed, and they raise an exception. The first just calls *increment* of the header. Also we declare a method *addTo* which adds this unfolded irregular single column tensor to folded (regular) single column tensor.

The header **IrregTensorHeader** lives with an object by a reference. This is dangerous. However, we will use this class only in a simple loop and both **IrregTensor** and **IrregTensorHeader** will be destructed at the end of a block. Since the super class **Tensor** must be initialized before any member, we could do either a save copy of **IrregTensorHeader**, or relatively dangerous the reference member. For the reason above we chose the latter.

(**IrregTensor** class declaration 479)  $\equiv$

```
class IrregTensor : public Tensor {
    const IrregTensorHeader &header;

public:
    IrregTensor(const IrregTensorHeader &h);
    void addTo(FRSingleTensor &out) const;
    void increment(IntSequence &v) const
    { header.increment(v); }
    void decrement(IntSequence &v) const
    { TL_RAISE("Not implemented error in IrregTensor::decrement"); }
    int getOffset(const IntSequence &v) const
    { TL_RAISE("Not implemented error in IrregTensor::getOffset"); return 0; }
};
```

This code is cited in section 1.

This code is used in section 477.

**480.** End of pyramid\_prod2.h file.

**481.** Start of pyramid\_prod2.cpp file.

```
#include "pyramid_prod2.h"
#include "rfs_tensor.h"
< IrregTensorHeader constructor code 482 >;
< IrregTensorHeader::increment code 483 >;
< IrregTensorHeader destructor code 485 >;
< IrregTensorHeader::calcMaxOffset code 486 >;
< IrregTensor constructor code 487 >;
< IrregTensor::addTo code 488 >;
```

**482.** Here we only call `sp.createPackedColumns(c, cols, unit_flag)` which fills `cols` and `unit_flag` for the given column `c`. Then we set `end_seq` according to `unit_flag` and columns lengths.

⟨**IrregTensorHeader** constructor code 482⟩ ≡

```
IrregTensorHeader::IrregTensorHeader(const StackProduct<FGSTensor> &sp, const
    IntSequence &c)
: nv(sp.getAllSize()), unit_flag(sp.dimen()), cols(new Vector*[sp.dimen()]), end_seq(sp.dimen()) {
    sp.createPackedColumns(c, cols, unit_flag);
    for (int i = 0; i < sp.dimen(); i++) {
        end_seq[i] = cols[i]-length();
        if (unit_flag[i] ≠ -1) end_seq[i] = unit_flag[i] + 1;
    }
}
```

This code is used in section 481.

**483.** Here we have to increment the given integer sequence. We do it by the following code, whose pattern is valid for all tensor. The only difference is how we increment item of coordinates.

⟨**IrregTensorHeader**::*increment* code 483⟩ ≡

```
void IrregTensorHeader::increment(IntSequence &v) const
{
    TL_RAISE_IF(v.size() ≠ dimen(),
        "Wrong size of coordinates in IrregTensorHeader::increment");
    if (v.size() ≡ 0) return;
    int i = v.size() - 1;
    ⟨increment i-th item in coordinate v 484⟩;
    while (i > 0 ∧ v[i] ≡ end_seq[i]) {
        v[i] = 0;
        i--;
        ⟨increment i-th item in coordinate v 484⟩;
    }
}
```

This code is used in section 481.

**484.** Here we increment item of coordinates. Whenever we reached end of column coming from matrices, and `unit_flag` is not `-1`, we have to jump to that `unit_flag`.

⟨increment *i*-th item in coordinate *v* 484⟩ ≡

```
v[i]++;
if (unit_flag[i] ≠ -1 ∧ v[i] ≡ cols[i]-length() - 1) v[i] = unit_flag[i];
```

This code is used in section 483.

**485.**

⟨**IrregTensorHeader** destructor code 485⟩ ≡

```
IrregTensorHeader::~IrregTensorHeader()
{
    for (int i = 0; i < dimen(); i++) delete cols[i];
    delete[] cols;
}
```

This code is used in section 481.

486. It is a product of all column lengths.

```
< IrregTensorHeader::calcMaxOffset code 486 > ≡
int IrregTensorHeader::calcMaxOffset() const
{
    int res = 1;
    for (int i = 0; i < dimen(); i++) res *= cols[i]-length();
    return res;
}
```

This code is used in section 481.

487. Everything is done in **IrregTensorHeader**, only we have to Kronecker multiply all columns of the header.

```
< IrregTensor constructor code 487 > ≡
IrregTensor::IrregTensor(const IrregTensorHeader &h)
: Tensor(along_row, IntSequence(h.dimen(), 0), h.end_seq, h.calcMaxOffset(), 1, h.dimen()), header(h)
{
    if (header.dimen() ≡ 1) {
        getData() = *(header.cols[0]);
        return;
    }
    Vector *last = new Vector(*(header.cols[header.dimen() - 1]));
    for (int i = header.dimen() - 2; i > 0; i--) {
        Vector *newlast = new Vector(last-length() * header.cols[i]-length());
        KronProd::kronMult(ConstVector(*(header.cols[i])), ConstVector(*last), *newlast);
        delete last;
        last = newlast;
    }
    KronProd::kronMult(ConstVector(*(header.cols[0])), ConstVector(*last), getData());
    delete last;
}
```

This code is used in section 481.

488. Clear.

```
< IrregTensor::addTo code 488 > ≡
void IrregTensor::addTo(FRSingleTensor &out) const
{
    for (index it = begin(); it ≠ end(); ++it) {
        IntSequence tmp(it.getCoor());
        tmp.sort();
        Tensor::index ind(&out, tmp);
        out.get(*ind, 0) += get(*it, 0);
    }
}
```

This code is used in section 481.

489. End of `pyramid_prod2.cpp` file.

**490. Miscellany.****491. Tensor polynomial evaluation.** Start of `t_polynomial.h` file.

We need to evaluate a tensor polynomial of the form:

$$[g_x]_{\alpha_1} [x]^{\alpha_1} + [g_{x^2}]_{\alpha_1 \alpha_2} [x]^{\alpha_1} [x]^{\alpha_2} + \dots + [g_{x^n}]_{\alpha_1 \dots \alpha_n} \prod_{i=1}^n [x]^{\alpha_i}$$

where  $x$  is a column vector.

We have basically two options. The first is to use the formula above, the second is to use a Horner-like formula:

$$\left[ \dots \left[ [g_{x^{n-1}}] + [g_{x^n}]_{\alpha_1 \dots \alpha_{n-1} \alpha_n} [x]^{\alpha_n} \right]_{\alpha_1 \dots \alpha_{n-2} \alpha_{n-1}} [x]^{\alpha_{n-1}} \right]_{\alpha_1} [x]^{\alpha_1}$$

Alternatively, we can put the the polynomial into a more compact form

$$[g_x]_{\alpha_1} [x]^{\alpha_1} + [g_{x^2}]_{\alpha_1 \alpha_2} [x]^{\alpha_1} [x]^{\alpha_2} + \dots + [g_{x^n}]_{\alpha_1 \dots \alpha_n} \prod_{i=1}^n [x]^{\alpha_i} = [G]_{\alpha_1 \dots \alpha_n} \prod_{i=1}^n \begin{bmatrix} 1 \\ x \end{bmatrix}^{\alpha_i}$$

Then the polynomial evaluation becomes just a matrix multiplication of the vector power.

Here we define the tensor polynomial as a container of full symmetry tensors and add an evaluation methods. We have two sorts of containers, folded and unfolded. For each type we declare two methods implementing the above formulas. We define classes for the compactification of the polynomial. The class derives from the tensor and has a eval method.

```
#include "t_container.h"
#include "fs_tensor.h"
#include "rfs_tensor.h"
#include "tl_static.h"
< PowerProvider class declaration 492 >;
< TensorPolynomial class declaration 493 >;
< UTensorPolynomial class declaration 503 >;
< FTensorPolynomial class declaration 504 >;
< CompactPolynomial class declaration 505 >;
< UCompactPolynomial class declaration 508 >;
< FCompactPolynomial class declaration 509 >;
```

**492.** Just to make the code nicer, we implement a Kronecker power of a vector encapsulated in the following class. It has *getNext* method which returns either folded or unfolded row-oriented single column Kronecker power of the vector according to the type of a dummy argument. This allows us to use the type dependent code in templates below.

The implementation of the Kronecker power is that we maintain the last unfolded power. If unfolded *getNext* is called, we Kronecker multiply the last power with a vector and return it. If folded *getNext* is called, we do the same plus we fold it.

*getNext* returns the vector for the first call (first power), the second power is returned on the second call, and so on.

⟨ **PowerProvider** class declaration 492 ⟩  $\equiv$

```
class PowerProvider {
    Vector origv;
    URSingleTensor *ut;
    FRSingleTensor *ft;
    int nv;
public:
    PowerProvider(const ConstVector &v)
    : origv(v), ut( $\Lambda$ ), ft( $\Lambda$ ), nv(v.length()) {}
    ~PowerProvider();
    const URSingleTensor &getNext(const URSingleTensor *dummy);
    const FRSingleTensor &getNext(const FRSingleTensor *dummy);
};
```

This code is used in section 491.

**493.** The tensor polynomial is basically a tensor container which is more strict on insertions. It maintains number of rows and number of variables and allows insertions only of those tensors, which yield these properties. The maximum dimension is maintained by *insert* method.

So we re-implement *insert* method and implement *evalTrad* (traditional polynomial evaluation) and horner-like evaluation *evalHorner*.

In addition, we implement derivatives of the polynomial and its evaluation. The evaluation of a derivative is different from the evaluation of the whole polynomial, simply because the evaluation of the derivatives is a tensor, and the evaluation of the polynomial is a vector (zero dimensional tensor). See documentation to `<TensorPolynomial::derivative code 501>` and `<TensorPolynomial::evalPartially code 502>` for details.

`<TensorPolynomial class declaration 493> ≡`

```
template<class _Ttype, class _TGStype, class _Stype>
class TensorPolynomial : public TensorContainer<_Ttype> {
    int nr;
    int nv;
    int maxdim;
    typedef TensorContainer<_Ttype> _Tparent;
    typedef typename _Tparent::_ptr _ptr;
public:
    TensorPolynomial(int rows, int vars)
    : TensorContainer<_Ttype>(1, nr(rows), nv(vars), maxdim(0)) {}

    TensorPolynomial(const TensorPolynomial<_Ttype, _TGStype, _Stype> &tp, int k)
    : TensorContainer<_Ttype>(tp), nr(tp.nr), nv(tp.nv), maxdim(0) { derivative(k); }

    TensorPolynomial(int first_row, int num, TensorPolynomial<_Ttype, _TGStype, _Stype> &tp)
    : TensorContainer<_Ttype>(first_row, num, tp), nr(num), nv(tp.nv), maxdim(tp.maxdim) {}

    <TensorPolynomial contract constructor code 494>;

    TensorPolynomial(const TensorPolynomial &tp)
    : TensorContainer<_Ttype>(tp), nr(tp.nr), nv(tp.nv), maxdim(tp.maxdim) {}

    int nrows() const
    { return nr; }

    int nvars() const
    { return nv; }

    <TensorPolynomial::evalTrad code 498>;
    <TensorPolynomial::evalHorner code 499>;
    <TensorPolynomial::insert code 500>;
    <TensorPolynomial::derivative code 501>;
    <TensorPolynomial::evalPartially code 502>;
};
```

This code is used in section 491.

**494.** This constructor takes a tensor polynomial

$$P(x, y) = \sum_{k=0}^m [g_{(xy)^k}]_{\alpha_1 \dots \alpha_k} \begin{bmatrix} x \\ y \end{bmatrix}^{\alpha_1 \dots \alpha_k}$$

and for a given  $x$  it makes a polynomial

$$Q(y) = P(x, y).$$

The algorithm for each full symmetry  $(xy)^k$  works with subtensors (slices) of symmetry  $x^i y^j$  (with  $i + j = k$ ), and contracts these subtensors with respect to  $x^i$  to obtain a tensor of full symmetry  $y^j$ . Since the column  $x^i$  is calculated by **PowerProvider** we cycle for  $i = 1, \dots, m$ . Then we have to add everything for  $i = 0$ .

The code works as follows: For slicing purposes we need stack sizes  $ss$  corresponding to lengths of  $x$  and  $y$ , and then identity  $pp$  for unfolding a symmetry of the slice to obtain stack coordinates of the slice. Then we do the calculations for  $i = 1, \dots, m$  and then for  $i = 0$ .

⟨ **TensorPolynomial** contract constructor code [494](#) ⟩ ≡

```
TensorPolynomial(const TensorPolynomial<_Ttype, _TGStype, _Stype> &tp, const Vector
    &xval)
: TensorContainer<_Ttype>(1), nr(tp.nrows()), nv(tp.nvars() - xval.length()), maxdim(0) {
    TL_RAISE_IF(nvars() < 0,
        "Length_of_xval_too_big_in_TensorPolynomial_contract_constructor");
    IntSequence ss(2); ss[0] = xval.length(); ss[1] = nvars();
    IntSequence pp(2); pp[0] = 0; pp[1] = 1;
    ⟨do contraction for all  $i > 0$  495⟩;
    ⟨do contraction for  $i = 0$  496⟩;
}
```

This code is used in section [493](#).



**495.** Here we setup the **PowerProvider**, and cycle through  $i = 1, \dots, m$ . Within the loop we cycle through  $j = 0, \dots, m - i$ . If there is a tensor with symmetry  $(xy)^{i+j}$  in the original polynomial, we make its slice with symmetry  $x^i y^j$ , and *contractAndAdd* it to the tensor *ten* in the **this** polynomial with a symmetry  $y^j$ .

Note three things: First, the tensor *ten* is either created and put to **this** container or just got from the container, this is done in  $\langle \text{initialize } \textit{ten} \text{ of dimension } j \text{ 497} \rangle$ . Second, the contribution to the *ten* tensor must be multiplied by  $\binom{i+j}{j}$ , since there are exactly that number of slices of  $(xy)^{i+j}$  of the symmetry  $x^i y^j$  and all must be added. Third, the tensor *ten* is fully symmetric and **\_TGStyle::contractAndAdd** works with general symmetry, that is why we have to in-place convert fully symmetric *ten* to a general symmetry tensor.

```

<do contraction for all  $i > 0$  495>  $\equiv$ 
PowerProvider pwp(xval);
for (int i = 1; i  $\leq$  tp.maxdim; i++) {
    const _Stype &xpow = pwp.getNext((const _Stype *)  $\Lambda$ );
    for (int j = 0; j  $\leq$  tp.maxdim - i; j++) {
        if (tp.check(Symmetry(i + j))) {
            <initialize ten of dimension  $j$  497>;
            Symmetry sym(i, j);
            IntSequence coor(sym, pp);
            _TGStyle slice(*(tp.get(Symmetry(i + j))), ss, coor, TensorDimens(sym, ss));
            slice.mult(Tensor::noverk(i + j, j));
            _TGStyle tmp(*ten);
            slice.contractAndAdd(0, tmp, xpow);
        }
    }
}

```

This code is cited in section 496.

This code is used in section 494.

**496.** This is easy. The code is equivalent to code  $\langle \text{do contraction for all } i > 0 \text{ 495} \rangle$  as for  $i = 0$ . The contraction here takes a form of a simple addition.

```

<do contraction for  $i = 0$  496>  $\equiv$ 
for (int j = 0; j  $\leq$  tp.maxdim; j++) {
    if (tp.check(Symmetry(j))) {
        <initialize ten of dimension  $j$  497>;
        Symmetry sym(0, j);
        IntSequence coor(sym, pp);
        _TGStyle slice(*(tp.get(Symmetry(j))), ss, coor, TensorDimens(sym, ss));
        ten-add(1.0, slice);
    }
}

```

This code is used in section 494.

**497.** The pointer *ten* is either a new tensor or got from **this** container.

```

⟨ initialize ten of dimension j 497 ⟩ ≡
    _Ttype *ten;
    if (_Tparent::check(Symmetry(j))) {
        ten = _Tparent::get(Symmetry(j));
    }
    else {
        ten = new _Ttype(nrows(), nvars(), j);
        ten→zeros();
        insert(ten);
    }

```

This code is cited in section 495.

This code is used in sections 495 and 496.

**498.** Here we cycle up to the maximum dimension, and if a tensor exists in the container, then we multiply it with the Kronecker power of the vector supplied by **PowerProvider**.

```

⟨ TensorPolynomial::evalTrad code 498 ⟩ ≡
    void evalTrad(Vector &out, const ConstVector &v) const
    {
        if (_Tparent::check(Symmetry(0))) out = _Tparent::get(Symmetry(0))→getData();
        else out.zeros();
        PowerProvider pp(v);
        for (int d = 1; d ≤ maxdim; d++) {
            const _Stype &p = pp.getNext((const _Stype *) Λ);
            Symmetry cs(d);
            if (_Tparent::check(cs)) {
                const _Ttype *t = _Tparent::get(cs);
                t→multVec(out, p.getData());
            }
        }
    }

```

This code is used in section 493.

**499.** Here we construct by contraction  $maxdim - 1$  tensor first, and then cycle. The code is clear, the only messy thing is **new** and **delete**.

```

⟨ TensorPolynomial::evalHorner code 499 ⟩ ≡
  void evalHorner(Vector &out, const ConstVector &v) const
  {
    if (_Tparent::check(Symmetry(0))) out = _Tparent::get(Symmetry(0))→getData();
    else out.zeros();
    if (maxdim ≡ 0) return;
    _Ttype *last;
    if (maxdim ≡ 1) last = new _Ttype(*(_Tparent::get(Symmetry(1))));
    else last = new _Ttype(*(_Tparent::get(Symmetry(maxdim))), v);
    for (int d = maxdim - 1; d ≥ 1; d--) {
      Symmetry cs(d);
      if (_Tparent::check(cs)) {
        const _Ttype *nt = _Tparent::get(cs);
        last→add(1.0, ConstTwoDMatrix(*nt));
      }
      if (d > 1) {
        _Ttype *new_last = new _Ttype(*last, v);
        delete last;
        last = new_last;
      }
    }
    last→multVec(out, v);
    delete last;
  }

```

This code is used in section 493.

**500.** Before a tensor is inserted, we check for the number of rows, and number of variables. Then we insert and update the  $maxdim$ .

```

⟨ TensorPolynomial::insert code 500 ⟩ ≡
  void insert(_ptr t)
  {
    TL_RAISE_IF(t→nrows() ≠ nr, "Wrong_number_of_rows_in_TensorPolynomial::insert");
    TL_RAISE_IF(t→nvar() ≠ nv, "Wrong_number_of_variables_in_TensorPolynomial::insert");
    TensorContainer⟨_Ttype⟩::insert(t);
    if (maxdim < t→dimen()) maxdim = t→dimen();
  }

```

This code is used in section 493.

**501.** The polynomial takes the form

$$\sum_{i=0}^n \frac{1}{i!} [g_{y^i}]_{\alpha_1 \dots \alpha_i} [y]^{\alpha_1} \dots [y]^{\alpha_i},$$

where  $[g_{y^i}]$  are  $i$ -order derivatives of the polynomial. We assume that  $\frac{1}{i!} [g_{y^i}]$  are items in the tensor container. This method differentiates the polynomial by one order to yield:

$$\sum_{i=1}^n \frac{1}{i!} [i \cdot g_{y^i}]_{\alpha_1 \dots \alpha_i} [y]^{\alpha_1} \dots [y]^{\alpha_i-1},$$

where  $[i \cdot \frac{1}{i!} \cdot g_{y^i}]$  are put to the container.

A polynomial can be derivative of some order, and the order cannot be recognized from the object. That is why we need to input the order.

```

< TensorPolynomial::derivative code 501 > ≡
void derivative(int k)
{
    for (int d = 1; d ≤ maxdim; d++) {
        if (_Tparent::check(Symmetry(d))) {
            _Ttype *ten = _Tparent::get(Symmetry(d));
            ten→mult((double) max((d - k), 0));
        }
    }
}

```

This code is cited in section 493.

This code is used in section 493.

**502.** Now let us suppose that we have an  $s$  order derivative of a polynomial whose  $i$  order derivatives are  $[g_{y^i}]$ , so we have

$$\sum_{i=s}^n \frac{1}{i!} [g_{y^i}]_{\alpha_1 \dots \alpha_i} \prod_{k=1}^{i-s} [y]^{\alpha_k},$$

where  $\frac{1}{i!} [g_{y^i}]$  are tensors in the container.

This methods performs this evaluation. The result is an  $s$  dimensional tensor. Note that when combined with the method *derivative*, they evaluate a derivative of some order. For example a sequence of calls  $g.derivative(0)$ ,  $g.derivative(1)$  and  $der = g.evalPartially(2, v)$  calculates  $2!$  multiple of the second derivative of  $g$  at  $v$ .

```

⟨TensorPolynomial::evalPartially code 502⟩ ≡
_Ttype *evalPartially(int s, const ConstVector &v)
{
    TL_RAISE_IF(v.length() ≠ nvars(),
        "Wrong length of vector for TensorPolynomial::evalPartially");
    _Ttype *res = new _Ttype(nrows(), nvars(), s);
    res→zeros();
    if (_Tparent::check(Symmetry(s))) res→add(1.0, *(_Tparent::get(Symmetry(s))));
    for (int d = s + 1; d ≤ maxdim; d++) {
        if (_Tparent::check(Symmetry(d))) {
            const _Ttype &lttmp = *(_Tparent::get(Symmetry(d)));
            _Ttype *last = new _Ttype(ltmp);
            for (int j = 0; j < d - s; j++) {
                _Ttype *newlast = new _Ttype(*last, v);
                delete last;
                last = newlast;
            }
            res→add(1.0, *last);
            delete last;
        }
    }
    return res;
}

```

This code is cited in section 493.

This code is used in section 493.

**503.** This just gives a name to unfolded tensor polynomial.

```

< UTensorPolynomial class declaration 503 > ≡
class FTensorPolynomial;
class UTensorPolynomial : public TensorPolynomial<UFSTensor, UGSTensor,
    URSingleTensor> {
public:
    UTensorPolynomial(int rows, int vars)
    : TensorPolynomial<UFSTensor, UGSTensor, URSingleTensor>(rows, vars) {}
    UTensorPolynomial(const UTensorPolynomial &up, int k)
    : TensorPolynomial<UFSTensor, UGSTensor, URSingleTensor>(up, k) {}
    UTensorPolynomial(const FTensorPolynomial &fp);
    UTensorPolynomial(const UTensorPolynomial &tp, const Vector &xval)
    : TensorPolynomial<UFSTensor, UGSTensor, URSingleTensor>(tp, xval) {}
    UTensorPolynomial(int first_row, int num, UTensorPolynomial &tp)
    : TensorPolynomial<UFSTensor, UGSTensor, URSingleTensor>(first_row, num, tp) {}
};

```

This code is cited in section 1.

This code is used in section 491.

**504.** This just gives a name to folded tensor polynomial.

```

< FTensorPolynomial class declaration 504 > ≡
class FTensorPolynomial : public TensorPolynomial<FFSTensor, FGSTensor, FRSingleTensor>
{
public:
    FTensorPolynomial(int rows, int vars)
    : TensorPolynomial<FFSTensor, FGSTensor, FRSingleTensor>(rows, vars) {}
    FTensorPolynomial(const FTensorPolynomial &fp, int k)
    : TensorPolynomial<FFSTensor, FGSTensor, FRSingleTensor>(fp, k) {}
    FTensorPolynomial(const UTensorPolynomial &up);
    FTensorPolynomial(const FTensorPolynomial &tp, const Vector &xval)
    : TensorPolynomial<FFSTensor, FGSTensor, FRSingleTensor>(tp, xval) {}
    FTensorPolynomial(int first_row, int num, FTensorPolynomial &tp)
    : TensorPolynomial<FFSTensor, FGSTensor, FRSingleTensor>(first_row, num, tp) {}
};

```

This code is cited in section 1.

This code is used in section 491.

**505.** The compact form of **TensorPolynomial** is in fact a full symmetry tensor, with the number of variables equal to the number of variables of the polynomial plus 1 for 1.

```

< CompactPolynomial class declaration 505 > ≡
template<class _Ttype, class _TGStype, class _Stype>
class CompactPolynomial : public _Ttype {
public:
    < CompactPolynomial constructor code 506 >;
    < CompactPolynomial::eval method code 507 >;
};

```

This code is used in section 491.

**506.** This constructor copies matrices from the given tensor polynomial to the appropriate location in this matrix. It creates a dummy tensor *dum* with two variables (one corresponds to 1, the other to *x*). The index goes through this dummy tensor and the number of columns of the folded/unfolded general symmetry tensor corresponding to the selections of 1 or *x* given by the index. Length of 1 is one, and length of *x* is *pol.nvars()*. This nvs information is stored in *dumnvs*. The symmetry of this general symmetry dummy tensor *dumgs* is given by a number of ones and x's in the index. We then copy the matrix, if it exists in the polynomial and increase *offset* for the following cycle.

⟨ **CompactPolynomial** constructor code 506 ⟩ ≡

```

CompactPolynomial(const TensorPolynomial<_Ttype, _TGStype, _Stype> &pol)
: _Ttype(pol.nrows(), pol.nvars() + 1, pol.getMaxDim()) {
    _Ttype::zeros();
    IntSequence dumnvs(2);
    dumnvs[0] = 1;
    dumnvs[1] = pol.nvars();
    int offset = 0;
    _Ttype dum(0, 2, _Ttype::dimen());
    for (Tensor::index i = dum.begin(); i ≠ dum.end(); ++i) {
        int d = i.getCoor().sum();
        Symmetry symrun(_Ttype::dimen() - d, d);
        _TGStype dumgs(0, TensorDimens(symrun, dumnvs));
        if (pol.check(Symmetry(d))) {
            TwoDMatrix sub(*this, offset, dumgs.ncols());
            sub.add(1.0, *(pol.get(Symmetry(d))));
        }
        offset += dumgs.ncols();
    }
}

```

This code is used in section 505.

**507.** We create  $x1$  to be a concatenation of 1 and  $x$ , and then create **PowerProvider** to make a corresponding power  $xpow$  of  $x1$ , and finally multiply this matrix with the power.

```
< CompactPolynomial::eval method code 507 > ≡
void eval(Vector &out, const ConstVector &v) const
{
    TL_RAISE_IF(v.length() + 1 ≠ _Ttype::nvar(),
        "Wrong_input_vector_length_in_CompactPolynomial::eval");
    TL_RAISE_IF(out.length() ≠ _Ttype::nrows(),
        "Wrong_output_vector_length_in_CompactPolynomial::eval");
    Vector x1(v.length() + 1);
    Vector x1p(x1, 1, v.length());

    x1p = v;
    x1[0] = 1.0;
    if (_Ttype::dimen() ≡ 0) out = ConstVector(*this, 0);
    else {
        PowerProvider pp(x1);
        const _Stype &xpow = pp.getNext((const _Stype *) Λ);
        for (int i = 1; i < _Ttype::dimen(); i++) xpow = pp.getNext((const _Stype *) Λ);
        multVec(0.0, out, 1.0, xpow);
    }
}
```

This code is used in section 505.

**508.** Specialization of the **CompactPolynomial** for unfolded tensor.

```
< UCompactPolynomial class declaration 508 > ≡
class UCompactPolynomial : public CompactPolynomial<UFSTensor, UGSTensor,
    URSingleTensor> {
public:
    UCompactPolynomial(const UTensorPolynomial &upol)
        : CompactPolynomial<UFSTensor, UGSTensor, URSingleTensor>(upol) {}
};
```

This code is used in section 491.

**509.** Specialization of the **CompactPolynomial** for folded tensor.

```
< FCompactPolynomial class declaration 509 > ≡
class FCompactPolynomial : public CompactPolynomial<FFSTensor, FGSTensor,
    FRSingleTensor> {
public:
    FCompactPolynomial(const FTensorPolynomial &fpol)
        : CompactPolynomial<FFSTensor, FGSTensor, FRSingleTensor>(fpol) {}
};
```

This code is used in section 491.

**510.** End of `t_polynomial.h` file.



511. Start of `t_polynomial.cpp` file.

```
#include "t_polynomial.h"
#include "kron_prod.h"
{ PowerProvider::getNext unfolded code 512};
{ PowerProvider::getNext folded code 513};
{ PowerProvider destructor code 514};
{ UTensorPolynomial constructor conversion code 515};
{ FTensorPolynomial constructor conversion code 516};
```

512. This method constructs unfolded *ut* of higher dimension, deleting the previous.

```
< PowerProvider::getNext unfolded code 512 > ≡
const URSingleTensor &PowerProvider::getNext(const URSingleTensor *dummy)
{
    if (ut) {
        URSingleTensor *ut_new = new URSingleTensor(nv, ut-dimen() + 1);
        KronProd::kronMult(ConstVector(origv), ConstVector(ut->getData()), ut_new->getData());
        delete ut;
        ut = ut_new;
    }
    else {
        ut = new URSingleTensor(nv, 1);
        ut->getData() = origv;
    }
    return *ut;
}
```

This code is used in section 511.

513. This method just constructs next unfolded *ut* and creates folded *ft*.

```
< PowerProvider::getNext folded code 513 > ≡
const FRSingleTensor &PowerProvider::getNext(const FRSingleTensor *dummy)
{
    getNext(ut);
    if (ft) delete ft;
    ft = new FRSingleTensor(*ut);
    return *ft;
}
```

This code is used in section 511.

514.

```
< PowerProvider destructor code 514 > ≡
PowerProvider::~PowerProvider()
{
    if (ut) delete ut;
    if (ft) delete ft;
}
```

This code is used in section 511.

**515.** Clear.

```

< UTensorPolynomial constructor conversion code 515 > ≡
    UTensorPolynomial::UTensorPolynomial(const FTensorPolynomial &fp)
    : TensorPolynomial<UFSTensor, UGSTensor, URSingleTensor>(fp.nrows(), fp.nvars()) {
        for (FTensorPolynomial::const_iterator it = fp.begin(); it ≠ fp.end(); ++it) {
            insert(new UFSTensor(*(*it).second));
        }
    }

```

This code is used in section 511.

**516.** Clear.

```

< FTensorPolynomial constructor conversion code 516 > ≡
    FTensorPolynomial::FTensorPolynomial(const UTensorPolynomial &up)
    : TensorPolynomial<FFSTensor, FGSTensor, FRSingleTensor>(up.nrows(), up.nvars()) {
        for (UTensorPolynomial::const_iterator it = up.begin(); it ≠ up.end(); ++it) {
            insert(new FFSTensor(*(*it).second));
        }
    }

```

This code is used in section 511.

**517.** End of `t_polynomial.cpp` file.

**518. Moments of normal distribution.** Start of `normal_moments.h` file.

Here we calculate the higher order moments of normally distributed random vector  $u$  with means equal to zero and given variance–covariance matrix  $V$ , this is  $u \sim N(0, V)$ . The moment generating function for such distribution is  $f(t) = e^{\frac{1}{2}t^T V t}$ . If we derivate it wrt  $t$  and unfold the higher dimensional tensors row-wise, we obtain terms like

$$\frac{\partial}{\partial t} f(t) = f(t) \cdot V t$$

$$\frac{\partial^2}{\partial t^2} f(t) = f(t) \cdot (V t \otimes V t + v)$$

$$\frac{\partial^3}{\partial t^3} f(t) = f(t) \cdot (V t \otimes V t \otimes V t + P_?(v \otimes V t) + P_?(V t \otimes v) + v \otimes V t)$$

$$\frac{\partial^4}{\partial t^4} f(t) = f(t) \cdot (V t \otimes V t \otimes V t \otimes V t + S_?(v \otimes V t \otimes V t) + S_?(V t \otimes v \otimes V t) + S_?(V t \otimes V t \otimes v) + S_?(v \otimes v))$$

where  $v$  is vectorized  $V$  ( $v = \text{vec}(V)$ ), and  $P_?$  is a suitable row permutation (corresponds to permutation of multidimensional indices) which permutes the tensor data, so that the index of a variable being derived would be the last. This ensures that all (permuted) tensors can be summed yielding a tensor whose indices have some order (in here we chose the order that more recent derivating variables are to the right). Finally,  $S_?$  is a suitable sum of various  $P_?$ .

We are interested in  $S_?$  multiplying the Kronecker powers  $\otimes^n v$ . The  $S_?$  is a (possibly) multi-set of permutations of even order. Note that we know a number of permutations in  $S_?$ . The above formulas for  $F(t)$  derivatives are valid also for monomial  $u$ , and from literature we know that  $2n$ -th moment is  $\frac{(2n!)}{n!2^n} \sigma^2$ . So there are  $\frac{(2n!)}{n!2^n}$  permutations in  $S_?$ .

In order to find the  $S_?$  we need to define a couple of things. First we define a sort of equivalence between the permutations applicable to even number of indices. We write  $P_1 \equiv P_2$  whenever  $P_1^{-1} \circ P_2$  permutes only whole pairs, or items within pairs, but not indices across the pairs. For instance the permutations  $(0, 1, 2, 3)$  and  $(3, 2, 0, 1)$  are equivalent, but  $(0, 2, 1, 3)$  is not equivalent with the two. Clearly, the  $\equiv$  is an equivalence.

This allows to define a relation  $\sqsubseteq$  between the permutation multi-sets  $S$ , which is basically the subset relation  $\subseteq$  but with respect to the equivalence  $\equiv$ , more formally:

$$S_1 \sqsubseteq S_2 \quad \text{iff} \quad P \in S_1 \Rightarrow \exists Q \in S_2 : P \equiv Q$$

This induces an equivalence  $S_1 \equiv S_2$ .

Now let  $F_n$  denote a set of permutations on  $2n$  indices which is maximal with respect to  $\sqsubseteq$ , and minimal with respect to  $\equiv$ . (In other words, it contains everything up to the equivalence  $\equiv$ .) It is straightforward to calculate a number of permutations in  $F_n$ . This is a total number of all permutations of  $2n$  divided by permutations of pairs divided by permutations within the pairs. This is  $\frac{(2n!)}{n!2^n}$ .

We prove that  $S_? \equiv F_n$ . Clearly  $S_? \sqsubseteq F_n$ , since  $F_n$  is maximal. In order to prove that  $F_n \sqsubseteq S_?$ , let us assert that for any permutation  $P$  and for any (semi)positive definite matrix  $V$  we have  $PS_? \otimes^n v = S_? \otimes^n v$ . Below we show that there is a positive definite matrix  $V$  of some dimension that for any two permutation multi-sets  $S_1, S_2$ , we have

$$S_1 \not\equiv S_2 \Rightarrow S_1(\otimes^n v) \neq S_2(\otimes^n v)$$

So it follows that for any permutation  $P$ , we have  $PS_? \equiv S_?$ . For a purpose of contradiction let  $P \in F_n$  be a permutation which is not equivalent to any permutation from  $S_?$ . Since  $S_?$  is non-empty, let us pick  $P_0 \in S_?$ . Now assert that  $P_0^{-1} S_? \not\equiv P^{-1} S_?$  since the first contains an identity and the second does not contain a permutation equivalent to identity. Thus we have  $(P \circ P_0^{-1}) S_? \not\equiv S_?$  which gives the contradiction and we have proved that  $F_n \sqsubseteq S_?$ . Thus  $F_n \equiv S_?$ . Moreover, we know that  $S_?$  and  $F_n$  have the same number of permutations, hence the minimality of  $S_?$  with respect to  $\equiv$ .

Now it suffices to prove that there exists a positive definite  $V$  such that for any two permutation multi-sets  $S_1$ , and  $S_2$  holds  $S_1 \not\equiv S_2 \Rightarrow S_1(\otimes^n v) \neq S_2(\otimes^n v)$ . If  $V$  is  $n \times n$  matrix, then  $S_1 \not\equiv S_2$  implies that there is identically nonzero polynomial of elements from  $V$  of order  $n$  over integers. If  $V = A^T A$  then there is

identically non-zero polynomial of elements from  $A$  of order  $2n$ . This means, that we have to find  $n(n+1)/2$  tuple  $x$  of real numbers such that all identically non-zero polynomials  $p$  of order  $2n$  over integers yield  $p(x) \neq 0$ .

The  $x$  is constructed as follows:  $x_i = \pi^{\log r_i}$ , where  $r_i$  is  $i$ -th prime. Let us consider monom  $x_1^{j_1} \cdot \dots \cdot x_k^{j_k}$ . When the monom is evaluated, we get

$$\pi^{\log r_1^{j_1} + \dots + \log r_k^{j_k}} = \pi^{\log (r_1^{j_1} \cdot \dots \cdot r_k^{j_k})}$$

Now it is easy to see that if an integer combination of such terms is zero, then the combination must be either trivial or sum to 0 and all monoms must be equal. Both cases imply a polynomial identically equal to zero. So, any non-trivial integer polynomial evaluated at  $x$  must be non-zero.

So, having this result in hand, now it is straightforward to calculate higher moments of normal distribution. Here we define a container, which does the job. In its constructor, we simply calculate Kronecker powers of  $v$  and apply  $F_n$  to  $\otimes^n v$ .  $F_n$  is, in fact, a set of all equivalences in sense of class **Equivalence** over  $2n$  elements, having  $n$  classes each of them having exactly 2 elements.

```
#ifndef NORMAL_MOMENTS_H
#define NORMAL_MOMENTS_H
#include "t_container.h"
    <UNormalMoments class declaration 519>;
    <FNormalMoments class declaration 520>;
#endif
```

519.

```
<UNormalMoments class declaration 519> ≡
class UNormalMoments : public TensorContainer<URSingleTensor> {
public:
    UNormalMoments(int maxdim, const TwoDMatrix &v);
private:
    void generateMoments(int maxdim, const TwoDMatrix &v);
    static bool selectEquiv(const Equivalence &e);
};
```

This code is cited in section 1.

This code is used in section 518.

520.

```
<FNormalMoments class declaration 520> ≡
class FNormalMoments : public TensorContainer<FRSingleTensor> {
public:
    FNormalMoments(const UNormalMoments &moms);
};
```

This code is cited in section 1.

This code is used in section 518.

521. End of normal\_moments.h file.

**522.** Start of `normal_moments.cpp` file.

```
#include "normal_moments.h"
#include "permutation.h"
#include "kron_prod.h"
#include "tl_static.h"
{ UNormalMoments constructor code 523 };
{ UNormalMoments::generateMoments code 524 };
{ UNormalMoments::selectEquiv code 526 };
{ FNormalMoments constructor code 527 };
```

**523.**

```
{ UNormalMoments constructor code 523 } ≡
UNormalMoments::UNormalMoments(int maxdim, const TwoDMatrix &v)
: TensorContainer<URSingleTensor>(1) {
    if (maxdim ≥ 2) generateMoments(maxdim, v);
}
```

This code is used in section 522.

**524.** Here we fill up the container with the tensors for  $d = 2, 4, 6, \dots$  up to the given dimension. Each tensor of moments is equal to  $F_n(\otimes^n v)$ . This has a dimension equal to  $2n$ . See the header file for proof and details.

Here we sequentially construct the Kronecker power  $\otimes^n v$ , and apply  $F_n$ .

```
{ UNormalMoments::generateMoments code 524 } ≡
void UNormalMoments::generateMoments(int maxdim, const TwoDMatrix &v)
{
    TL_RAISE_IF(v.nrows() ≠ v.ncols(),
        "Variance-covariance matrix is not square in UNormalMoments constructor");
    int nv = v.nrows();
    URSingleTensor *mom2 = new URSingleTensor(nv, 2);
    mom2->getData() = v.getData();
    insert(mom2);
    URSingleTensor *kronv = new URSingleTensor(nv, 2);
    kronv->getData() = v.getData();
    for (int d = 4; d ≤ maxdim; d += 2) {
        URSingleTensor *newkronv = new URSingleTensor(nv, d);
        KronProd::kronMult(ConstVector(v->getData()), ConstVector(kronv->getData()),
            newkronv->getData());
        delete kronv;
        kronv = newkronv;
        URSingleTensor *mom = new URSingleTensor(nv, d);
        { apply  $F_n$  to  $kronv$  525 };
        insert(mom);
    }
    delete kronv;
}
```

This code is used in section 522.

**525.** Here we go through all equivalences, select only those having 2 elements in each class, then go through all elements in *kronv* and add to permuted location of *mom*.

The permutation must be taken as inverse of the permutation implied by the equivalence, since we need a permutation which after application to identity of indices yields indices in the equivalence classes. Note how the **Equivalence::apply** method works.

```
<apply  $F_n$  to kronv 525> ≡
    mom→zeros();
    const EquivalenceSet eset = ebundle.get(d);
    for (EquivalenceSet::const_iterator cit = eset.begin(); cit ≠ eset.end(); cit++) {
        if (selectEquiv(*cit)) {
            Permutation per(*cit);
            per.inverse();
            for (Tensor::index it = kronv→begin(); it ≠ kronv→end(); ++it) {
                IntSequence ind(kronv→dimen());
                per.apply(it.getCoor(), ind);
                Tensor::index it2(mom, ind);
                mom→get(*it2, 0) += kronv→get(*it, 0);
            }
        }
    }
}
```

This code is used in section 524.

**526.** We return *true* for an equivalence whose each class has 2 elements.

```
<UNormalMoments::selectEquiv code 526> ≡
    bool UNormalMoments::selectEquiv(const Equivalence &e)
    {
        if (2 * e.numClasses() ≠ e.getN()) return false;
        for (Equivalence::const_seqit si = e.begin(); si ≠ e.end(); ++si) {
            if ((*si).length() ≠ 2) return false;
        }
        return true;
    }
}
```

This code is used in section 522.

**527.** Here we go through all the unfolded container, fold each tensor and insert it.

```
<FNormalMoments constructor code 527> ≡
    FNormalMoments::FNormalMoments(const UNormalMoments &moms)
    : TensorContainer<FRSingleTensor>(1) {
        for (UNormalMoments::const_iterator it = moms.begin(); it ≠ moms.end(); ++it) {
            FRSingleTensor *fm = new FRSingleTensor((*it).second);
            insert(fm);
        }
    }
}
```

This code is used in section 522.

**528.** End of `normal_moments.cpp` file.

**529. Tensor library static data.** Start of `tl_static.h` file.

The purpose of this file is to make a unique static variable which would contain all other static variables and be responsible for their correct initialization and destruction. The variables include an equivalence bundle and a Pascal triangle for binomial coefficients. Both depend on dimension of the problem, and maximum number of variables.

So we declare static *tls* variable of type **TLStatic** encapsulating the variables. The *tls* must be initialized at the beginning of the program, as dimension and number of variables is known.

Also we define a class for Pascal triangle.

```
#ifndef TL_STATIC_H
#define TL_STATIC_H
#include "equivalence.h"
#include "permutation.h"
  (PascalTriangle class declaration 530);
  (TLStatic class declaration 531);
  extern TLStatic tls;
#endif
```

**530.** Pascal triangle is a storage for binomial coefficients. We store in *data* array the coefficients of rectangle starting at  $\binom{0}{0}$ , and ending  $\binom{nmax + kmax}{kmax}$ .

```
(PascalTriangle class declaration 530) ≡
class PascalTriangle {
  int *data;
  int kmax;
  int nmax;
public:
  PascalTriangle(int n, int k);
  ~PascalTriangle()
  { delete[] data; }
  int noverk(int n, int k) const;
};
```

This code is used in section 529.

**531.**

```
(TLStatic class declaration 531) ≡
struct TLStatic {
  EquivalenceBundle *ebundle;
  PermutationBundle *pbundle;
  PascalTriangle *ptriang;
  TLStatic();
  ~TLStatic();
  void init(int dim, int nvar);
};
```

This code is cited in section 1.

This code is used in section 529.

**532.** End of `tl_static.h` file.

**533.** Start of `tl_static.cpp` file.

```
#include "tl_static.h"
#include "tl_exception.h"
    TLStatic tls;
    < TLStatic methods 534 >;
    < PascalTriangle constructor code 535 >;
    < PascalTriangle::noverk code 536 >;
```

**534.** Note that we allow for repeated calls of *init*. This is not normal and the only purpose of allowing this is the test suite.

```
< TLStatic methods 534 > ≡
    TLStatic::TLStatic()
    {
        ebundle = Λ;
        pbundle = Λ;
        ptriang = Λ;
    }
    TLStatic::~~TLStatic()
    {
        if (ebundle) delete ebundle;
        if (pbundle) delete pbundle;
        if (ptriang) delete ptriang;
    }
    void TLStatic::init(int dim, int nvar)
    {
        if (ebundle) ebundle->generateUpTo(dim);
        else ebundle = new EquivalenceBundle(dim);
        if (pbundle) pbundle->generateUpTo(dim);
        else pbundle = new PermutationBundle(dim);
        if (ptriang) delete ptriang;
        ptriang = new PascalTriangle(nvar, dim);
    }
```

This code is used in section 533.

**535.** The coefficients are stored in *data* row by row where a row are coeffs with the same *k*.

We first initialize the first row with ones. Then for each other row we initialize the first item to one, and other items are a sum of coefficients of  $n - 1$  which is in code  $i + j - 1$ .

```
< PascalTriangle constructor code 535 > ≡
    PascalTriangle::PascalTriangle(int n, int k)
    : data(new int[(n + 1) * (k + 1)]), kmax(k), nmax(n) {
        for (int i = 0; i ≤ n; i++) data[i] = 1;
        for (int j = 1; j ≤ k; j++) {
            data[j * (nmax + 1)] = 1;
            for (int i = 1; i ≤ n; i++) data[j * (nmax + 1) + i] = noverk(i + j - 1, j) + noverk(i + j - 1, j - 1);
        }
    }
```

This code is used in section 533.



**536.** Clear. Recall, that there are  $n_{max} + 1$  items in a row.

```

⟨PascalTriangle::noverk code 536⟩ ≡
  int PascalTriangle::noverk(int n, int k) const
  {
    TL_RAISE_IF(k > n ∨ n < 0, "Wrong arguments for PascalTriangle::noverk");
    if (k ≤ kmax ∧ n - k ≤ nmax) return data[k * (nmax + 1) + n - k];
    if (n - k ≤ kmax ∧ k ≤ nmax) return data[(n - k) * (nmax + 1) + k];
    TL_RAISE("n or k out of range in PascalTriangle::noverk");
    return 0;
  }

```

This code is used in section 533.

**537.** End of `tl.static.cpp` file.

**538. Index.**

- `__FILE__`: [46](#), [375](#).
- `__LINE__`: [46](#), [375](#).
- `_const_ptr`: [369](#), [373](#), [382](#).
- `_Ctraits`: [28](#).
- `_Ctype`: [8](#), [12](#), [28](#), [29](#), [37](#), [41](#), [43](#), [395](#), [396](#), [405](#), [407](#), [408](#), [409](#), [411](#), [412](#), [413](#), [461](#), [462](#).
- `_Dtype`: [12](#), [37](#), [42](#), [43](#).
- `_index`: [213](#), [214](#).
- `_Map`: [369](#).
- `_Mtraits`: [14](#), [17](#), [18](#), [19](#).
- `_mvtype`: [14](#), [369](#), [375](#).
- `_ptr`: [369](#), [373](#), [375](#), [376](#), [493](#), [500](#).
- `_Ptype`: [420](#), [421](#).
- `_Self`: [213](#).
- `_Stype`: [396](#), [397](#), [398](#), [399](#), [400](#), [401](#), [405](#), [406](#), [409](#), [410](#), [413](#), [419](#), [420](#), [421](#), [461](#), [462](#), [463](#), [493](#), [494](#), [495](#), [498](#), [505](#), [506](#), [507](#).
- `_Tcond`: [20](#), [21](#), [39](#), [43](#).
- `_TGStype`: [493](#), [494](#), [495](#), [496](#), [505](#), [506](#).
- `_Tmutex`: [13](#), [14](#), [17](#), [18](#), [20](#), [21](#), [39](#), [43](#).
- `_Tparent`: [14](#), [15](#), [16](#), [405](#), [409](#), [493](#), [497](#), [498](#), [499](#), [501](#), [502](#).
- `_Tptr`: [213](#).
- `_Tthread`: [7](#), [12](#).
- `_Ttraits`: [7](#), [8](#), [10](#), [28](#).
- `_Ttype`: [317](#), [369](#), [371](#), [372](#), [395](#), [396](#), [398](#), [401](#), [405](#), [409](#), [413](#), [416](#), [420](#), [461](#), [462](#), [493](#), [494](#), [497](#), [498](#), [499](#), [500](#), [501](#), [502](#), [505](#), [506](#), [507](#).
- `a`: [82](#), [115](#), [117](#), [214](#), [219](#), [315](#), [318](#), [332](#), [339](#), [357](#).
- `ad`: [403](#), [404](#), [445](#), [446](#), [457](#).
- `add`: [50](#), [73](#), [74](#), [89](#), [90](#), [111](#), [117](#), [120](#), [153](#), [169](#), [173](#), [174](#), [228](#), [245](#), [252](#), [271](#), [273](#), [277](#), [287](#), [327](#), [329](#), [330](#), [339](#), [361](#), [475](#), [496](#), [499](#), [502](#), [506](#).
- `addColumn`: [84](#), [90](#), [237](#), [245](#), [252](#), [326](#), [338](#).
- `added`: [155](#), [183](#), [185](#).
- `addKronColumn`: [471](#), [474](#), [475](#).
- `addParents`: [155](#), [183](#), [184](#), [185](#).
- `addRow`: [83](#), [89](#), [298](#).
- `addSubTensor`: [231](#), [233](#), [245](#), [252](#).
- `addTo`: [314](#), [317](#), [326](#), [327](#), [338](#), [388](#), [391](#), [436](#), [441](#), [445](#), [446](#), [452](#), [457](#), [479](#), [488](#).
- `akronid`: [118](#), [123](#).
- `along_col`: [214](#), [232](#), [234](#), [237](#), [239](#), [240](#), [247](#), [248](#), [258](#), [260](#), [270](#), [271](#), [273](#), [274](#), [279](#), [280](#), [281](#), [315](#), [318](#), [322](#), [339](#).
- `along_row`: [214](#), [291](#), [293](#), [298](#), [302](#), [487](#).
- `apply`: [193](#), [198](#), [205](#), [313](#), [326](#), [327](#), [330](#), [338](#), [417](#), [436](#), [452](#), [474](#), [525](#).
- `attr`: [37](#).
- `aux`: [127](#).
- `average`: [153](#), [167](#), [171](#).
- `b`: [82](#), [214](#), [219](#).
- `back`: [209](#).
- `base`: [117](#), [123](#).
- `bdims`: [471](#), [474](#).
- `begin`: [11](#), [31](#), [148](#), [155](#), [158](#), [169](#), [170](#), [177](#), [178](#), [179](#), [180](#), [182](#), [186](#), [214](#), [237](#), [239](#), [240](#), [245](#), [248](#), [252](#), [253](#), [270](#), [274](#), [279](#), [280](#), [281](#), [285](#), [298](#), [302](#), [309](#), [326](#), [329](#), [338](#), [339](#), [350](#), [351](#), [352](#), [353](#), [357](#), [370](#), [371](#), [372](#), [377](#), [378](#), [379](#), [380](#), [381](#), [382](#), [387](#), [388](#), [389](#), [391](#), [435](#), [436](#), [438](#), [441](#), [442](#), [445](#), [446](#), [451](#), [452](#), [454](#), [457](#), [474](#), [488](#), [506](#), [515](#), [516](#), [525](#), [526](#), [527](#).
- `bldim`: [266](#).
- `blnvar`: [266](#).
- `block.first`: [267](#), [268](#), [269](#).
- `block.last`: [267](#), [268](#), [269](#).
- `blstart`: [266](#).
- `body`: [137](#).
- `broadcast`: [20](#), [24](#), [25](#), [39](#), [43](#).
- `bundle`: [156](#), [188](#), [189](#), [190](#), [195](#), [207](#), [208](#), [209](#).
- `c`: [8](#), [10](#), [12](#), [14](#), [15](#), [16](#), [17](#), [27](#), [29](#), [32](#), [33](#), [37](#), [40](#), [41](#), [42](#), [43](#), [82](#), [97](#), [119](#), [213](#), [214](#), [215](#), [216](#), [245](#), [252](#), [271](#), [273](#), [291](#), [293](#), [329](#), [330](#), [332](#), [334](#), [339](#), [343](#), [344](#), [345](#), [348](#), [356](#), [357](#), [361](#), [363](#), [371](#), [372](#), [383](#), [384](#), [387](#), [389](#), [403](#), [404](#), [423](#), [424](#), [425](#), [427](#), [428](#), [432](#), [437](#), [440](#), [444](#), [448](#), [453](#), [456](#), [462](#), [478](#), [482](#).
- `calcFoldColumns`: [313](#).
- `calcFoldMaxOffset`: [256](#), [258](#), [265](#), [270](#), [271](#), [273](#), [274](#), [277](#), [331](#), [361](#).
- `calcFoldOffset`: [256](#), [257](#), [266](#), [332](#).
- `calcMaxOffset`: [231](#), [232](#), [233](#), [234](#), [237](#), [238](#), [239](#), [240](#), [247](#), [248](#), [266](#), [291](#), [293](#), [298](#), [302](#), [316](#), [354](#), [478](#), [486](#), [487](#).
- `calcOffset`: [316](#), [332](#), [337](#).
- `calculateOffsets`: [402](#), [405](#), [409](#), [462](#).
- `calcUnfoldColumns`: [313](#).
- `calcUnfoldMaxOffset`: [256](#), [260](#), [264](#), [279](#), [280](#), [281](#), [287](#), [322](#).
- `caller`: [17](#), [18](#), [19](#).
- `cc`: [332](#).
- `changed`: [21](#), [22](#), [24](#), [25](#), [26](#).
- `check`: [14](#), [18](#), [19](#), [374](#), [375](#), [397](#), [403](#), [404](#), [432](#), [448](#), [495](#), [496](#), [497](#), [498](#), [499](#), [501](#), [502](#), [506](#).
- `checkDimForMult`: [98](#), [110](#), [115](#), [117](#), [118](#).
- `cit`: [357](#), [525](#).
- `cl`: [136](#).
- `classes`: [154](#), [158](#), [173](#), [174](#), [175](#), [176](#), [177](#), [178](#), [179](#), [182](#).
- `clear`: [175](#), [369](#), [377](#), [392](#).
- `cnt`: [351](#), [352](#), [353](#).

- col*: [91](#), [257](#), [259](#), [277](#), [287](#).
- coli*: [475](#).
- cols*: [81](#), [82](#), [94](#), [95](#), [96](#), [97](#), [106](#), [107](#), [108](#), [109](#), [115](#), [116](#), [117](#), [118](#), [129](#), [130](#), [294](#), [306](#), [327](#), [478](#), [482](#), [484](#), [485](#), [486](#), [487](#).
- CompactPolynomial**: [505](#), [506](#), [508](#), [509](#).
- compression*: [88](#).
- COMPRESSION\_NONE**: [88](#).
- computeSortingMap*: [193](#), [201](#).
- cond*: [20](#), [21](#), [22](#), [23](#), [24](#), [25](#), [26](#), [39](#), [43](#).
- cond\_traits**: [5](#), [20](#), [21](#), [22](#), [23](#), [24](#), [25](#), [26](#), [28](#), [39](#), [43](#).
- condition*: [6](#).
- condition\_counter**: [21](#), [22](#), [23](#), [27](#), [28](#), [42](#).
- const\_iterator**: [154](#), [155](#), [169](#), [170](#), [186](#), [239](#), [271](#), [274](#), [329](#), [330](#), [339](#), [343](#), [344](#), [345](#), [350](#), [351](#), [352](#), [353](#), [357](#), [361](#), [369](#), [370](#), [371](#), [373](#), [374](#), [378](#), [379](#), [380](#), [381](#), [387](#), [388](#), [389](#), [391](#), [436](#), [441](#), [445](#), [446](#), [452](#), [457](#), [474](#), [515](#), [516](#), [525](#), [527](#).
- const\_reverse\_iterator**: [184](#).
- const\_seqit*: [148](#), [149](#), [154](#), [158](#), [159](#), [177](#), [178](#), [180](#), [181](#), [182](#), [382](#), [526](#).
- ConstGeneralMatrix**: [81](#), [87](#), [401](#).
- ConstTwoDMatrix**: [80](#), [81](#), [82](#), [83](#), [84](#), [87](#), [88](#), [89](#), [90](#), [98](#), [99](#), [101](#), [102](#), [103](#), [110](#), [115](#), [117](#), [118](#), [119](#), [122](#), [247](#), [315](#), [318](#), [327](#), [380](#), [381](#), [499](#).
- ConstVector**: [1](#), [89](#), [90](#), [98](#), [111](#), [126](#), [127](#), [128](#), [232](#), [234](#), [237](#), [247](#), [294](#), [306](#), [307](#), [401](#), [475](#), [487](#), [492](#), [498](#), [499](#), [502](#), [507](#), [512](#), [524](#).
- cont*: [422](#), [423](#), [424](#), [425](#), [426](#), [427](#), [428](#), [433](#), [434](#), [436](#), [437](#), [439](#), [440](#), [443](#), [444](#), [449](#), [450](#), [452](#), [453](#), [455](#), [456](#), [471](#), [474](#).
- container*: [422](#), [423](#), [424](#), [425](#), [426](#), [427](#), [428](#), [434](#), [437](#), [440](#), [444](#), [450](#), [453](#), [456](#).
- contractAndAdd*: [257](#), [259](#), [277](#), [287](#), [495](#).
- conts*: [396](#), [397](#), [398](#), [405](#), [409](#), [462](#).
- convert*: [462](#).
- coord*: [213](#), [256](#), [258](#), [260](#), [263](#), [271](#), [272](#), [273](#), [280](#), [281](#), [313](#), [314](#), [316](#), [322](#), [329](#), [330](#), [332](#), [338](#), [345](#), [361](#), [362](#), [395](#), [401](#), [417](#), [423](#), [424](#), [425](#), [427](#), [428](#), [433](#), [436](#), [437](#), [439](#), [440](#), [443](#), [444](#), [445](#), [446](#), [449](#), [452](#), [453](#), [455](#), [456](#), [495](#), [496](#).
- coord\_srt*: [330](#).
- copyColumn*: [84](#), [90](#), [240](#), [248](#), [253](#), [270](#), [273](#), [279](#), [280](#), [281](#), [285](#).
- copyRow*: [83](#), [89](#), [302](#).
- count*: [144](#).
- counter*: [21](#), [22](#), [24](#), [25](#), [26](#), [27](#), [28](#), [29](#), [31](#), [42](#).
- cp*: [330](#).
- cr*: [213](#).
- createPackedColumn*: [395](#), [401](#), [417](#).
- createPackedColumns*: [417](#), [482](#).
- cs*: [498](#), [499](#).
- ct*: [41](#), [42](#).
- cum*: [67](#), [329](#), [330](#).
- cumtmp*: [329](#).
- curmax*: [190](#), [209](#).
- d*: [51](#), [81](#), [82](#), [83](#), [84](#), [89](#), [90](#), [137](#), [214](#), [215](#), [216](#), [231](#), [232](#), [233](#), [234](#), [238](#), [291](#), [293](#), [294](#), [295](#), [307](#), [343](#), [344](#), [354](#), [498](#), [499](#), [501](#), [502](#), [506](#), [524](#).
- data*: [50](#), [51](#), [52](#), [56](#), [57](#), [58](#), [59](#), [60](#), [65](#), [66](#), [67](#), [88](#), [153](#), [166](#), [169](#), [170](#), [171](#), [172](#), [530](#), [535](#), [536](#).
- dcontainer*: [422](#), [426](#), [434](#), [450](#).
- decideFillMethod*: [314](#), [321](#), [322](#).
- decrease*: [25](#), [42](#).
- decrement*: [213](#), [214](#), [215](#), [216](#), [222](#), [224](#), [227](#), [231](#), [233](#), [243](#), [250](#), [256](#), [257](#), [259](#), [267](#), [269](#), [282](#), [290](#), [292](#), [301](#), [304](#), [314](#), [317](#), [323](#), [335](#), [479](#).
- dense\_cont*: [422](#), [426](#), [433](#), [434](#), [449](#), [450](#).
- dense\_slice*: [439](#), [455](#).
- dense\_slice1*: [439](#), [455](#).
- der*: [502](#).
- derivative*: [493](#), [501](#), [502](#).
- destroy*: [20](#), [23](#), [39](#), [43](#), [50](#), [51](#), [52](#), [56](#), [57](#), [58](#), [59](#), [60](#).
- detach\_run*: [7](#), [12](#), [27](#), [37](#), [43](#).
- detach\_thread**: [5](#), [12](#), [27](#), [28](#), [32](#).
- detach\_thread\_group**: [5](#), [28](#), [30](#), [32](#), [35](#).
- dim*: [95](#), [98](#), [99](#), [100](#), [137](#), [214](#), [238](#), [343](#), [360](#), [378](#), [403](#), [404](#), [432](#), [448](#), [531](#), [534](#).
- dimen*: [56](#), [57](#), [97](#), [98](#), [101](#), [106](#), [114](#), [121](#), [122](#), [124](#), [125](#), [126](#), [129](#), [135](#), [137](#), [145](#), [147](#), [214](#), [231](#), [233](#), [237](#), [239](#), [240](#), [242](#), [243](#), [244](#), [245](#), [247](#), [248](#), [250](#), [251](#), [252](#), [256](#), [258](#), [260](#), [266](#), [270](#), [271](#), [273](#), [274](#), [275](#), [277](#), [279](#), [280](#), [281](#), [282](#), [284](#), [287](#), [290](#), [292](#), [298](#), [300](#), [301](#), [302](#), [304](#), [305](#), [306](#), [309](#), [315](#), [318](#), [322](#), [323](#), [325](#), [326](#), [327](#), [328](#), [332](#), [334](#), [338](#), [339](#), [343](#), [348](#), [358](#), [361](#), [378](#), [388](#), [391](#), [400](#), [401](#), [413](#), [414](#), [415](#), [417](#), [419](#), [421](#), [435](#), [436](#), [438](#), [441](#), [442](#), [445](#), [446](#), [451](#), [452](#), [454](#), [457](#), [474](#), [475](#), [478](#), [482](#), [483](#), [485](#), [486](#), [487](#), [500](#), [506](#), [507](#), [512](#), [525](#).
- dims*: [88](#).
- dleft*: [277](#), [287](#).
- dright*: [277](#), [287](#).
- ds*: [316](#), [331](#), [332](#), [333](#).
- dum*: [506](#).
- dumgs*: [506](#).
- dummy*: [82](#), [136](#), [157](#), [173](#), [339](#), [435](#), [451](#), [492](#), [512](#), [513](#).
- dummy\_f*: [438](#), [442](#), [454](#).
- dummy\_u*: [445](#), [446](#), [457](#).
- dumvns*: [506](#).
- e*: [139](#), [148](#), [149](#), [154](#), [155](#), [157](#), [174](#), [175](#), [176](#), [184](#), [185](#), [193](#), [313](#), [315](#), [316](#), [318](#), [339](#), [382](#),

- [413](#), [519](#), [526](#).  
*ebundle*: [369](#), [370](#), [371](#), [372](#), [388](#), [391](#), [395](#), [423](#), [427](#), [436](#), [437](#), [441](#), [445](#), [446](#), [452](#), [453](#), [457](#), [525](#), [531](#), [534](#).  
**Else**: [6](#).  
**Empty**: [5](#), [12](#), [13](#), [20](#), [43](#).  
*empty*: [5](#), [9](#), [30](#), [33](#), [35](#), [43](#), [183](#), [377](#).  
*end*: [14](#), [15](#), [16](#), [31](#), [148](#), [155](#), [158](#), [169](#), [170](#), [177](#), [178](#), [179](#), [180](#), [182](#), [186](#), [214](#), [237](#), [239](#), [240](#), [245](#), [248](#), [252](#), [253](#), [270](#), [274](#), [279](#), [280](#), [281](#), [285](#), [298](#), [302](#), [309](#), [326](#), [329](#), [338](#), [339](#), [350](#), [351](#), [352](#), [353](#), [357](#), [370](#), [371](#), [372](#), [373](#), [374](#), [376](#), [378](#), [379](#), [380](#), [381](#), [382](#), [384](#), [387](#), [388](#), [389](#), [391](#), [392](#), [435](#), [436](#), [438](#), [441](#), [442](#), [445](#), [446](#), [451](#), [452](#), [454](#), [457](#), [474](#), [488](#), [506](#), [515](#), [516](#), [525](#), [526](#), [527](#).  
*end\_col*: [353](#).  
*end\_flag*: [138](#), [145](#), [147](#).  
*end\_seq*: [478](#), [482](#), [483](#), [487](#).  
*equis*: [155](#), [183](#), [184](#), [185](#), [186](#).  
**Equivalence**: [139](#), [148](#), [149](#), [154](#), [155](#), [157](#), [173](#), [174](#), [175](#), [176](#), [177](#), [178](#), [179](#), [180](#), [181](#), [182](#), [183](#), [184](#), [185](#), [186](#), [193](#), [313](#), [315](#), [316](#), [318](#), [339](#), [382](#), [413](#), [518](#), [519](#), [525](#), [526](#).  
EQUIVALENCE\_H: [152](#).  
**EquivalenceBundle**: [156](#), [187](#), [188](#), [189](#), [190](#), [369](#), [370](#), [395](#), [423](#), [427](#), [531](#), [534](#).  
**EquivalenceSet**: [155](#), [156](#), [183](#), [184](#), [185](#), [186](#), [189](#), [190](#), [388](#), [391](#), [436](#), [441](#), [445](#), [446](#), [452](#), [457](#), [474](#), [525](#).  
*erase*: [16](#), [174](#), [376](#), [377](#).  
*eset*: [388](#), [391](#), [436](#), [441](#), [445](#), [446](#), [452](#), [457](#), [474](#), [525](#).  
*eval*: [507](#).  
*evalHorner*: [493](#), [499](#).  
*evalPartially*: [502](#).  
*evalTrad*: [493](#), [498](#).  
*exit*: [7](#), [12](#), [37](#), [41](#), [42](#), [43](#).  
*expr*: [46](#).  
*f*: [47](#), [50](#), [74](#).  
*false*: [6](#), [26](#), [51](#), [61](#), [121](#), [145](#), [168](#), [170](#), [176](#), [184](#), [350](#), [414](#), [526](#).  
*fc*: [384](#).  
*fclose*: [91](#).  
**FCompactPolynomial**: [509](#).  
*fd*: [81](#), [82](#), [88](#), [91](#), [380](#).  
*fetchTensors*: [369](#), [382](#), [388](#), [391](#), [474](#).  
**FFSTensor**: [231](#), [232](#), [233](#), [234](#), [237](#), [238](#), [239](#), [240](#), [241](#), [242](#), [243](#), [244](#), [245](#), [248](#), [249](#), [258](#), [266](#), [273](#), [281](#), [289](#), [292](#), [293](#), [298](#), [300](#), [324](#), [354](#), [438](#), [442](#), [454](#), [504](#), [509](#), [516](#).  
**FGSContainer**: [1](#), [3](#), [383](#), [384](#), [386](#), [387](#), [389](#), [390](#), [391](#), [392](#), [403](#), [422](#), [432](#), [434](#), [471](#), [474](#).  
**FGSTensor**: [1](#), [230](#), [231](#), [245](#), [255](#), [257](#), [258](#), [259](#), [260](#), [270](#), [271](#), [273](#), [274](#), [275](#), [276](#), [277](#), [279](#), [280](#), [281](#), [283](#), [314](#), [317](#), [326](#), [338](#), [345](#), [384](#), [389](#), [390](#), [391](#), [394](#), [403](#), [407](#), [411](#), [422](#), [423](#), [424](#), [425](#), [431](#), [432](#), [433](#), [434](#), [435](#), [436](#), [437](#), [438](#), [439](#), [440](#), [441](#), [442](#), [444](#), [445](#), [446](#), [455](#), [464](#), [471](#), [474](#), [475](#), [478](#), [482](#), [504](#), [509](#), [516](#).  
*fi*: [279](#), [280](#), [281](#), [403](#), [404](#), [438](#), [442](#), [454](#), [457](#).  
**fill\_method**: [314](#), [321](#).  
*fill\_threshold*: [403](#), [404](#), [430](#), [439](#).  
*fillFromSparseOne*: [314](#), [321](#), [322](#), [329](#).  
*fillFromSparseTwo*: [314](#), [322](#), [330](#).  
*find*: [14](#), [15](#), [16](#), [149](#), [154](#), [174](#), [178](#), [181](#), [373](#), [374](#), [376](#).  
*findClass*: [57](#), [135](#), [143](#).  
*findHaving*: [159](#), [177](#).  
FINE\_CONTAINER\_H: [459](#).  
**FineContainer**: [459](#), [461](#), [462](#), [463](#), [464](#), [465](#).  
*first*: [14](#), [18](#), [19](#), [183](#), [214](#), [239](#), [271](#), [274](#), [314](#), [321](#), [322](#), [329](#), [330](#), [339](#), [349](#), [351](#), [352](#), [353](#), [357](#), [361](#), [378](#), [379](#), [380](#), [381](#).  
*first\_col*: [81](#), [82](#), [87](#).  
*first\_nz\_row*: [343](#), [348](#).  
*first\_pos*: [348](#), [349](#), [357](#).  
*first\_row*: [81](#), [82](#), [87](#), [214](#), [215](#), [216](#), [232](#), [234](#), [258](#), [260](#), [372](#), [493](#), [503](#), [504](#).  
*flag*: [201](#).  
*flags*: [201](#).  
*fm*: [527](#).  
*fname*: [47](#), [82](#), [91](#).  
**FNormalMoments**: [520](#), [527](#).  
*fold*: [215](#), [233](#), [249](#), [259](#), [283](#), [290](#), [294](#), [303](#), [308](#), [314](#), [324](#).  
*fold\_ind*: [339](#).  
*folded*: [281](#), [389](#).  
**FoldedFineContainer**: [459](#), [464](#).  
**FoldedGContainer**: [411](#).  
**FoldedStackContainer**: [394](#), [403](#), [407](#), [411](#), [422](#), [423](#), [424](#), [425](#), [430](#), [431](#), [432](#), [434](#), [435](#), [437](#), [438](#), [439](#), [440](#), [441](#), [442](#), [444](#), [445](#), [446](#), [464](#).  
**FoldedZContainer**: [394](#), [407](#).  
*fopen*: [91](#).  
*former*: [199](#).  
*fp*: [503](#), [504](#), [515](#).  
*fpol*: [509](#).  
*fprintf*: [91](#).  
*fps*: [391](#), [436](#), [445](#), [446](#).  
**FPSTensor**: [311](#), [317](#), [318](#), [334](#), [335](#), [336](#), [337](#), [338](#), [339](#), [391](#), [436](#), [442](#), [445](#), [446](#).  
*fr\_col*: [90](#).  
*fr\_row*: [89](#).  
*from*: [83](#), [84](#), [89](#), [90](#), [237](#), [252](#), [327](#).

- from\_ind*: [237](#).  
*front*: [9](#), [30](#), [183](#).  
**FRSingleTensor**: [230](#), [257](#), [277](#), [295](#), [308](#), [309](#),  
[441](#), [479](#), [488](#), [492](#), [504](#), [509](#), [513](#), [516](#), [520](#), [527](#).  
**FRTensor**: [290](#), [291](#), [292](#), [293](#), [295](#), [298](#), [299](#),  
[300](#), [301](#), [302](#), [303](#), [309](#).  
*fs*: [475](#).  
**FS\_TENSOR\_H**: [230](#).  
*fslice*: [455](#).  
**FSSparseTensor**: [230](#), [232](#), [239](#), [255](#), [258](#), [260](#),  
[271](#), [274](#), [280](#), [314](#), [321](#), [322](#), [329](#), [330](#), [344](#), [345](#),  
[354](#), [355](#), [356](#), [357](#), [360](#), [361](#), [403](#), [404](#), [423](#), [424](#),  
[425](#), [427](#), [428](#), [431](#), [435](#), [437](#), [438](#), [440](#), [441](#),  
[442](#), [444](#), [447](#), [451](#), [453](#), [454](#), [456](#).  
*ft*: [216](#), [232](#), [234](#), [248](#), [258](#), [260](#), [279](#), [280](#), [281](#),  
[291](#), [293](#), [295](#), [302](#), [318](#), [492](#), [513](#), [514](#).  
**FTensor**: [215](#), [216](#), [227](#), [228](#), [231](#), [232](#), [233](#), [237](#),  
[239](#), [240](#), [243](#), [244](#), [249](#), [257](#), [258](#), [259](#), [266](#), [269](#),  
[270](#), [271](#), [273](#), [274](#), [283](#), [290](#), [292](#), [293](#), [294](#), [298](#),  
[301](#), [303](#), [308](#), [314](#), [317](#), [318](#), [324](#), [339](#).  
**FTensorPolynomial**: [503](#), [504](#), [509](#), [515](#), [516](#).  
*function*: [41](#).  
*g*: [403](#), [404](#), [405](#), [407](#), [408](#), [433](#), [445](#), [446](#), [449](#), [457](#).  
**GContainer**: [409](#), [411](#), [412](#).  
**GeneralMatrix**: [81](#), [82](#).  
*generateMoments*: [519](#), [523](#), [524](#).  
*generateUpTo*: [156](#), [187](#), [190](#), [195](#), [206](#), [209](#), [534](#).  
*get*: [15](#), [18](#), [19](#), [88](#), [91](#), [117](#), [156](#), [189](#), [194](#), [195](#),  
[208](#), [239](#), [271](#), [274](#), [309](#), [329](#), [330](#), [357](#), [373](#),  
[382](#), [388](#), [391](#), [398](#), [403](#), [404](#), [433](#), [436](#), [441](#),  
[445](#), [446](#), [449](#), [452](#), [457](#), [474](#), [488](#), [495](#), [496](#),  
[497](#), [498](#), [499](#), [501](#), [502](#), [506](#), [525](#).  
*getAllSize*: [395](#), [413](#), [431](#), [447](#), [482](#).  
*getCont*: [395](#), [396](#), [462](#).  
*getCoord*: [213](#), [214](#), [237](#), [240](#), [245](#), [248](#), [252](#), [253](#),  
[270](#), [273](#), [279](#), [280](#), [281](#), [286](#), [298](#), [302](#), [309](#), [326](#),  
[329](#), [338](#), [339](#), [357](#), [392](#), [435](#), [438](#), [441](#), [442](#), [445](#),  
[446](#), [451](#), [454](#), [457](#), [488](#), [506](#), [525](#).  
*getData*: [117](#), [123](#), [136](#), [153](#), [306](#), [307](#), [475](#), [487](#),  
[498](#), [499](#), [512](#), [524](#).  
*getDims*: [245](#), [246](#), [252](#), [257](#), [259](#), [274](#), [277](#),  
[287](#), [326](#), [327](#), [339](#), [345](#), [388](#), [391](#), [436](#), [445](#),  
[446](#), [452](#), [457](#).  
*getEqBundle*: [370](#), [474](#).  
*getFillFactor*: [321](#), [343](#), [353](#).  
*getFirstIndexOf*: [259](#), [285](#), [286](#), [339](#).  
*getFirstNonZeroRow*: [343](#), [439](#), [455](#).  
*getFoldIndexFillFactor*: [343](#), [351](#).  
*getIndices*: [384](#), [392](#).  
*getLastNonZeroRow*: [343](#), [439](#), [455](#).  
*getLD*: [117](#).  
*getLengthOfMatrixStacks*: [395](#), [399](#), [401](#).  
*getMap*: [131](#), [149](#), [181](#), [193](#), [239](#), [271](#), [274](#), [329](#),  
[330](#), [339](#), [343](#), [361](#), [433](#), [449](#).  
*getMat*: [99](#), [101](#), [102](#), [103](#).  
*getMatrix*: [395](#), [398](#), [401](#), [416](#), [421](#).  
*getMax*: [50](#), [72](#).  
*getMaxDim*: [378](#), [506](#).  
*getN*: [154](#), [193](#), [526](#).  
*getNC*: [460](#), [462](#).  
*getNext*: [492](#), [495](#), [498](#), [507](#), [512](#), [513](#).  
*getNum*: [194](#), [436](#), [452](#).  
*getNumDistinct*: [50](#), [71](#), [142](#).  
*getNumNonZero*: [343](#), [439](#), [443](#), [455](#).  
*getNVS*: [245](#), [246](#), [252](#), [256](#), [277](#), [287](#), [331](#),  
[332](#), [364](#).  
*getNVX*: [252](#), [256](#), [258](#), [260](#), [266](#), [267](#), [268](#), [269](#),  
[270](#), [271](#), [273](#), [274](#), [275](#), [279](#), [280](#), [281](#), [282](#),  
[284](#), [315](#), [318](#), [322](#), [323](#), [325](#), [327](#), [328](#), [334](#),  
[339](#), [363](#), [474](#).  
*getOffset*: [213](#), [214](#), [215](#), [216](#), [225](#), [226](#), [231](#), [233](#),  
[244](#), [251](#), [257](#), [259](#), [266](#), [284](#), [289](#), [290](#), [292](#),  
[305](#), [314](#), [317](#), [325](#), [337](#), [475](#), [479](#).  
*getOffsetRecurse*: [216](#), [228](#).  
*getOldIndex*: [460](#), [461](#), [462](#).  
*getPer*: [100](#), [313](#), [315](#), [318](#), [326](#), [327](#), [338](#), [436](#), [452](#).  
*getPrefixLength*: [49](#), [50](#), [70](#), [228](#).  
*getPreserving*: [194](#), [205](#), [330](#).  
*getProdSym*: [413](#).  
*getRC*: [97](#), [110](#), [118](#).  
*getRefSize*: [460](#), [462](#).  
*getSize*: [418](#), [421](#).  
*getStackOffsets*: [395](#), [396](#).  
*getStackSizes*: [395](#), [396](#), [418](#), [436](#), [439](#), [443](#),  
[452](#), [455](#), [462](#).  
*getSym*: [231](#), [233](#), [246](#), [256](#), [257](#), [259](#), [266](#), [267](#),  
[268](#), [275](#), [277](#), [278](#), [286](#), [287](#), [290](#), [292](#), [316](#),  
[334](#), [343](#), [344](#), [345](#), [364](#), [375](#), [388](#), [391](#), [436](#),  
[441](#), [445](#), [446](#), [452](#), [457](#), [474](#).  
*getThreadId*: [7](#), [37](#).  
*getType*: [394](#), [395](#), [396](#), [397](#), [398](#), [399](#), [400](#), [401](#),  
[405](#), [406](#), [410](#), [415](#), [419](#), [421](#), [461](#).  
*getUnfoldIndexFillFactor*: [343](#), [352](#), [439](#).  
*getUnitPos*: [395](#), [400](#), [401](#).  
*gr*: [432](#), [435](#), [438](#), [442](#), [448](#), [451](#), [454](#).  
*gs*: [409](#), [411](#), [412](#).  
**GS\_TENSOR\_H**: [255](#).  
*gss*: [405](#), [407](#), [408](#).  
**GSSParseTensor**: [258](#).  
**GSSparseTensor**: [257](#), [258](#), [274](#), [318](#), [339](#), [345](#),  
[361](#), [363](#), [364](#), [403](#), [439](#), [443](#), [446](#), [455](#).  
*h*: [479](#), [487](#).  
*has*: [153](#), [155](#), [170](#), [177](#), [184](#), [185](#).  
**HAVE\_PTHREAD**: [5](#), [35](#).

*hdims*: [471](#), [474](#).

*header*: [441](#), [479](#), [487](#).

*i*: [10](#), [50](#), [51](#), [52](#), [56](#), [57](#), [58](#), [59](#), [61](#), [62](#), [63](#), [64](#), [65](#),  
[66](#), [67](#), [68](#), [69](#), [70](#), [71](#), [72](#), [73](#), [75](#), [76](#), [77](#), [78](#),  
[88](#), [95](#), [97](#), [98](#), [99](#), [103](#), [106](#), [111](#), [112](#), [113](#), [114](#),  
[115](#), [117](#), [118](#), [121](#), [124](#), [126](#), [129](#), [135](#), [142](#), [143](#),  
[144](#), [149](#), [150](#), [153](#), [154](#), [159](#), [166](#), [168](#), [169](#), [170](#),  
[171](#), [172](#), [173](#), [177](#), [178](#), [180](#), [181](#), [182](#), [186](#), [188](#),  
[190](#), [193](#), [194](#), [198](#), [199](#), [200](#), [201](#), [203](#), [204](#), [205](#),  
[207](#), [219](#), [221](#), [222](#), [223](#), [224](#), [225](#), [226](#), [227](#), [237](#),  
[246](#), [247](#), [256](#), [257](#), [259](#), [263](#), [265](#), [268](#), [272](#), [277](#),  
[286](#), [287](#), [306](#), [307](#), [316](#), [329](#), [330](#), [331](#), [332](#), [334](#),  
[362](#), [380](#), [381](#), [382](#), [388](#), [391](#), [392](#), [395](#), [396](#), [397](#),  
[398](#), [399](#), [400](#), [401](#), [402](#), [406](#), [410](#), [414](#), [417](#), [421](#),  
[460](#), [461](#), [462](#), [463](#), [468](#), [471](#), [474](#), [475](#), [482](#), [483](#),  
[485](#), [486](#), [487](#), [495](#), [506](#), [507](#), [535](#).

*ibl*: [266](#).

*iblock*: [267](#), [268](#).

*iclass*: [181](#).

*id*: [14](#), [15](#), [16](#), [17](#), [32](#), [33](#), [40](#).

*id\_cols*: [115](#), [117](#), [118](#).

*iden*: [17](#), [18](#), [19](#), [433](#), [436](#), [449](#), [452](#).

*idkrona*: [125](#).

**IF**: [6](#), [12](#), [13](#), [20](#).

*in*: [98](#), [99](#), [101](#), [102](#), [103](#), [110](#), [115](#), [117](#), [118](#), [119](#),  
[120](#), [121](#), [122](#), [123](#), [124](#), [213](#), [214](#), [240](#), [248](#), [253](#),  
[259](#), [285](#), [286](#), [298](#), [302](#), [309](#), [326](#), [475](#).

*in\_beg*: [214](#).

*in\_bl\_width*: [118](#).

*in\_end*: [214](#).

*in\_res*: [117](#).

*increase*: [24](#), [31](#).

*increment*: [213](#), [214](#), [215](#), [221](#), [222](#), [223](#), [224](#), [231](#),  
[233](#), [242](#), [250](#), [257](#), [259](#), [275](#), [282](#), [290](#), [292](#), [300](#),  
[304](#), [314](#), [317](#), [323](#), [327](#), [334](#), [478](#), [479](#), [483](#).

*ind*: [213](#), [239](#), [245](#), [271](#), [273](#), [274](#), [330](#), [357](#),  
[401](#), [475](#), [488](#), [525](#).

*ind\_map*: [460](#), [468](#).

**index**: [1](#), [214](#), [237](#), [239](#), [240](#), [245](#), [248](#), [252](#), [253](#),  
[259](#), [270](#), [271](#), [273](#), [274](#), [279](#), [280](#), [281](#), [285](#), [286](#),  
[298](#), [302](#), [309](#), [326](#), [327](#), [329](#), [330](#), [338](#), [339](#), [357](#),  
[384](#), [392](#), [401](#), [435](#), [438](#), [441](#), [442](#), [445](#), [446](#), [451](#),  
[454](#), [457](#), [475](#), [477](#), [488](#), [506](#), [525](#).

**indor**: [214](#), [215](#), [216](#).

**InducedSymmetries**: [134](#), [139](#), [148](#), [149](#),  
[150](#), [316](#), [413](#).

*ini*: [115](#), [117](#), [118](#).

*init*: [13](#), [14](#), [18](#), [20](#), [22](#), [38](#), [39](#), [43](#), [531](#), [534](#).

*insert*: [8](#), [14](#), [18](#), [28](#), [29](#), [159](#), [169](#), [174](#), [179](#), [343](#),  
[344](#), [345](#), [348](#), [356](#), [361](#), [363](#), [371](#), [372](#), [375](#), [381](#),  
[387](#), [389](#), [432](#), [435](#), [438](#), [442](#), [448](#), [451](#), [454](#),  
[493](#), [497](#), [500](#), [515](#), [516](#), [524](#), [527](#).

*installCounter*: [27](#), [29](#).

**INT\_MIN**: [72](#).

**INT\_SEQUENCE\_H**: [49](#).

*interkron*: [124](#).

**IntSequence**: [50](#), [51](#), [52](#), [53](#), [56](#), [57](#), [58](#), [59](#), [60](#),  
[61](#), [62](#), [63](#), [64](#), [65](#), [66](#), [67](#), [68](#), [69](#), [70](#), [71](#), [72](#), [73](#),  
[74](#), [75](#), [76](#), [77](#), [78](#), [94](#), [99](#), [126](#), [134](#), [135](#), [136](#),  
[142](#), [152](#), [154](#), [180](#), [181](#), [193](#), [194](#), [198](#), [199](#), [201](#),  
[205](#), [213](#), [214](#), [215](#), [216](#), [220](#), [221](#), [222](#), [223](#), [224](#),  
[225](#), [226](#), [227](#), [228](#), [231](#), [232](#), [233](#), [234](#), [237](#), [239](#),  
[240](#), [242](#), [243](#), [244](#), [245](#), [246](#), [247](#), [248](#), [250](#), [251](#),  
[252](#), [253](#), [256](#), [257](#), [258](#), [259](#), [260](#), [263](#), [266](#), [267](#),  
[269](#), [271](#), [272](#), [273](#), [275](#), [280](#), [281](#), [282](#), [284](#), [286](#),  
[290](#), [291](#), [292](#), [293](#), [298](#), [300](#), [301](#), [302](#), [304](#), [305](#),  
[309](#), [312](#), [313](#), [314](#), [316](#), [317](#), [322](#), [323](#), [325](#), [326](#),  
[327](#), [329](#), [330](#), [331](#), [332](#), [334](#), [335](#), [337](#), [338](#), [339](#),  
[341](#), [342](#), [343](#), [344](#), [345](#), [348](#), [351](#), [352](#), [353](#), [356](#),  
[357](#), [361](#), [362](#), [363](#), [384](#), [392](#), [395](#), [396](#), [401](#), [403](#),  
[404](#), [414](#), [417](#), [419](#), [421](#), [423](#), [424](#), [425](#), [427](#), [428](#),  
[433](#), [436](#), [437](#), [440](#), [444](#), [445](#), [446](#), [449](#), [452](#), [453](#),  
[456](#), [457](#), [460](#), [468](#), [471](#), [474](#), [475](#), [478](#), [479](#), [482](#),  
[483](#), [487](#), [488](#), [494](#), [495](#), [496](#), [506](#), [525](#).

*inverse*: [193](#), [199](#), [445](#), [446](#), [457](#), [525](#).

*io*: [214](#), [215](#), [216](#).

*ip*: [413](#), [415](#), [416](#), [419](#).

*iper*: [436](#), [452](#).

*irrows*: [99](#), [126](#), [127](#).

**IrregTensor**: [441](#), [478](#), [479](#), [487](#), [488](#).

**IrregTensorHeader**: [441](#), [478](#), [479](#), [482](#), [483](#),  
[485](#), [486](#), [487](#).

*irten*: [441](#).

*is*: [415](#), [416](#), [418](#).

*is\_zero*: [121](#).

*isConstant*: [50](#), [76](#), [474](#).

*isEnd*: [137](#), [138](#), [147](#), [432](#), [448](#).

*isfinite*: [348](#), [350](#).

*isFinite*: [343](#), [350](#), [375](#).

*isFull*: [135](#), [144](#), [445](#), [457](#).

*isPositive*: [50](#), [75](#), [252](#).

*isSorted*: [50](#), [77](#), [263](#), [356](#).

*istack*: [421](#).

*istacks*: [317](#), [414](#), [419](#).

*isUnit*: [99](#), [114](#), [120](#).

*isZero*: [121](#), [395](#), [397](#), [398](#), [414](#), [436](#), [445](#), [446](#),  
[452](#), [457](#).

*it*: [15](#), [16](#), [31](#), [149](#), [180](#), [182](#), [186](#), [239](#), [274](#), [349](#),  
[357](#), [371](#), [372](#), [373](#), [374](#), [376](#), [379](#), [380](#), [381](#), [382](#),  
[387](#), [388](#), [389](#), [391](#), [436](#), [441](#), [445](#), [446](#), [452](#),  
[457](#), [474](#), [488](#), [515](#), [516](#), [525](#), [527](#).

**Item**: [343](#), [348](#).

**iterator**: [8](#), [10](#), [11](#), [14](#), [15](#), [16](#), [28](#), [31](#), [154](#), [169](#),  
[343](#), [348](#), [349](#), [369](#), [370](#), [372](#), [373](#), [376](#).

- itper*: [181](#).
- itype**: [395](#), [396](#), [405](#), [406](#), [409](#), [410](#), [413](#), [415](#), [461](#).
- it2*: [525](#).
- iu*: [395](#), [401](#), [417](#).
- i1*: [50](#), [51](#), [69](#), [136](#), [157](#), [174](#), [185](#).
- i2*: [50](#), [51](#), [69](#), [136](#), [157](#), [174](#), [185](#).
- i3*: [136](#).
- i4*: [136](#).
- j*: [56](#), [58](#), [65](#), [67](#), [73](#), [74](#), [88](#), [117](#), [126](#), [129](#), [143](#), [178](#), [180](#), [181](#), [201](#), [203](#), [227](#), [278](#), [468](#), [475](#), [495](#), [496](#), [502](#), [535](#).
- jj*: [58](#), [59](#).
- join*: [10](#), [12](#), [37](#), [43](#).
- k*: [56](#), [203](#), [214](#), [219](#), [228](#), [388](#), [391](#), [493](#), [501](#), [503](#), [504](#), [530](#), [535](#), [536](#).
- kd*: [95](#), [96](#), [98](#), [106](#), [107](#), [108](#), [109](#).
- kdim*: [106](#), [107](#), [108](#), [109](#).
- key*: [348](#), [349](#), [351](#), [352](#), [353](#), [356](#), [357](#), [359](#).
- kmax*: [530](#), [535](#), [536](#).
- kp*: [98](#), [277](#), [287](#), [315](#), [318](#), [339](#), [388](#), [391](#), [436](#), [445](#), [446](#), [452](#), [457](#).
- kpa*: [101](#), [102](#), [103](#).
- kpd*: [98](#), [101](#), [102](#), [103](#), [110](#), [112](#), [113](#), [115](#), [116](#), [117](#), [118](#), [123](#), [124](#), [129](#), [130](#).
- kpiat*: [102](#), [116](#).
- KRON\_PROD\_H: [93](#).
- kronmult*: [475](#).
- kronMult*: [98](#), [111](#), [128](#), [306](#), [307](#), [487](#), [512](#), [524](#).
- KronProd**: [98](#), [99](#), [101](#), [102](#), [103](#), [110](#), [111](#), [116](#), [306](#), [307](#), [487](#), [512](#), [524](#).
- KronProdAI**: [93](#), [94](#), [98](#), [99](#), [102](#), [103](#), [116](#), [117](#), [118](#), [123](#).
- KronProdAll**: [1](#), [93](#), [94](#), [98](#), [99](#), [100](#), [101](#), [102](#), [103](#), [112](#), [113](#), [114](#), [119](#), [126](#), [277](#), [287](#), [315](#), [318](#), [339](#), [388](#), [421](#), [470](#).
- KronProdAllOptim**: [93](#), [94](#), [100](#), [129](#), [315](#), [318](#), [388](#), [391](#), [420](#), [421](#), [452](#).
- KronProdDimens**: [94](#), [95](#), [96](#), [98](#), [101](#), [102](#), [103](#), [106](#), [116](#).
- KronProdIA**: [93](#), [94](#), [98](#), [99](#), [101](#), [115](#), [125](#).
- KronProdIAI**: [93](#), [94](#), [98](#), [99](#), [102](#), [103](#), [116](#), [118](#), [124](#).
- KronProdStack**: [394](#), [420](#), [421](#), [436](#), [445](#), [446](#), [452](#), [457](#).
- kronv*: [524](#), [525](#).
- k1*: [14](#).
- k2*: [14](#).
- l*: [47](#), [51](#), [388](#), [391](#).
- last*: [119](#), [123](#), [124](#), [125](#), [126](#), [128](#), [183](#), [214](#), [215](#), [216](#), [286](#), [306](#), [307](#), [487](#), [499](#), [502](#).
- last\_cont*: [462](#).
- last\_nz\_row*: [343](#), [348](#).
- last\_pos*: [349](#), [357](#).
- last\_row*: [462](#).
- lastdim*: [475](#).
- lb*: [271](#), [272](#), [273](#), [361](#), [362](#).
- lb\_srt*: [330](#).
- lbi*: [271](#), [273](#), [330](#), [361](#).
- len*: [62](#), [136](#), [193](#), [401](#).
- length*: [50](#), [51](#), [52](#), [56](#), [57](#), [58](#), [59](#), [60](#), [65](#), [66](#), [68](#), [111](#), [128](#), [137](#), [153](#), [166](#), [168](#), [180](#), [181](#), [237](#), [247](#), [306](#), [307](#), [358](#), [482](#), [484](#), [486](#), [487](#), [492](#), [494](#), [502](#), [507](#), [526](#).
- less*: [53](#), [64](#), [252](#), [363](#).
- lessEq*: [53](#), [63](#), [271](#), [273](#), [330](#), [361](#).
- list**: [8](#), [28](#), [154](#), [155](#), [183](#), [184](#), [185](#), [186](#).
- lname*: [380](#), [381](#).
- lnum*: [47](#).
- lock*: [13](#), [14](#), [17](#), [18](#), [24](#), [25](#), [26](#), [38](#), [43](#).
- lock\_map*: [14](#), [18](#), [19](#).
- lower\_bound*: [271](#), [329](#), [330](#), [339](#), [348](#), [357](#), [361](#).
- lst*: [471](#), [474](#).
- ltmmkey**: [13](#), [14](#).
- ltmp*: [502](#).
- ltseq**: [342](#), [343](#).
- ltsym**: [368](#), [369](#).
- m*: [13](#), [14](#), [38](#), [43](#), [81](#), [82](#), [83](#), [84](#), [87](#), [89](#), [90](#), [99](#), [112](#), [130](#), [228](#), [343](#), [369](#), [380](#), [421](#).
- make\_pair*: [381](#).
- Map**: [343](#), [348](#).
- map**: [13](#), [14](#), [369](#), [381](#).
- mat*: [101](#), [102](#), [103](#), [115](#), [116](#), [117](#).
- MAT\_C\_DOUBLE: [88](#).
- MAT\_COMPRESSION\_NONE: [88](#).
- mat.t*: [81](#), [82](#), [88](#), [380](#).
- MAT\_T\_DOUBLE: [88](#).
- Mat\_VarCreate*: [88](#).
- Mat\_VarFree*: [88](#).
- Mat\_VarWrite*: [88](#).
- matio\_compression*: [88](#).
- MATIO\_MAJOR\_VERSION: [88](#).
- MATIO\_MINOR\_VERSION: [88](#).
- matlist*: [99](#), [112](#), [113](#), [114](#), [121](#), [122](#), [123](#), [124](#), [125](#), [127](#), [130](#).
- matrix*: [395](#), [397](#), [399](#), [401](#), [406](#), [410](#), [419](#), [421](#).
- matvar.t*: [88](#).
- max*: [187](#), [206](#), [460](#), [462](#), [464](#), [465](#), [468](#), [501](#).
- max\_parallel\_threads*: [8](#), [11](#), [28](#), [31](#), [35](#).
- maxdim*: [493](#), [494](#), [495](#), [496](#), [498](#), [499](#), [500](#), [501](#), [502](#), [519](#), [523](#), [524](#).
- memcpy*: [51](#), [60](#).
- mes*: [46](#), [47](#).
- message*: [47](#).
- min*: [62](#).



- mm*: [381](#).
- mmmap*: [17](#).
- mmkey**: [13](#), [14](#), [15](#), [16](#).
- mmval**: [14](#), [15](#).
- mom*: [524](#), [525](#).
- moms*: [520](#), [527](#).
- mom2*: [524](#).
- monotone*: [50](#), [66](#), [242](#), [300](#).
- mpt*: [31](#).
- mult*: [50](#), [69](#), [97](#), [98](#), [99](#), [101](#), [102](#), [103](#), [107](#), [108](#), [109](#), [115](#), [117](#), [118](#), [119](#), [122](#), [123](#), [124](#), [125](#), [264](#), [277](#), [287](#), [315](#), [316](#), [318](#), [328](#), [495](#), [501](#).
- multAndAdd*: [1](#), [3](#), [367](#), [383](#), [384](#), [388](#), [390](#), [391](#), [394](#), [403](#), [404](#), [431](#), [432](#), [438](#), [441](#), [442](#), [447](#), [448](#), [470](#).
- multAndAddSparse1*: [403](#), [404](#), [431](#), [435](#), [447](#), [451](#).
- multAndAddSparse2*: [403](#), [404](#), [431](#), [438](#), [442](#), [447](#), [448](#), [454](#).
- multAndAddSparse3*: [403](#), [431](#), [441](#).
- multAndAddSparse4*: [403](#), [431](#), [442](#).
- multAndAddStacks*: [403](#), [404](#), [433](#), [438](#), [439](#), [443](#), [445](#), [446](#), [449](#), [454](#), [455](#), [457](#).
- multaVec*: [247](#), [498](#), [499](#).
- multColumnAndAdd*: [344](#), [357](#), [441](#).
- multimap**: [341](#), [343](#).
- multRows*: [99](#), [126](#), [339](#).
- multVec*: [507](#).
- mut*: [18](#), [21](#), [22](#), [24](#), [25](#), [26](#).
- mutex*: [20](#), [39](#), [43](#).
- mutex\_int\_map**: [13](#), [14](#).
- mutex\_map**: [14](#), [17](#).
- mutex\_map\_t**: [17](#), [40](#).
- mutex\_traits**: [5](#), [13](#), [14](#), [17](#), [20](#), [21](#), [22](#), [24](#), [25](#), [26](#), [38](#), [43](#).
- mutmap*: [17](#), [18](#), [19](#).
- my\_cols*: [110](#).
- my\_rows*: [110](#).
- n*: [10](#), [51](#), [81](#), [99](#), [113](#), [154](#), [155](#), [156](#), [189](#), [194](#), [195](#), [203](#), [208](#), [209](#), [213](#), [214](#), [219](#), [369](#), [462](#), [530](#), [535](#), [536](#).
- nc*: [180](#), [343](#), [360](#), [364](#), [396](#), [460](#), [468](#).
- ncols*: [81](#), [82](#), [87](#), [88](#), [91](#), [97](#), [98](#), [110](#), [112](#), [115](#), [116](#), [117](#), [123](#), [124](#), [127](#), [247](#), [271](#), [273](#), [280](#), [298](#), [302](#), [315](#), [318](#), [327](#), [330](#), [339](#), [343](#), [351](#), [352](#), [358](#), [506](#), [524](#).
- new\_last*: [499](#).
- new\_nc*: [460](#), [468](#).
- newkronv*: [524](#).
- newlast*: [124](#), [128](#), [306](#), [307](#), [487](#), [502](#).
- ng*: [405](#), [407](#), [408](#).
- ngs*: [409](#), [411](#), [412](#).
- ngss*: [405](#), [407](#), [408](#).
- nmax*: [156](#), [187](#), [190](#), [195](#), [206](#), [209](#), [530](#), [535](#), [536](#).
- nn*: [369](#), [383](#), [384](#).
- nnc*: [343](#).
- nnr*: [343](#).
- no\_synchro**: [33](#).
- NORMAL\_MOMENTS\_H**: [518](#).
- NoSynchro**: [33](#).
- nothing*: [324](#), [336](#).
- NoThread**: [33](#).
- NoThreadGroup**: [33](#).
- noverk*: [214](#), [219](#), [220](#), [228](#), [238](#), [265](#), [495](#), [530](#), [535](#), [536](#).
- noverseq*: [214](#), [220](#), [352](#).
- noverseq\_ip*: [214](#), [220](#).
- nr*: [343](#), [348](#), [360](#), [364](#), [468](#), [493](#), [494](#), [500](#).
- nrows*: [81](#), [82](#), [87](#), [88](#), [91](#), [97](#), [98](#), [110](#), [112](#), [115](#), [116](#), [117](#), [123](#), [124](#), [237](#), [239](#), [240](#), [247](#), [248](#), [258](#), [260](#), [270](#), [271](#), [273](#), [274](#), [277](#), [279](#), [280](#), [281](#), [287](#), [315](#), [318](#), [322](#), [339](#), [343](#), [358](#), [361](#), [421](#), [493](#), [494](#), [497](#), [500](#), [502](#), [506](#), [507](#), [515](#), [516](#), [524](#).
- ns*: [174](#), [185](#), [396](#).
- nt*: [499](#).
- nu*: [405](#), [407](#), [408](#), [409](#), [411](#), [412](#).
- num*: [56](#), [57](#), [67](#), [81](#), [82](#), [87](#), [135](#), [144](#), [155](#), [157](#), [173](#), [180](#), [183](#), [214](#), [215](#), [216](#), [232](#), [234](#), [246](#), [258](#), [260](#), [266](#), [267](#), [277](#), [278](#), [286](#), [287](#), [370](#), [372](#), [373](#), [374](#), [375](#), [380](#), [381](#), [384](#), [387](#), [389](#), [392](#), [432](#), [433](#), [448](#), [449](#), [493](#), [503](#), [504](#).
- num\_conts*: [396](#).
- num\_one\_time*: [384](#), [386](#).
- numClasses*: [149](#), [154](#), [181](#), [185](#), [186](#), [382](#), [388](#), [391](#), [436](#), [441](#), [445](#), [446](#), [452](#), [457](#), [474](#), [526](#).
- numCols*: [81](#), [82](#).
- numConts*: [395](#), [396](#), [462](#), [463](#).
- numMatrices*: [419](#).
- numRefinements*: [460](#), [462](#).
- numRows*: [81](#), [82](#).
- numStacks*: [395](#), [396](#), [397](#), [399](#), [400](#), [401](#), [415](#), [432](#), [435](#), [438](#), [442](#), [445](#), [446](#), [448](#), [451](#), [454](#), [457](#).
- numSyms*: [316](#), [331](#), [332](#), [334](#).
- nv*: [215](#), [216](#), [221](#), [222](#), [225](#), [227](#), [228](#), [231](#), [232](#), [233](#), [234](#), [237](#), [239](#), [240](#), [242](#), [243](#), [244](#), [247](#), [248](#), [250](#), [251](#), [290](#), [291](#), [292](#), [293](#), [298](#), [300](#), [301](#), [302](#), [304](#), [305](#), [344](#), [354](#), [355](#), [356](#), [359](#), [360](#), [478](#), [482](#), [492](#), [493](#), [494](#), [500](#), [512](#), [524](#).
- nvar*: [231](#), [232](#), [233](#), [234](#), [237](#), [238](#), [239](#), [240](#), [245](#), [247](#), [248](#), [252](#), [256](#), [258](#), [260](#), [277](#), [287](#), [290](#), [291](#), [292](#), [293](#), [294](#), [295](#), [298](#), [302](#), [306](#), [309](#), [322](#), [344](#), [354](#), [355](#), [431](#), [441](#), [447](#), [500](#), [507](#), [531](#), [534](#).
- nvars*: [256](#), [313](#), [493](#), [494](#), [497](#), [502](#), [506](#), [515](#), [516](#).
- nvmax*: [256](#), [263](#), [264](#), [313](#), [327](#), [328](#), [333](#).
- nvmax\_part*: [327](#).



- nvtx*: [215](#), [223](#), [224](#), [226](#).
- nvs*: [256](#), [263](#), [265](#), [268](#), [313](#), [331](#).
- ny*: [405](#), [407](#), [408](#).
- off*: [327](#), [332](#), [334](#), [417](#).
- offset*: [213](#), [506](#).
- oper*: [100](#), [131](#), [436](#), [452](#).
- optimizeOrder*: [100](#), [129](#), [388](#), [391](#), [421](#), [436](#), [445](#), [452](#), [457](#).
- order*: [194](#), [202](#), [203](#), [205](#).
- OrdSequence**: [136](#), [153](#), [154](#), [159](#), [166](#), [167](#), [168](#), [169](#), [170](#), [171](#), [172](#), [173](#), [174](#), [179](#).
- origv*: [492](#), [512](#).
- os*: [413](#).
- ostreamstream**: [381](#).
- out*: [98](#), [99](#), [101](#), [102](#), [103](#), [110](#), [115](#), [117](#), [118](#), [119](#), [120](#), [121](#), [122](#), [125](#), [154](#), [180](#), [181](#), [257](#), [259](#), [277](#), [287](#), [314](#), [317](#), [326](#), [327](#), [338](#), [383](#), [384](#), [388](#), [390](#), [391](#), [392](#), [403](#), [404](#), [422](#), [423](#), [424](#), [425](#), [426](#), [427](#), [428](#), [431](#), [432](#), [433](#), [434](#), [435](#), [436](#), [437](#), [438](#), [439](#), [440](#), [441](#), [442](#), [443](#), [444](#), [445](#), [446](#), [447](#), [448](#), [449](#), [450](#), [451](#), [452](#), [453](#), [454](#), [455](#), [456](#), [457](#), [479](#), [488](#), [498](#), [499](#), [507](#).
- out\_bl\_width*: [118](#).
- out\_col*: [327](#).
- out\_resh*: [117](#).
- out\_row*: [339](#).
- outcol*: [247](#), [441](#).
- outi*: [115](#), [117](#), [118](#).
- outrun*: [327](#).
- outrun\_part*: [327](#).
- outten*: [422](#), [423](#), [424](#), [425](#), [426](#), [427](#), [428](#), [434](#), [437](#), [440](#), [444](#), [450](#), [453](#), [456](#).
- out1*: [439](#), [455](#).
- p*: [139](#), [142](#), [149](#), [193](#), [313](#), [315](#), [316](#), [318](#), [339](#), [413](#), [498](#).
- pair**: [13](#), [14](#), [343](#).
- PascalTriangle**: [530](#), [531](#), [534](#), [535](#), [536](#).
- pbundle*: [330](#), [436](#), [452](#), [531](#), [534](#).
- per*: [154](#), [181](#), [193](#), [313](#), [333](#), [413](#), [417](#), [436](#), [452](#), [474](#), [525](#).
- percoor*: [417](#), [436](#), [452](#).
- perindex*: [417](#), [474](#).
- permap*: [193](#), [198](#), [199](#), [200](#), [201](#).
- Permutation**: [100](#), [139](#), [149](#), [154](#), [181](#), [192](#), [193](#), [194](#), [198](#), [199](#), [200](#), [201](#), [202](#), [203](#), [205](#), [313](#), [315](#), [316](#), [318](#), [330](#), [339](#), [413](#), [433](#), [436](#), [445](#), [446](#), [449](#), [452](#), [457](#), [474](#), [525](#).
- PERMUTATION\_H**: [192](#).
- PermutationBundle**: [192](#), [195](#), [206](#), [207](#), [208](#), [209](#), [531](#), [534](#).
- PermutationSet**: [192](#), [194](#), [195](#), [202](#), [203](#), [204](#), [205](#), [208](#), [209](#), [330](#), [436](#), [452](#).
- perrun*: [327](#).
- pers*: [194](#), [202](#), [203](#), [204](#), [205](#).
- PerTensorDimens**: [313](#), [314](#), [315](#), [316](#), [318](#), [322](#), [339](#), [436](#), [452](#).
- PerTensorDimens2**: [311](#), [316](#), [317](#), [331](#), [332](#), [333](#), [337](#).
- pfirst*: [11](#).
- pindex*: [471](#), [475](#).
- pmonotone*: [50](#), [67](#), [275](#), [334](#).
- pol*: [506](#).
- pop\_front*: [9](#), [30](#), [183](#).
- pos*: [51](#), [59](#).
- posix*: [5](#), [12](#), [13](#), [20](#), [32](#), [35](#), [37](#), [38](#), [39](#), [41](#), [42](#).
- posix\_detach\_thread\_function*: [37](#), [42](#).
- posix\_mm*: [40](#).
- posix\_synchro**: [32](#), [40](#).
- posix\_thread\_function*: [37](#), [41](#).
- PosixSynchro**: [32](#), [40](#).
- PosixThread**: [32](#).
- PosixThreadGroup**: [32](#).
- pow*: [225](#), [226](#), [266](#).
- power*: [214](#), [219](#), [233](#), [306](#).
- PowerProvider**: [492](#), [494](#), [495](#), [498](#), [507](#), [512](#), [513](#), [514](#).
- pp*: [330](#), [494](#), [495](#), [496](#), [498](#), [507](#).
- prefix*: [153](#), [154](#), [155](#), [172](#), [182](#), [186](#), [228](#), [380](#), [381](#).
- print*: [47](#), [50](#), [78](#), [139](#), [150](#), [153](#), [154](#), [155](#), [172](#), [182](#), [186](#), [193](#), [213](#), [316](#), [333](#), [343](#), [344](#), [345](#), [353](#), [360](#), [364](#), [379](#).
- printf*: [47](#), [78](#), [150](#), [172](#), [182](#), [186](#), [213](#), [333](#), [353](#), [360](#), [364](#), [379](#).
- ps*: [194](#).
- PS\_TENSOR\_H**: [311](#).
- pset*: [330](#), [436](#), [452](#).
- ptd*: [314](#), [322](#).
- pthread\_attr\_destroy*: [37](#).
- pthread\_attr\_init*: [37](#).
- pthread\_attr\_setdetachstate*: [37](#).
- pthread\_attr\_t**: [37](#).
- pthread\_cond\_broadcast*: [39](#).
- pthread\_cond\_destroy*: [39](#).
- pthread\_cond\_init*: [39](#).
- pthread\_cond\_t**: [5](#), [20](#).
- pthread\_cond\_wait*: [39](#).
- pthread\_create*: [37](#).
- PTHREAD\_CREATE\_DETACHED**: [37](#).
- pthread\_exit*: [37](#).
- pthread\_join*: [37](#).
- pthread\_mutex\_init*: [38](#).
- pthread\_mutex\_lock*: [38](#).
- pthread\_mutex\_t**: [5](#), [13](#), [38](#).
- pthread\_mutex\_unlock*: [38](#).

**pthread\_t**: [5](#), [12](#).

*ptriang*: [219](#), [531](#), [534](#).

*push\_back*: [8](#), [29](#), [127](#), [148](#), [149](#), [173](#), [183](#), [185](#),  
[190](#), [205](#), [209](#), [392](#), [468](#), [475](#).

*pwp*: [495](#).

PYRAMID\_PROD\_H: [470](#).

PYRAMID\_PROD2\_H: [477](#).

*p1*: [193](#).

*p2*: [193](#).

*r*: [82](#), [97](#), [214](#), [215](#), [216](#), [232](#), [234](#), [258](#), [260](#), [343](#),  
[344](#), [345](#), [348](#), [354](#), [356](#), [357](#), [363](#).

*rbegin*: [184](#).

*ref\_conts*: [461](#), [462](#), [463](#).

*rem*: [11](#).

*remove*: [16](#), [19](#), [376](#).

*rend*: [184](#).

*res*: [26](#), [68](#), [69](#), [71](#), [72](#), [76](#), [77](#), [98](#), [111](#), [171](#), [205](#),  
[219](#), [225](#), [226](#), [265](#), [266](#), [350](#), [378](#), [382](#), [399](#),  
[401](#), [414](#), [486](#), [502](#).

**RET**: [6](#), [12](#), [13](#), [20](#).

*ret*: [10](#), [332](#), [419](#).

RFS\_TENSOR\_H: [289](#).

*rit*: [184](#).

*round*: [468](#).

*row*: [91](#), [126](#), [127](#), [128](#).

*row\_prod*: [339](#).

*rows*: [81](#), [82](#), [94](#), [95](#), [96](#), [97](#), [106](#), [107](#), [108](#), [109](#),  
[116](#), [129](#), [130](#), [493](#), [503](#), [504](#).

*rsizes*: [460](#), [468](#).

*rsym*: [382](#).

*run*: [5](#), [7](#), [8](#), [10](#), [11](#), [12](#), [27](#), [28](#), [31](#), [37](#), [43](#), [137](#),  
[271](#), [273](#), [329](#), [330](#), [339](#), [350](#), [353](#), [361](#), [378](#), [392](#),  
[432](#), [435](#), [438](#), [441](#), [442](#), [448](#), [451](#), [454](#).

*run\_portion*: [10](#), [11](#).

*r1*: [439](#), [455](#).

*r2*: [439](#), [455](#).

*s*: [50](#), [51](#), [52](#), [53](#), [58](#), [59](#), [60](#), [61](#), [62](#), [63](#), [64](#), [65](#),  
[67](#), [74](#), [130](#), [136](#), [137](#), [138](#), [139](#), [142](#), [148](#), [149](#),  
[153](#), [159](#), [167](#), [168](#), [169](#), [173](#), [179](#), [193](#), [194](#),  
[201](#), [205](#), [214](#), [220](#), [256](#), [312](#), [313](#), [343](#), [344](#),  
[345](#), [352](#), [363](#), [373](#), [374](#), [376](#), [382](#), [395](#), [396](#),  
[397](#), [398](#), [399](#), [400](#), [401](#), [406](#), [410](#), [422](#), [426](#),  
[434](#), [450](#), [460](#), [461](#), [468](#), [502](#).

*s\_offsets*: [272](#), [362](#).

*sa*: [167](#).

*save*: [82](#), [91](#).

*sc*: [413](#), [462](#), [464](#), [465](#).

*se*: [51](#), [56](#), [57](#).

*second*: [14](#), [15](#), [18](#), [19](#), [239](#), [271](#), [274](#), [314](#), [321](#), [329](#),  
[330](#), [339](#), [349](#), [350](#), [353](#), [357](#), [361](#), [371](#), [372](#), [373](#),  
[376](#), [377](#), [379](#), [380](#), [381](#), [387](#), [389](#), [515](#), [516](#), [527](#).

*selectEquiv*: [519](#), [525](#), [526](#).

*seq*: [214](#).

**seqit**: [154](#), [158](#), [159](#), [174](#), [177](#), [178](#), [179](#).

*setDimensionSizes*: [316](#), [331](#).

*setMat*: [99](#), [112](#), [277](#), [287](#), [388](#), [391](#), [421](#).

*setRC*: [97](#), [112](#), [113](#).

*setUnit*: [99](#), [113](#), [277](#), [287](#), [421](#).

*shift*: [245](#), [246](#), [252](#).

*shift\_pre*: [246](#).

*si*: [137](#), [177](#), [178](#), [179](#), [432](#), [448](#), [526](#).

*size*: [11](#), [52](#), [57](#), [58](#), [59](#), [61](#), [62](#), [63](#), [64](#), [70](#), [71](#), [72](#),  
[73](#), [74](#), [75](#), [76](#), [77](#), [78](#), [97](#), [126](#), [135](#), [136](#), [137](#),  
[142](#), [143](#), [145](#), [147](#), [150](#), [153](#), [154](#), [171](#), [172](#), [180](#),  
[181](#), [188](#), [189](#), [190](#), [193](#), [194](#), [198](#), [199](#), [200](#), [201](#),  
[202](#), [203](#), [204](#), [205](#), [207](#), [208](#), [209](#), [220](#), [221](#), [222](#),  
[223](#), [224](#), [225](#), [226](#), [227](#), [228](#), [242](#), [243](#), [244](#), [250](#),  
[251](#), [263](#), [265](#), [266](#), [267](#), [272](#), [275](#), [282](#), [284](#), [300](#),  
[301](#), [304](#), [305](#), [306](#), [316](#), [322](#), [323](#), [325](#), [329](#),  
[330](#), [332](#), [334](#), [343](#), [348](#), [356](#), [359](#), [362](#), [379](#),  
[396](#), [401](#), [402](#), [413](#), [414](#), [417](#), [419](#), [421](#), [436](#),  
[452](#), [460](#), [468](#), [474](#), [475](#), [478](#), [483](#).

**SizeRefinement**: [459](#), [460](#), [461](#), [462](#), [468](#).

*sl*: [329](#), [339](#).

*slice*: [436](#), [438](#), [439](#), [443](#), [452](#), [455](#), [495](#), [496](#).

*sort*: [50](#), [65](#), [201](#), [245](#), [252](#), [253](#), [286](#), [298](#), [309](#),  
[312](#), [329](#), [330](#), [357](#), [445](#), [446](#), [457](#), [488](#).

*sort\_per*: [445](#), [446](#), [454](#), [457](#).

**SortIntSequence**: [312](#), [313](#).

*sp*: [203](#), [258](#), [317](#), [421](#), [436](#), [441](#), [445](#), [446](#), [452](#),  
[457](#), [478](#), [482](#).

SPARSE\_TENSOR\_H: [341](#).

**SparseTensor**: [343](#), [344](#), [345](#), [348](#), [350](#), [351](#), [352](#),  
[353](#), [354](#), [355](#), [356](#), [360](#), [361](#), [363](#), [364](#).

*sparseTensor*: [344](#).

*sprintf*: [380](#).

*src*: [193](#), [198](#), [240](#), [248](#), [302](#), [338](#).

*srt*: [201](#).

*srun*: [329](#), [339](#).

*ss*: [138](#), [145](#), [256](#), [258](#), [260](#), [263](#), [271](#), [272](#), [273](#),  
[280](#), [281](#), [313](#), [314](#), [322](#), [329](#), [330](#), [345](#), [361](#),  
[362](#), [432](#), [448](#), [468](#), [494](#), [495](#), [496](#).

*stack\_cont*: [413](#), [414](#), [415](#), [416](#), [417](#), [418](#), [461](#), [462](#).

STACK\_CONTAINER\_H: [394](#).

*stack\_offsets*: [396](#), [401](#), [402](#).

*stack\_sizes*: [396](#), [399](#), [401](#), [402](#), [405](#), [409](#), [462](#).

**StackContainer**: [394](#), [396](#), [405](#), [409](#), [413](#), [421](#),  
[461](#), [462](#), [464](#), [465](#).

**StackContainerInterface**: [1](#), [394](#), [395](#), [396](#),  
[403](#), [404](#), [405](#), [409](#), [413](#), [420](#), [461](#).

**StackProduct**: [317](#), [394](#), [413](#), [420](#), [436](#), [441](#), [445](#),  
[446](#), [452](#), [454](#), [457](#), [478](#), [482](#).

*start*: [10](#), [384](#), [392](#).

*start\_col*: [351](#), [352](#), [353](#).

**std:** [5](#), [49](#), [152](#), [176](#), [341](#), [348](#), [350](#), [470](#).

**sthread:** [5](#), [32](#), [33](#), [35](#).

**STHREAD\_H:** [5](#).

**stl:** [369](#).

**str:** [381](#).

**strcat:** [186](#), [380](#).

**strcmp:** [14](#).

**strcpy:** [186](#).

**string:** [381](#).

**strncpy:** [47](#).

**su:** [329](#), [339](#).

**sub:** [111](#).

**subfrom:** [327](#).

**subit:** [138](#), [145](#), [146](#), [147](#).

**subout:** [327](#).

**subres:** [401](#).

**subs:** [138](#), [145](#), [146](#), [147](#).

**subt:** [506](#).

**subv:** [228](#), [266](#).

**sum:** [50](#), [68](#), [135](#), [143](#), [245](#), [252](#), [322](#), [506](#).

**sumcol:** [441](#).

**swaps:** [65](#), [129](#), [131](#).

**sy:** [51](#), [56](#), [57](#).

**sym:** [137](#), [138](#), [145](#), [147](#), [256](#), [263](#), [265](#), [344](#),  
[354](#), [355](#), [380](#), [381](#), [422](#), [426](#), [433](#), [434](#), [449](#),  
[450](#), [495](#), [496](#).

**sym\_left:** [277](#), [278](#), [287](#).

**sym\_right:** [277](#), [278](#), [287](#).

**symiterator:** [134](#), [137](#), [138](#), [145](#), [146](#), [147](#),  
[432](#), [448](#).

**Symmetry:** [50](#), [51](#), [56](#), [57](#), [67](#), [135](#), [136](#), [137](#), [138](#),  
[139](#), [142](#), [143](#), [144](#), [148](#), [149](#), [231](#), [233](#), [255](#), [256](#),  
[257](#), [259](#), [278](#), [290](#), [292](#), [311](#), [313](#), [316](#), [343](#), [344](#),  
[345](#), [352](#), [367](#), [368](#), [369](#), [373](#), [374](#), [376](#), [380](#), [381](#),  
[382](#), [395](#), [396](#), [397](#), [398](#), [399](#), [400](#), [401](#), [403](#), [404](#),  
[406](#), [410](#), [413](#), [422](#), [426](#), [434](#), [450](#), [461](#), [495](#),  
[496](#), [497](#), [498](#), [499](#), [501](#), [502](#), [506](#).

**SYMMETRY\_H:** [134](#).

**SymmetrySet:** [134](#), [137](#), [138](#), [145](#), [147](#), [432](#), [448](#).

**symrun:** [506](#).

**syms:** [316](#), [331](#), [332](#), [333](#), [413](#), [414](#), [415](#), [416](#), [417](#).

**syn:** [436](#), [445](#), [446](#), [452](#), [457](#).

**SYNCHRO:** [5](#), [32](#), [33](#), [436](#), [445](#), [446](#), [452](#), [457](#).

**synchro:** [5](#), [14](#), [16](#), [17](#), [32](#), [33](#).

**s1:** [174](#), [228](#), [342](#), [368](#).

**s2:** [174](#), [228](#), [342](#), [368](#).

**t:** [213](#), [214](#), [215](#), [216](#), [231](#), [232](#), [233](#), [234](#), [237](#), [239](#),  
[245](#), [247](#), [252](#), [258](#), [260](#), [271](#), [273](#), [274](#), [280](#), [281](#),  
[314](#), [318](#), [321](#), [322](#), [329](#), [330](#), [343](#), [344](#), [345](#), [355](#),  
[357](#), [361](#), [372](#), [375](#), [376](#), [383](#), [384](#), [388](#), [390](#), [391](#),  
[401](#), [403](#), [404](#), [423](#), [424](#), [425](#), [427](#), [428](#), [431](#), [435](#),  
[438](#), [441](#), [442](#), [447](#), [451](#), [454](#), [498](#), [500](#).

**T\_CONTAINER\_H:** [367](#).

**ta:** [167](#).

**tailIdentity:** [193](#), [200](#), [313](#), [327](#), [328](#).

**tailIdentitySize:** [314](#), [327](#), [328](#).

**tar:** [193](#), [198](#), [245](#), [252](#), [298](#), [309](#), [338](#).

**td:** [256](#), [258](#), [260](#), [271](#), [273](#), [280](#), [281](#), [313](#), [315](#),  
[316](#), [317](#), [318](#), [331](#), [332](#), [339](#), [345](#), [361](#).

**tdims:** [257](#), [258](#), [259](#), [260](#), [270](#), [271](#), [273](#), [274](#), [275](#),  
[277](#), [279](#), [280](#), [281](#), [282](#), [284](#), [286](#), [287](#), [314](#), [315](#),  
[317](#), [318](#), [322](#), [323](#), [325](#), [326](#), [327](#), [328](#), [334](#),  
[337](#), [338](#), [339](#), [345](#), [361](#), [363](#), [364](#).

**ten:** [371](#), [423](#), [424](#), [425](#), [427](#), [428](#), [437](#), [440](#), [444](#),  
[453](#), [456](#), [495](#), [496](#), [497](#), [501](#).

**tensor:** [213](#).

**Tensor:** [212](#), [214](#), [215](#), [216](#), [219](#), [220](#), [237](#), [245](#),  
[252](#), [265](#), [271](#), [273](#), [286](#), [306](#), [329](#), [330](#), [339](#), [344](#),  
[352](#), [357](#), [384](#), [392](#), [401](#), [435](#), [438](#), [441](#), [442](#), [445](#),  
[446](#), [451](#), [454](#), [457](#), [479](#), [487](#), [488](#), [495](#), [506](#), [525](#).

**TENSOR\_H:** [212](#).

**TensorContainer:** [367](#), [369](#), [371](#), [372](#), [383](#), [384](#),  
[387](#), [389](#), [395](#), [403](#), [404](#), [407](#), [408](#), [411](#), [412](#),  
[493](#), [494](#), [500](#), [519](#), [520](#), [523](#), [527](#).

**TensorDimens:** [256](#), [257](#), [258](#), [259](#), [260](#), [263](#),  
[264](#), [265](#), [266](#), [267](#), [271](#), [273](#), [277](#), [280](#), [281](#), [287](#),  
[313](#), [315](#), [316](#), [318](#), [331](#), [332](#), [339](#), [345](#), [361](#), [439](#),  
[443](#), [455](#), [471](#), [474](#), [495](#), [496](#), [506](#).

**TensorPolynomial:** [493](#), [494](#), [503](#), [504](#), [505](#),  
[506](#), [515](#), [516](#).

**th:** [7](#).

**Then:** [6](#).

**thread:** [5](#), [7](#), [8](#), [12](#), [27](#), [33](#).

**THREAD:** [5](#), [32](#), [33](#), [422](#), [423](#), [424](#), [425](#), [426](#), [427](#),  
[428](#), [432](#), [435](#), [438](#), [442](#), [448](#), [451](#), [454](#).

**thread\_group:** [5](#), [8](#), [9](#), [28](#), [33](#), [35](#).

**THREAD\_GROUP:** [5](#), [32](#), [33](#), [432](#), [435](#), [438](#), [442](#),  
[448](#), [451](#), [454](#).

**thread\_impl:** [5](#), [7](#), [8](#), [12](#), [13](#), [14](#), [17](#), [20](#), [21](#), [22](#),  
[23](#), [24](#), [25](#), [26](#), [27](#), [28](#).

**thread\_traits:** [5](#), [7](#), [8](#), [12](#), [27](#), [28](#), [37](#), [41](#), [42](#), [43](#).

**ti:** [270](#).

**tin:** [326](#).

**TL\_DEBUG:** [46](#).

**TL\_DEBUG\_EXCEPTION:** [46](#).

**TL\_EXCEPTION\_H:** [45](#).

**TL\_RAISE:** [46](#), [91](#), [189](#), [208](#), [324](#), [335](#), [336](#), [349](#),  
[373](#), [406](#), [410](#), [479](#), [536](#).

**TL\_RAISE\_IF:** [46](#), [57](#), [59](#), [60](#), [63](#), [64](#), [74](#), [106](#), [110](#),  
[111](#), [126](#), [166](#), [171](#), [177](#), [178](#), [180](#), [181](#), [198](#), [201](#),  
[203](#), [205](#), [237](#), [242](#), [243](#), [244](#), [245](#), [247](#), [250](#),  
[251](#), [252](#), [263](#), [266](#), [267](#), [271](#), [273](#), [275](#), [277](#),  
[282](#), [284](#), [287](#), [300](#), [301](#), [304](#), [305](#), [322](#), [323](#),  
[325](#), [326](#), [327](#), [330](#), [332](#), [334](#), [348](#), [356](#), [358](#),

- 359, 363, 373, 374, 375, 397, 398, 401, 414, 415, 417, 419, 421, 431, 432, 447, 448, 474, 483, 494, 500, 502, 507, 524, 536.
- TL\_STATIC\_H:** [529](#).
- TLException:** [46](#), [47](#), [375](#).
- tlist:* [8](#), [9](#), [11](#), [28](#), [29](#), [30](#), [31](#).
- tls:* [219](#), [330](#), [369](#), [372](#), [395](#), [436](#), [437](#), [452](#), [453](#), [529](#), [533](#).
- TLStatic:** [529](#), [531](#), [533](#), [534](#).
- tmp:* [186](#), [198](#), [205](#), [253](#), [277](#), [287](#), [380](#), [445](#), [446](#), [457](#), [488](#), [495](#).
- tmpcols:* [475](#).
- to:* [83](#), [84](#), [89](#), [90](#), [237](#).
- to\_col:* [90](#).
- to\_delete:* [126](#), [127](#).
- to\_row:* [89](#).
- tp:* [493](#), [494](#), [495](#), [496](#), [503](#), [504](#).
- tpart:* [247](#).
- trace:* [154](#), [180](#), [181](#), [193](#).
- true:* [22](#), [24](#), [25](#), [51](#), [56](#), [57](#), [58](#), [59](#), [60](#), [76](#), [77](#), [147](#), [170](#), [176](#), [184](#), [350](#), [526](#).
- ts:* [388](#), [391](#), [471](#), [474](#), [475](#).
- TWOD\_MATRIX\_H:** [80](#).
- TwoDMatrix:** [80](#), [81](#), [82](#), [83](#), [84](#), [87](#), [89](#), [90](#), [91](#), [98](#), [99](#), [101](#), [102](#), [103](#), [110](#), [112](#), [115](#), [117](#), [118](#), [119](#), [123](#), [124](#), [130](#), [214](#), [327](#), [421](#), [506](#), [519](#), [523](#), [524](#).
- ub:* [271](#), [272](#), [273](#), [361](#), [362](#).
- ub\_srt:* [330](#).
- ubi:* [271](#), [273](#), [330](#), [361](#).
- uc:* [383](#).
- UCompactPolynomial:** [508](#).
- UFSTensor:** [231](#), [232](#), [233](#), [234](#), [240](#), [241](#), [247](#), [248](#), [249](#), [250](#), [251](#), [252](#), [253](#), [260](#), [281](#), [289](#), [290](#), [291](#), [302](#), [336](#), [435](#), [445](#), [446](#), [451](#), [457](#), [503](#), [508](#), [515](#).
- ug:* [445](#).
- UGSContainer:** [1](#), [3](#), [383](#), [384](#), [387](#), [388](#), [389](#), [404](#), [426](#), [448](#), [450](#), [470](#).
- UGSTensor:** [230](#), [233](#), [252](#), [255](#), [257](#), [258](#), [259](#), [260](#), [270](#), [276](#), [279](#), [280](#), [281](#), [282](#), [283](#), [284](#), [285](#), [286](#), [287](#), [314](#), [327](#), [339](#), [383](#), [384](#), [387](#), [388](#), [390](#), [391](#), [394](#), [404](#), [408](#), [412](#), [426](#), [427](#), [428](#), [445](#), [447](#), [448](#), [449](#), [450](#), [451](#), [452](#), [453](#), [454](#), [455](#), [456](#), [457](#), [465](#), [503](#), [508](#), [515](#).
- ui:* [270](#), [279](#), [280](#), [281](#), [435](#), [445](#), [446](#), [451](#), [457](#).
- unfold:* [216](#), [231](#), [241](#), [257](#), [276](#), [292](#), [299](#), [317](#), [336](#).
- unfoldData:* [233](#), [248](#), [253](#), [259](#), [279](#), [280](#), [281](#), [285](#).
- unfolded:* [387](#).
- UnfoldedFineContainer:** [459](#), [465](#).
- UnfoldedGContainer:** [412](#).
- UnfoldedStackContainer:** [394](#), [404](#), [408](#), [412](#), [426](#), [427](#), [428](#), [430](#), [447](#), [448](#), [450](#), [451](#), [453](#), [454](#), [456](#), [457](#), [465](#).
- UnfoldedZContainer:** [394](#), [408](#).
- unit:* [395](#), [398](#), [400](#), [406](#), [410](#), [421](#).
- unit\_flag:* [478](#), [482](#), [484](#).
- unlock:* [13](#), [14](#), [17](#), [19](#), [24](#), [25](#), [26](#), [38](#), [43](#).
- unlock\_map:* [14](#), [18](#), [19](#).
- UNormalMoments:** [519](#), [520](#), [523](#), [524](#), [526](#), [527](#).
- unsort:* [330](#).
- up:* [503](#), [504](#), [516](#).
- upol:* [508](#).
- upper\_bound:* [271](#), [329](#), [330](#), [339](#), [349](#), [351](#), [352](#), [353](#), [357](#), [361](#).
- ups:* [388](#), [452](#), [457](#).
- UPSTensor:** [314](#), [315](#), [318](#), [321](#), [322](#), [323](#), [324](#), [325](#), [326](#), [327](#), [328](#), [329](#), [330](#), [388](#), [391](#), [436](#), [445](#), [452](#), [454](#), [457](#), [470](#).
- URSingleTensor:** [259](#), [287](#), [294](#), [295](#), [306](#), [307](#), [308](#), [309](#), [475](#), [492](#), [503](#), [508](#), [512](#), [515](#), [519](#), [523](#), [524](#).
- URTensor:** [290](#), [291](#), [293](#), [294](#), [295](#), [298](#), [299](#), [302](#), [303](#), [304](#), [305](#), [306](#), [307](#), [309](#), [470](#), [471](#), [474](#).
- USubTensor:** [471](#), [474](#), [475](#).
- ut:* [215](#), [232](#), [234](#), [240](#), [258](#), [260](#), [270](#), [291](#), [293](#), [294](#), [295](#), [298](#), [309](#), [314](#), [390](#), [492](#), [512](#), [513](#), [514](#).
- ut\_new:* [512](#).
- UTensor:** [215](#), [216](#), [221](#), [222](#), [223](#), [224](#), [225](#), [226](#), [231](#), [233](#), [234](#), [241](#), [242](#), [247](#), [248](#), [250](#), [251](#), [257](#), [259](#), [260](#), [275](#), [276](#), [279](#), [280](#), [281](#), [282](#), [284](#), [290](#), [291](#), [292](#), [299](#), [300](#), [302](#), [304](#), [305](#), [314](#), [315](#), [317](#), [322](#), [323](#), [325](#), [327](#), [334](#), [336](#).
- UTensorPolynomial:** [503](#), [504](#), [508](#), [515](#), [516](#).
- v:* [214](#), [215](#), [216](#), [221](#), [222](#), [223](#), [224](#), [225](#), [226](#), [227](#), [228](#), [231](#), [233](#), [242](#), [243](#), [244](#), [250](#), [251](#), [253](#), [256](#), [257](#), [259](#), [266](#), [267](#), [275](#), [282](#), [284](#), [286](#), [290](#), [292](#), [294](#), [300](#), [301](#), [304](#), [305](#), [307](#), [314](#), [317](#), [323](#), [325](#), [334](#), [335](#), [337](#), [344](#), [357](#), [478](#), [479](#), [483](#), [492](#), [498](#), [499](#), [502](#), [507](#), [519](#), [523](#), [524](#).
- value\_type:** [14](#), [348](#), [369](#).
- vars:* [493](#), [503](#), [504](#).
- vector:** [49](#), [51](#), [57](#), [126](#), [139](#), [152](#), [153](#), [156](#), [169](#), [170](#), [194](#), [195](#), [205](#), [294](#), [306](#), [330](#), [382](#), [384](#), [388](#), [391](#), [392](#), [460](#), [471](#), [474](#), [475](#).
- Vector:** [1](#), [89](#), [90](#), [98](#), [99](#), [111](#), [126](#), [127](#), [128](#), [247](#), [306](#), [307](#), [339](#), [344](#), [357](#), [395](#), [401](#), [417](#), [441](#), [475](#), [478](#), [482](#), [487](#), [492](#), [494](#), [498](#), [499](#), [503](#), [504](#), [507](#).
- vit:* [169](#), [170](#).
- vname:* [81](#), [82](#), [88](#).
- vs:* [417](#).
- vtmp:* [216](#), [269](#), [286](#), [298](#), [309](#), [326](#).

*v1*: [98](#), [111](#).  
*v2*: [98](#), [111](#).  
*wait*: [20](#), [26](#), [39](#), [43](#).  
*waitForChange*: [21](#), [26](#), [31](#).  
*worker*: [432](#), [435](#), [438](#), [442](#), [448](#), [451](#), [454](#).  
**WorkerFoldMAADense**: [403](#), [422](#), [432](#), [433](#),  
[434](#).  
**WorkerFoldMAASparse1**: [403](#), [423](#), [435](#),  
[436](#), [437](#).  
**WorkerFoldMAASparse2**: [403](#), [424](#), [438](#),  
[439](#), [440](#), [443](#).  
**WorkerFoldMAASparse4**: [403](#), [425](#), [442](#),  
[443](#), [444](#).  
**WorkerUnfoldMAADense**: [404](#), [426](#), [448](#),  
[449](#), [450](#).  
**WorkerUnfoldMAASparse1**: [404](#), [427](#), [451](#),  
[452](#), [453](#).  
**WorkerUnfoldMAASparse2**: [404](#), [428](#), [454](#),  
[455](#), [456](#).  
*writeMat*: [81](#), [82](#), [88](#), [380](#).  
*writeMMap*: [381](#).  
*x*: [232](#), [234](#), [237](#), [247](#).  
*xpow*: [495](#), [507](#).  
*xval*: [494](#), [495](#), [503](#), [504](#).  
*x1*: [507](#).  
*x1p*: [507](#).  
**ZContainer**: [3](#), [394](#), [405](#), [407](#), [408](#).  
*zero*: [395](#), [397](#), [406](#), [410](#), [421](#).  
*zeros*: [111](#), [117](#), [120](#), [121](#), [127](#), [237](#), [239](#), [247](#), [271](#),  
[273](#), [274](#), [298](#), [302](#), [309](#), [329](#), [330](#), [339](#), [441](#),  
[474](#), [497](#), [498](#), [499](#), [502](#), [506](#), [525](#).

- ⟨No threading specializations 33⟩ Used in section 5.
- ⟨POSIX specializations methods 36⟩ Used in section 35.
- ⟨POSIX thread specializations 32⟩ Used in section 5.
- ⟨apply  $F_n$  to *kronv* 525⟩ Used in section 524.
- ⟨body of `tl_exception` header 46⟩ Used in section 45.
- ⟨check compatibility of input parameters 358⟩ Used in section 357.
- ⟨check for zero trailing blocks 268⟩ Used in section 267.
- ⟨check that pair *key* and *r* is unique 349⟩ Used in section 348.
- ⟨check that *key* is within the range 359⟩ Used in section 357.
- ⟨classical IF template 6⟩ Used in section 5.
- ⟨decrease the non-zero block 269⟩ Used in section 267.
- ⟨do contraction for  $i = 0$  496⟩ Used in section 494.
- ⟨do contraction for all  $i > 0$  495⟩ Cited in section 496. Used in section 494.
- ⟨increment *i*-th item in coordinate *v* 484⟩ Used in section 483.
- ⟨index class definition 213⟩ Used in section 212.
- ⟨initialize *ten* of dimension *j* 497⟩ Cited in section 495. Used in sections 495 and 496.
- ⟨non-threading specialization methods 43⟩ Used in section 35.
- ⟨perform first multiplication AI 123⟩ Used in section 119.
- ⟨perform intermediate multiplications IAI 124⟩ Used in section 119.
- ⟨perform last multiplication IA 125⟩ Used in section 119.
- ⟨project the swap to the permutation *oper* 131⟩ Used in section 129.
- ⟨quick copy if product is unit 120⟩ Used in section 119.
- ⟨quick multiplication if dimension is 1 122⟩ Used in section 119.
- ⟨quick zero if one of the matrices is zero 121⟩ Used in section 119.
- ⟨set AI dimensions 107⟩ Used in section 106.
- ⟨set IA dimensions 108⟩ Used in section 106.
- ⟨set IAI dimensions 109⟩ Used in section 106.
- ⟨set shift for *addSubTensor* 246⟩ Used in sections 245 and 252.
- ⟨set *last* to product of *row* and *last* 128⟩ Used in section 126.
- ⟨set *lb* and *ub* to lower and upper bounds of indices 272⟩ Cited in section 362. Used in sections 271 and 273.
- ⟨set *lb* and *ub* to lower and upper bounds of slice indices 362⟩ Used in section 361.
- ⟨set *row* to the row of *j*-th matrix 127⟩ Used in section 126.
- ⟨set *sym\_left* and *sym\_right* to symmetries around *i* 278⟩ Used in sections 277 and 287.
- ⟨swap dimensions and matrices at *j* and  $j + 1$  130⟩ Used in section 129.
- ⟨**CompactPolynomial**::*eval* method code 507⟩ Used in section 505.
- ⟨**CompactPolynomial** class declaration 505⟩ Used in section 491.
- ⟨**CompactPolynomial** constructor code 506⟩ Used in section 505.
- ⟨**ConstTwoDMatrix**::*writeMat* code 88⟩ Used in section 86.
- ⟨**ConstTwoDMatrix** class declaration 81⟩ Cited in section 1. Used in section 80.
- ⟨**ConstTwoDMatrix** constructors 87⟩ Used in section 86.
- ⟨**Equivalence**::*findHaving* codes 177⟩ Used in section 163.
- ⟨**Equivalence**::*find* codes 178⟩ Used in section 163.
- ⟨**Equivalence**::*insert* code 179⟩ Used in section 163.
- ⟨**Equivalence**::**operator** $\equiv$  code 176⟩ Used in section 163.
- ⟨**Equivalence**::**operator** $=$  code 175⟩ Used in section 163.
- ⟨**Equivalence**::*print* code 182⟩ Used in section 163.
- ⟨**Equivalence**::*trace* code 180⟩ Used in section 163.
- ⟨**Equivalence**::*trace* permuted code 181⟩ Used in section 163.
- ⟨**EquivalenceBundle**::*generateUpTo* code 190⟩ Used in section 165.
- ⟨**EquivalenceBundle**::*get* code 189⟩ Used in section 165.
- ⟨**EquivalenceBundle** class declaration 156⟩ Used in section 152.
- ⟨**EquivalenceBundle** constructor code 187⟩ Used in section 165.



- ⟨ **EquivalenceBundle** destructor code 188 ⟩ Used in section 165.
- ⟨ **EquivalenceBundle** method codes 165 ⟩ Used in section 161.
- ⟨ **EquivalenceSet** :: *addParents* code 185 ⟩ Used in section 164.
- ⟨ **EquivalenceSet** :: *has* code 184 ⟩ Used in section 164.
- ⟨ **EquivalenceSet** :: *print* code 186 ⟩ Used in section 164.
- ⟨ **EquivalenceSet** class declaration 155 ⟩ Cited in section 1. Used in section 152.
- ⟨ **EquivalenceSet** constructor code 183 ⟩ Used in section 164.
- ⟨ **EquivalenceSet** method codes 164 ⟩ Used in section 161.
- ⟨ **Equivalence** begin and end methods 158 ⟩ Used in section 154.
- ⟨ **Equivalence** class declaration 154 ⟩ Cited in section 1. Used in section 152.
- ⟨ **Equivalence** constructors 157, 173 ⟩ Used in sections 154 and 163.
- ⟨ **Equivalence** copy constructors 174 ⟩ Used in section 163.
- ⟨ **Equivalence** method codes 163 ⟩ Used in section 161.
- ⟨ **Equivalence** protected methods 159 ⟩ Used in section 154.
- ⟨ **FCompactPolynomial** class declaration 509 ⟩ Used in section 491.
- ⟨ **FFSTensor** :: *addSubTensor* code 245 ⟩ Cited in section 252. Used in section 236.
- ⟨ **FFSTensor** :: *calcMaxOffset* code 238 ⟩ Used in section 236.
- ⟨ **FFSTensor** :: *decrement* code 243 ⟩ Used in section 236.
- ⟨ **FFSTensor** :: *getOffset* code 244 ⟩ Used in section 236.
- ⟨ **FFSTensor** :: *increment* code 242 ⟩ Used in section 236.
- ⟨ **FFSTensor** :: *unfold* code 241 ⟩ Used in section 236.
- ⟨ **FFSTensor** class declaration 231 ⟩ Cited in section 1. Used in section 230.
- ⟨ **FFSTensor** constructor declaration 232 ⟩ Used in section 231.
- ⟨ **FFSTensor** contraction constructor 237 ⟩ Cited in sections 232 and 247. Used in section 236.
- ⟨ **FFSTensor** conversion from sparse 239 ⟩ Used in section 236.
- ⟨ **FFSTensor** conversion from unfolded 240 ⟩ Used in section 236.
- ⟨ **FGSContainer** :: *getIndices* code 392 ⟩ Used in section 386.
- ⟨ **FGSContainer** :: *multAndAdd* folded code 390 ⟩ Cited in section 470. Used in section 386.
- ⟨ **FGSContainer** :: *multAndAdd* unfolded code 391 ⟩ Cited in section 470. Used in section 386.
- ⟨ **FGSContainer** class declaration 384 ⟩ Cited in section 1. Used in section 367.
- ⟨ **FGSContainer** conversion from **UGSContainer** 389 ⟩ Used in section 386.
- ⟨ **FGSTensor** :: *contractAndAdd* code 277 ⟩ Cited in section 287. Used in section 262.
- ⟨ **FGSTensor** :: *increment* code 275 ⟩ Used in section 262.
- ⟨ **FGSTensor** :: *unfold* code 276 ⟩ Used in section 262.
- ⟨ **FGSTensor** class declaration 257 ⟩ Cited in sections 1 and 231. Used in section 255.
- ⟨ **FGSTensor** constructor declarations 258 ⟩ Used in section 257.
- ⟨ **FGSTensor** conversion from **GSSparseTensor** 274 ⟩ Used in section 262.
- ⟨ **FGSTensor** conversion from **UGSTensor** 270 ⟩ Used in section 262.
- ⟨ **FGSTensor** slicing from **FFSTensor** 273 ⟩ Used in section 262.
- ⟨ **FGSTensor** slicing from **FSSparseTensor** 271 ⟩ Cited in sections 273 and 361. Used in section 262.
- ⟨ **FNormalMoments** class declaration 520 ⟩ Cited in section 1. Used in section 518.
- ⟨ **FNormalMoments** constructor code 527 ⟩ Used in section 522.
- ⟨ **FPSTensor** :: *addTo* code 338 ⟩ Used in section 320.
- ⟨ **FPSTensor** :: *decrement* code 335 ⟩ Used in section 320.
- ⟨ **FPSTensor** :: *getOffset* code 337 ⟩ Used in section 320.
- ⟨ **FPSTensor** :: *increment* code 334 ⟩ Used in section 320.
- ⟨ **FPSTensor** :: *unfold* code 336 ⟩ Used in section 320.
- ⟨ **FPSTensor** class declaration 317 ⟩ Cited in section 1. Used in section 311.
- ⟨ **FPSTensor** constructors 318 ⟩ Used in section 317.
- ⟨ **FPSTensor** sparse constructor 339 ⟩ Cited in sections 442 and 446. Used in section 320.
- ⟨ **FRSingleTensor** class declaration 295 ⟩ Cited in section 1. Used in section 289.
- ⟨ **FRSingleTensor** conversion from unfolded 309 ⟩ Used in section 297.

- ⟨ **FRTensor** :: *decrement* code 301 ⟩ Used in section 297.
- ⟨ **FRTensor** :: *increment* code 300 ⟩ Used in section 297.
- ⟨ **FRTensor** :: *unfold* code 299 ⟩ Used in section 297.
- ⟨ **FRTensor** class declaration 292 ⟩ Cited in section 1. Used in section 289.
- ⟨ **FRTensor** constructor declaration 293 ⟩ Used in section 292.
- ⟨ **FRTensor** conversion from unfolded 298 ⟩ Used in section 297.
- ⟨ **FSSparseTensor** :: *insert* code 356 ⟩ Used in section 347.
- ⟨ **FSSparseTensor** :: *multColumnAndAdd* code 357 ⟩ Used in section 347.
- ⟨ **FSSparseTensor** :: *print* code 360 ⟩ Used in section 347.
- ⟨ **FSSparseTensor** class declaration 344 ⟩ Cited in section 1. Used in section 341.
- ⟨ **FSSparseTensor** constructor code 354 ⟩ Used in section 347.
- ⟨ **FSSparseTensor** copy constructor code 355 ⟩ Used in section 347.
- ⟨ **FTensor** :: *decrement* code 227 ⟩ Used in section 218.
- ⟨ **FTensor** :: *getOffsetRecurse* code 228 ⟩ Used in section 218.
- ⟨ **FTensorPolynomial** class declaration 504 ⟩ Cited in section 1. Used in section 491.
- ⟨ **FTensorPolynomial** constructor conversion code 516 ⟩ Used in section 511.
- ⟨ **FTensor** class declaration 216 ⟩ Cited in section 1. Used in section 212.
- ⟨ **FineContainer** class declaration 461 ⟩ Used in section 459.
- ⟨ **FineContainer** constructor 462 ⟩ Used in section 461.
- ⟨ **FineContainer** destructor 463 ⟩ Used in section 461.
- ⟨ **FoldedFineContainer** class declaration 464 ⟩ Used in section 459.
- ⟨ **FoldedGContainer** class declaration 411 ⟩ Cited in section 1. Used in section 394.
- ⟨ **FoldedStackContainer** :: *multAndAddSparse1* code 435 ⟩ Used in section 430.
- ⟨ **FoldedStackContainer** :: *multAndAddSparse2* code 438 ⟩ Used in section 430.
- ⟨ **FoldedStackContainer** :: *multAndAddSparse3* code 441 ⟩ Used in section 430.
- ⟨ **FoldedStackContainer** :: *multAndAddSparse4* code 442 ⟩ Used in section 430.
- ⟨ **FoldedStackContainer** :: *multAndAddStacks* dense code 445 ⟩ Cited in section 446. Used in section 430.
- ⟨ **FoldedStackContainer** :: *multAndAddStacks* sparse code 446 ⟩ Cited in sections 442 and 455. Used in section 430.
- ⟨ **FoldedStackContainer** :: *multAndAdd* dense code 432 ⟩ Used in section 430.
- ⟨ **FoldedStackContainer** :: *multAndAdd* sparse code 431 ⟩ Used in section 430.
- ⟨ **FoldedStackContainer** class declaration 403 ⟩ Cited in section 1. Used in section 394.
- ⟨ **FoldedZContainer** class declaration 407 ⟩ Cited in section 1. Used in section 394.
- ⟨ **GContainer** :: *getType* code 410 ⟩ Used in section 409.
- ⟨ **GContainer** class declaration 409 ⟩ Cited in section 1. Used in section 394.
- ⟨ **GSSparseTensor** :: *insert* code 363 ⟩ Used in section 347.
- ⟨ **GSSparseTensor** :: *print* code 364 ⟩ Used in section 347.
- ⟨ **GSSparseTensor** class declaration 345 ⟩ Used in section 341.
- ⟨ **GSSparseTensor** slicing constructor 361 ⟩ Used in section 347.
- ⟨ **InducedSymmetries** :: *print* code 150 ⟩ Used in section 141.
- ⟨ **InducedSymmetries** class declaration 139 ⟩ Used in section 134.
- ⟨ **InducedSymmetries** constructor code 148 ⟩ Used in section 141.
- ⟨ **InducedSymmetries** permuted constructor code 149 ⟩ Used in section 141.
- ⟨ **IntSequence** :: *add* code 1 73 ⟩ Used in section 55.
- ⟨ **IntSequence** :: *add* code 2 74 ⟩ Used in section 55.
- ⟨ **IntSequence** :: *getMax* code 72 ⟩ Used in section 55.
- ⟨ **IntSequence** :: *getNumDistinct* code 71 ⟩ Used in section 55.
- ⟨ **IntSequence** :: *getPrefixLength* code 70 ⟩ Used in section 55.
- ⟨ **IntSequence** :: *isConstant* code 76 ⟩ Used in section 55.
- ⟨ **IntSequence** :: *isPositive* code 75 ⟩ Used in section 55.
- ⟨ **IntSequence** :: *isSorted* code 77 ⟩ Used in section 55.
- ⟨ **IntSequence** :: *lessEq* code 63 ⟩ Used in section 55.



- ⟨ **IntSequence**::*less* code 64 ⟩ Used in section 55.
- ⟨ **IntSequence**::*monotone* code 66 ⟩ Used in section 55.
- ⟨ **IntSequence**::*mult* code 69 ⟩ Used in section 55.
- ⟨ **IntSequence**::**operator** < code 62 ⟩ Used in section 55.
- ⟨ **IntSequence**::**operator**≡ code 61 ⟩ Used in section 55.
- ⟨ **IntSequence**::**operator**= code 60 ⟩ Used in section 55.
- ⟨ **IntSequence**::*pmonotone* code 67 ⟩ Used in section 55.
- ⟨ **IntSequence**::*print* code 78 ⟩ Used in section 55.
- ⟨ **IntSequence**::*sort* code 65 ⟩ Used in section 55.
- ⟨ **IntSequence**::*sum* code 68 ⟩ Used in section 55.
- ⟨ **IntSequence** class declaration 50 ⟩ Cited in section 1. Used in section 49.
- ⟨ **IntSequence** constructor code 1 56 ⟩ Used in section 55.
- ⟨ **IntSequence** constructor code 2 57 ⟩ Used in section 55.
- ⟨ **IntSequence** constructor code 3 58 ⟩ Used in section 55.
- ⟨ **IntSequence** constructor code 4 59 ⟩ Used in section 55.
- ⟨ **IntSequence** constructors 51 ⟩ Used in section 50.
- ⟨ **IntSequence** inlines and operators 52 ⟩ Used in section 50.
- ⟨ **IntSequence** orderings 53 ⟩ Used in section 50.
- ⟨ **IrregTensor**::*addTo* code 488 ⟩ Used in section 481.
- ⟨ **IrregTensorHeader**::*calcMaxOffset* code 486 ⟩ Used in section 481.
- ⟨ **IrregTensorHeader**::*increment* code 483 ⟩ Used in section 481.
- ⟨ **IrregTensorHeader** class declaration 478 ⟩ Used in section 477.
- ⟨ **IrregTensorHeader** constructor code 482 ⟩ Used in section 481.
- ⟨ **IrregTensorHeader** destructor code 485 ⟩ Used in section 481.
- ⟨ **IrregTensor** class declaration 479 ⟩ Cited in section 1. Used in section 477.
- ⟨ **IrregTensor** constructor code 487 ⟩ Used in section 481.
- ⟨ **KronProd**::*checkDimForMult* code 110 ⟩ Used in section 105.
- ⟨ **KronProd**::*kronMult* code 111 ⟩ Used in section 105.
- ⟨ **KronProdAI**::*mult* code 117 ⟩ Used in section 105.
- ⟨ **KronProdAI** class declaration 102 ⟩ Used in section 93.
- ⟨ **KronProdAI** constructor code 116 ⟩ Used in section 105.
- ⟨ **KronProdAll**::*isUnit* code 114 ⟩ Used in section 105.
- ⟨ **KronProdAll**::*multRows* code 126 ⟩ Used in section 105.
- ⟨ **KronProdAll**::*mult* code 119 ⟩ Used in section 105.
- ⟨ **KronProdAll**::*setMat* code 112 ⟩ Used in section 105.
- ⟨ **KronProdAll**::*setUnit* code 113 ⟩ Used in section 105.
- ⟨ **KronProdAllOptim**::*optimizeOrder* code 129 ⟩ Used in section 105.
- ⟨ **KronProdAllOptim** class declaration 100 ⟩ Cited in sections 1 and 129. Used in section 93.
- ⟨ **KronProdAll** class declaration 99 ⟩ Cited in section 1. Used in section 93.
- ⟨ **KronProdDimens** class declaration 94 ⟩ Used in section 93.
- ⟨ **KronProdDimens** constructor code 106 ⟩ Used in section 105.
- ⟨ **KronProdDimens** constructors 95 ⟩ Used in section 94.
- ⟨ **KronProdDimens** inline methods 97 ⟩ Used in section 94.
- ⟨ **KronProdDimens** inline operators 96 ⟩ Used in section 94.
- ⟨ **KronProdIA**::*mult* code 115 ⟩ Used in section 105.
- ⟨ **KronProdIAI**::*mult* code 118 ⟩ Used in section 105.
- ⟨ **KronProdIAI** class declaration 103 ⟩ Used in section 93.
- ⟨ **KronProdIA** class declaration 101 ⟩ Used in section 93.
- ⟨ **KronProdStack** class declaration 420 ⟩ Used in section 394.
- ⟨ **KronProdStack** constructor code 421 ⟩ Used in section 420.
- ⟨ **KronProd** class declaration 98 ⟩ Used in section 93.
- ⟨ **OrdSequence**::*add* codes 169 ⟩ Used in section 162.

< **OrdSequence** :: *average* ( ) code 171 > Used in section 162.  
 < **OrdSequence** :: *has* code 170 > Used in section 162.  
 < **OrdSequence** :: *operator* < code 167 > Used in section 162.  
 < **OrdSequence** :: *operator* ≡ code 168 > Used in section 162.  
 < **OrdSequence** :: *operator* [] code 166 > Used in section 162.  
 < **OrdSequence** :: *print* code 172 > Used in section 162.  
 < **OrdSequence** class declaration 153 > Used in section 152.  
 < **OrdSequence** method codes 162 > Used in section 161.  
 < **PascalTriangle** :: *noverk* code 536 > Used in section 533.  
 < **PascalTriangle** class declaration 530 > Used in section 529.  
 < **PascalTriangle** constructor code 535 > Used in section 533.  
 < **PerTensorDimens2** :: *calcOffset* code 332 > Used in section 320.  
 < **PerTensorDimens2** :: *print* code 333 > Used in section 320.  
 < **PerTensorDimens2** :: *setDimensionSizes* code 331 > Used in section 320.  
 < **PerTensorDimens2** class declaration 316 > Used in section 311.  
 < **PerTensorDimens** class declaration 313 > Used in section 311.  
 < **Permutation** :: *apply* code 198 > Used in section 197.  
 < **Permutation** :: *computeSortingMap* code 201 > Used in section 197.  
 < **Permutation** :: *inverse* code 199 > Used in section 197.  
 < **Permutation** :: *tailIdentity* code 200 > Used in section 197.  
 < **PermutationBundle** :: *generateUpTo* code 209 > Used in section 197.  
 < **PermutationBundle** :: *get* code 208 > Used in section 197.  
 < **PermutationBundle** class declaration 195 > Used in section 192.  
 < **PermutationBundle** constructor code 206 > Used in section 197.  
 < **PermutationBundle** destructor code 207 > Used in section 197.  
 < **PermutationSet** :: *getPreserving* code 205 > Used in section 197.  
 < **PermutationSet** class declaration 194 > Used in section 192.  
 < **PermutationSet** constructor code 1 202 > Used in section 197.  
 < **PermutationSet** constructor code 2 203 > Used in section 197.  
 < **PermutationSet** destructor code 204 > Used in section 197.  
 < **Permutation** class declaration 193 > Cited in section 1. Used in section 192.  
 < **PosixSynchro** constructor 40 > Used in section 36.  
 < **PowerProvider** :: *getNext* folded code 513 > Used in section 511.  
 < **PowerProvider** :: *getNext* unfolded code 512 > Used in section 511.  
 < **PowerProvider** class declaration 492 > Used in section 491.  
 < **PowerProvider** destructor code 514 > Used in section 511.  
 < **SizeRefinement** class declaration 460 > Used in section 459.  
 < **SizeRefinement** constructor code 468 > Used in section 467.  
 < **SortIntSequence** class declaration 312 > Used in section 311.  
 < **SparseTensor** :: *getFoldIndexFillFactor* code 351 > Used in section 347.  
 < **SparseTensor** :: *getUnfoldIndexFillFactor* code 352 > Used in section 347.  
 < **SparseTensor** :: *insert* code 348 > Used in section 347.  
 < **SparseTensor** :: *isFinite* code 350 > Used in section 347.  
 < **SparseTensor** :: *print* code 353 > Used in section 347.  
 < **SparseTensor** class declaration 343 > Used in section 341.  
 < **StackContainer** :: *calculateOffsets* code 402 > Used in section 396.  
 < **StackContainer** :: *createPackedColumn* code 401 > Used in section 396.  
 < **StackContainer** :: *getLengthOfMatrixStacks* code 399 > Used in section 396.  
 < **StackContainer** :: *getMatrix* code 398 > Used in section 396.  
 < **StackContainer** :: *getUnitPos* code 400 > Used in section 396.  
 < **StackContainer** :: *isZero* code 397 > Used in section 396.  
 < **StackContainerInterface** class declaration 395 > Cited in section 1. Used in section 394.

< **StackContainer** class declaration 396 > Used in section 394.  
 < **StackProduct**::*createPackedColumns* code 417 > Used in section 413.  
 < **StackProduct**::*getMatrix* code 416 > Used in section 413.  
 < **StackProduct**::*getSize* code 418 > Used in section 413.  
 < **StackProduct**::*getType* code 415 > Used in section 413.  
 < **StackProduct**::*isZero* code 414 > Used in section 413.  
 < **StackProduct**::*numMatrices* code 419 > Used in section 413.  
 < **StackProduct** class declaration 413 > Used in section 394.  
 < **Symmetry**::*findClass* code 143 > Used in section 141.  
 < **Symmetry**::*isFull* code 144 > Used in section 141.  
 < **SymmetrySet** class declaration 137 > Cited in section 138. Used in section 134.  
 < **Symmetry** class declaration 135 > Cited in section 1. Used in section 134.  
 < **Symmetry** constructor code 142 > Used in section 141.  
 < **Symmetry** constructors 136 > Used in section 135.  
 < **TLException** class definition 47 > Cited in section 1. Used in section 46.  
 < **TLStatic** class declaration 531 > Cited in section 1. Used in section 529.  
 < **TLStatic** methods 534 > Used in section 533.  
 < **Tensor**::*noverseq\_ip* static method 220 > Used in section 218.  
 < **TensorContainer**::*check* code 374 > Used in section 369.  
 < **TensorContainer**::*clear* code 377 > Used in section 369.  
 < **TensorContainer**::*fetchTensors* code 382 > Used in section 369.  
 < **TensorContainer**::*getMaxDim* code 378 > Used in section 369.  
 < **TensorContainer**::*insert* code 375 > Used in section 369.  
 < **TensorContainer**::*print* code 379 > Used in section 369.  
 < **TensorContainer**::*remove* code 376 > Used in section 369.  
 < **TensorContainer**::*writeMMap* code 381 > Used in section 369.  
 < **TensorContainer**::*writeMat* code 380 > Used in section 369.  
 < **TensorContainer** : *get* code 373 > Used in section 369.  
 < **TensorContainer** class definition 369 > Used in section 367.  
 < **TensorContainer** copy constructor 371 > Used in section 369.  
 < **TensorContainer** inline methods 370 > Used in section 369.  
 < **TensorContainer** subtensor constructor 372 > Used in section 369.  
 < **TensorDimens**::*calcFoldMaxOffset* code 265 > Used in section 262.  
 < **TensorDimens**::*calcFoldOffset* code 266 > Used in section 262.  
 < **TensorDimens**::*calcUnfoldMaxOffset* code 264 > Used in section 262.  
 < **TensorDimens**::*decrement* code 267 > Used in section 262.  
 < **TensorDimens** class declaration 256 > Cited in section 263. Used in section 255.  
 < **TensorDimens** constructor code 263 > Used in section 262.  
 < **TensorPolynomial**::*derivative* code 501 > Cited in section 493. Used in section 493.  
 < **TensorPolynomial**::*evalHorner* code 499 > Used in section 493.  
 < **TensorPolynomial**::*evalPartially* code 502 > Cited in section 493. Used in section 493.  
 < **TensorPolynomial**::*evalTrad* code 498 > Used in section 493.  
 < **TensorPolynomial**::*insert* code 500 > Used in section 493.  
 < **TensorPolynomial** class declaration 493 > Used in section 491.  
 < **TensorPolynomial** contract constructor code 494 > Used in section 493.  
 < **Tensor** class declaration 214 > Cited in section 1. Used in section 212.  
 < **Tensor** static methods 219 > Used in section 218.  
 < **TwoDMatrix**::*save* code 91 > Used in section 86.  
 < **TwoDMatrix** class declaration 82 > Cited in section 1. Used in section 80.  
 < **TwoDMatrix** column methods code 90 > Used in section 86.  
 < **TwoDMatrix** column methods declarations 84 > Used in section 82.  
 < **TwoDMatrix** row methods code 89 > Used in section 86.

< **TwoDMatrix** row methods declarations 83 > Used in section 82.  
 < **UCompactPolynomial** class declaration 508 > Used in section 491.  
 < **UFSTensor**::*addSubTensor* code 252 > Used in section 236.  
 < **UFSTensor**::*fold* code 249 > Used in section 236.  
 < **UFSTensor**::*getOffset* code 251 > Used in section 236.  
 < **UFSTensor**::*unfoldData* code 253 > Used in section 236.  
 < **UFSTensor** class declaration 233 > Cited in section 1. Used in section 230.  
 < **UFSTensor** constructor declaration 234 > Used in section 233.  
 < **UFSTensor** contraction constructor 247 > Used in section 236.  
 < **UFSTensor** conversion from folded 248 > Used in section 236.  
 < **UFSTensor** increment and decrement 250 > Used in section 236.  
 < **UGSContainer**::*multAndAdd* code 388 > Cited in section 391. Used in section 386.  
 < **UGSContainer** class declaration 383 > Cited in section 1. Used in section 367.  
 < **UGSContainer** conversion from **FGSContainer** 387 > Used in section 386.  
 < **UGSTensor**::*contractAndAdd* code 287 > Used in section 262.  
 < **UGSTensor**::*fold* code 283 > Used in section 262.  
 < **UGSTensor**::*getFirstIndexOf* code 286 > Used in section 262.  
 < **UGSTensor**::*getOffset* code 284 > Used in section 262.  
 < **UGSTensor**::*unfoldData* code 285 > Used in section 262.  
 < **UGSTensor** class declaration 259 > Cited in section 1. Used in section 255.  
 < **UGSTensor** constructor declarations 260 > Used in section 259.  
 < **UGSTensor** conversion from **FGSTensor** 279 > Used in section 262.  
 < **UGSTensor** increment and decrement codes 282 > Used in section 262.  
 < **UGSTensor** slicing from **FSSparseTensor** 280 > Used in section 262.  
 < **UGSTensor** slicing from **UFSTensor** 281 > Used in section 262.  
 < **UNormalMoments**::*generateMoments* code 524 > Used in section 522.  
 < **UNormalMoments**::*selectEquiv* code 526 > Used in section 522.  
 < **UNormalMoments** class declaration 519 > Cited in section 1. Used in section 518.  
 < **UNormalMoments** constructor code 523 > Used in section 522.  
 < **UPSTensor**::*addTo* folded code 326 > Used in section 320.  
 < **UPSTensor**::*addTo* unfolded code 327 > Used in section 320.  
 < **UPSTensor**::*decideFillMethod* code 321 > Used in section 320.  
 < **UPSTensor**::*fillFromSparseOne* code 329 > Used in section 320.  
 < **UPSTensor**::*fillFromSparseTwo* code 330 > Used in section 320.  
 < **UPSTensor**::*fold* code 324 > Used in section 320.  
 < **UPSTensor**::*getOffset* code 325 > Used in section 320.  
 < **UPSTensor**::*tailIdentitySize* code 328 > Used in section 320.  
 < **UPSTensor** class declaration 314 > Cited in section 1. Used in section 311.  
 < **UPSTensor** constructors from Kronecker product 315 > Used in section 314.  
 < **UPSTensor** increment and decrement 323 > Used in section 320.  
 < **UPSTensor** slicing constructor code 322 > Used in section 320.  
 < **URSingleTensor**::*fold* code 308 > Used in section 297.  
 < **URSingleTensor** class declaration 294 > Cited in section 1. Used in section 289.  
 < **URSingleTensor** constructor 1 code 306 > Used in section 297.  
 < **URSingleTensor** constructor 2 code 307 > Used in section 297.  
 < **URTensor**::*fold* code 303 > Used in section 297.  
 < **URTensor**::*getOffset* code 305 > Used in section 297.  
 < **URTensor** class declaration 290 > Cited in section 1. Used in section 289.  
 < **URTensor** constructor declaration 291 > Used in section 290.  
 < **URTensor** conversion from folded 302 > Used in section 297.  
 < **URTensor** increment and decrement 304 > Used in section 297.  
 < **USubTensor**::*addKronColumn* code 475 > Used in section 473.

- ⟨ **USubTensor** class declaration 471 ⟩ Cited in section 1. Used in section 470.
- ⟨ **USubTensor** constructor code 474 ⟩ Used in section 473.
- ⟨ **UTensor** :: *decrement* code 1 222 ⟩ Used in section 218.
- ⟨ **UTensor** :: *decrement* code 2 224 ⟩ Used in section 218.
- ⟨ **UTensor** :: *getOffset* code 1 225 ⟩ Used in section 218.
- ⟨ **UTensor** :: *getOffset* code 2 226 ⟩ Used in section 218.
- ⟨ **UTensor** :: *increment* code 1 221 ⟩ Used in section 218.
- ⟨ **UTensor** :: *increment* code 2 223 ⟩ Used in section 218.
- ⟨ **UTensorPolynomial** class declaration 503 ⟩ Cited in section 1. Used in section 491.
- ⟨ **UTensorPolynomial** constructor conversion code 515 ⟩ Used in section 511.
- ⟨ **UTensor** class declaration 215 ⟩ Cited in section 1. Used in section 212.
- ⟨ **UnfoldedFineContainer** class declaration 465 ⟩ Used in section 459.
- ⟨ **UnfoldedGContainer** class declaration 412 ⟩ Cited in section 1. Used in section 394.
- ⟨ **UnfoldedStackContainer** :: *multAndAddSparse1* code 451 ⟩ Cited in sections 435 and 452. Used in section 430.
- ⟨ **UnfoldedStackContainer** :: *multAndAddSparse2* code 454 ⟩ Cited in sections 438, 454, and 455. Used in section 430.
- ⟨ **UnfoldedStackContainer** :: *multAndAddStacks* code 457 ⟩ Cited in section 445. Used in section 430.
- ⟨ **UnfoldedStackContainer** :: *multAndAdd* dense code 448 ⟩ Cited in section 432. Used in section 430.
- ⟨ **UnfoldedStackContainer** :: *multAndAdd* sparse code 447 ⟩ Used in section 430.
- ⟨ **UnfoldedStackContainer** class declaration 404 ⟩ Cited in section 1. Used in section 394.
- ⟨ **UnfoldedZContainer** class declaration 408 ⟩ Cited in section 1. Used in section 394.
- ⟨ **WorkerFoldMAADense** :: *operator*()() code 433 ⟩ Used in section 430.
- ⟨ **WorkerFoldMAADense** class declaration 422 ⟩ Used in section 394.
- ⟨ **WorkerFoldMAADense** constructor code 434 ⟩ Used in section 430.
- ⟨ **WorkerFoldMAASparse1** :: *operator*()() code 436 ⟩ Used in section 430.
- ⟨ **WorkerFoldMAASparse1** class declaration 423 ⟩ Used in section 394.
- ⟨ **WorkerFoldMAASparse1** constructor code 437 ⟩ Used in section 430.
- ⟨ **WorkerFoldMAASparse2** :: *operator*()() code 439 ⟩ Cited in section 455. Used in section 430.
- ⟨ **WorkerFoldMAASparse2** class declaration 424 ⟩ Used in section 394.
- ⟨ **WorkerFoldMAASparse2** constructor code 440 ⟩ Used in section 430.
- ⟨ **WorkerFoldMAASparse4** :: *operator*()() code 443 ⟩ Used in section 430.
- ⟨ **WorkerFoldMAASparse4** class declaration 425 ⟩ Used in section 394.
- ⟨ **WorkerFoldMAASparse4** constructor code 444 ⟩ Used in section 430.
- ⟨ **WorkerUnfoldMAADense** :: *operator*()() code 449 ⟩ Cited in section 433. Used in section 430.
- ⟨ **WorkerUnfoldMAADense** class declaration 426 ⟩ Used in section 394.
- ⟨ **WorkerUnfoldMAADense** constructor code 450 ⟩ Used in section 430.
- ⟨ **WorkerUnfoldMAASparse1** :: *operator*()() code 452 ⟩ Cited in section 436. Used in section 430.
- ⟨ **WorkerUnfoldMAASparse1** class declaration 427 ⟩ Used in section 394.
- ⟨ **WorkerUnfoldMAASparse1** constructor code 453 ⟩ Used in section 430.
- ⟨ **WorkerUnfoldMAASparse2** :: *operator*()() code 455 ⟩ Used in section 430.
- ⟨ **WorkerUnfoldMAASparse2** class declaration 428 ⟩ Used in section 394.
- ⟨ **WorkerUnfoldMAASparse2** constructor code 456 ⟩ Used in section 430.
- ⟨ **ZContainer** :: *getType* code 406 ⟩ Used in section 405.
- ⟨ **ZContainer** class declaration 405 ⟩ Cited in sections 1 and 409. Used in section 394.
- ⟨ **cond\_traits** method codes 39 ⟩ Used in section 36.
- ⟨ **cond\_traits** template class declaration 20 ⟩ Used in section 5.
- ⟨ **condition\_counter** :: *decrease* code 25 ⟩ Used in section 21.
- ⟨ **condition\_counter** :: *increase* code 24 ⟩ Used in section 21.
- ⟨ **condition\_counter** :: *waitForChange* code 26 ⟩ Used in section 21.
- ⟨ **condition\_counter** constructor code 22 ⟩ Used in section 21.
- ⟨ **condition\_counter** destructor code 23 ⟩ Used in section 21.



< **condition\_counter** template class declaration 21 > Used in section 5.  
 < **detach\_thread\_group::insert** code 29 > Used in section 28.  
 < **detach\_thread\_group::run** code 31 > Used in section 28.  
 < **detach\_thread\_group** destructor code 30 > Used in section 28.  
 < **detach\_thread\_group** template class declaration 28 > Used in section 5.  
 < **detach\_thread** template class declaration 27 > Used in section 5.  
 < **ltseq** predicate 342 > Used in section 341.  
 < **ltsym** predicate 368 > Used in section 367.  
 < **mutex\_map::get** code 15 > Used in section 14.  
 < **mutex\_map::remove** code 16 > Used in section 14.  
 < **mutex\_map** template class declaration 14 > Used in section 5.  
 < **mutex\_traits** method codes 38 > Used in section 36.  
 < **mutex\_traits** template class declaration 13 > Used in section 5.  
 < *posix\_detach\_thread\_function* code 42 > Used in section 36.  
 < *posix\_thread\_function* code 41 > Used in section 36.  
 < **symiterator::operator++** code 147 > Used in section 141.  
 < **symiterator** class declaration 138 > Used in section 134.  
 < **symiterator** constructor code 145 > Used in section 141.  
 < **symiterator** destructor code 146 > Used in section 141.  
 < **synchro::lock** code 18 > Used in section 17.  
 < **synchro::unlock** code 19 > Used in section 17.  
 < **synchro** template class declaration 17 > Used in section 5.  
 < **thread\_group::run\_portion** code 10 > Used in section 8.  
 < **thread\_group::run** code 11 > Used in section 8.  
 < **thread\_group** destructor code 9 > Used in section 8.  
 < **thread\_group** template class declaration 8 > Used in section 5.  
 < **thread\_traits** method codes 37 > Used in section 36.  
 < **thread\_traits** template class declaration 12 > Used in section 5.  
 < **thread** template class declaration 7 > Used in section 5.

## Tensor Library

	Section	Page
Library overview .....	<a href="#">1</a>	2
Utilities .....	<a href="#">4</a>	6
Simple threads .....	<a href="#">5</a>	7
Exception .....	<a href="#">45</a>	23
Integer sequence .....	<a href="#">49</a>	24
Matrix interface .....	<a href="#">80</a>	34
Kronecker product .....	<a href="#">93</a>	39
Combinatorics .....	<a href="#">133</a>	55
Symmetry .....	<a href="#">134</a>	55
Equivalences .....	<a href="#">152</a>	62
Permutations .....	<a href="#">192</a>	77
Tensors .....	<a href="#">211</a>	84
Tensor concept .....	<a href="#">212</a>	84
Full symmetry tensor .....	<a href="#">230</a>	93
General symmetry tensor .....	<a href="#">255</a>	101
Row-wise full symmetry tensor .....	<a href="#">289</a>	115
Even more general symmetry tensor .....	<a href="#">311</a>	122
Sparse tensor .....	<a href="#">341</a>	138
The Faa Di Bruno formula .....	<a href="#">366</a>	147
Tensor containers .....	<a href="#">367</a>	147
Stack of containers .....	<a href="#">394</a>	159
Refined stack of containers .....	<a href="#">459</a>	192
Multiplying tensor columns .....	<a href="#">470</a>	197
Multiplying stacked tensor columns .....	<a href="#">477</a>	200
Miscellany .....	<a href="#">490</a>	205
Tensor polynomial evaluation .....	<a href="#">491</a>	205
Moments of normal distribution .....	<a href="#">518</a>	219
Tensor library static data .....	<a href="#">529</a>	223
Index .....	<a href="#">538</a>	226